Huawei

Huawei RH5885 V3 (Intel Xeon E7-4850 v4)

**SPECfp\_rate2006 = Not Run**

**SPECfp\_rate_base2006 = 1170**

**CPU2006 license:** 3175

**Test sponsor:** Huawei

**Test date:** Aug-2016

**Hardware Availability:** Jun-2016

**Tested by:** Huawei

**Software Availability:** Oct-2015

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<tr>
<th>Hardware</th>
<th>Software</th>
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<tbody>
<tr>
<td><strong>CPU Name:</strong> Intel Xeon E7-4850 v4</td>
<td><strong>Operating System:</strong> Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.el7.x86_64</td>
</tr>
<tr>
<td><strong>CPU Characteristics:</strong> Intel Turbo Boost Technology up to 2.80 GHz</td>
<td><strong>Compiler:</strong> C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td><strong>CPU MHz:</strong> 2100</td>
<td><strong>Auto Parallel:</strong> No</td>
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<tr>
<td><strong>FPU:</strong> Integrated</td>
<td><strong>File System:</strong> xfs</td>
</tr>
<tr>
<td><strong>CPU(s) enabled:</strong> 64 cores, 4 chips, 16 cores/chip, 2 threads/core</td>
<td></td>
</tr>
<tr>
<td><strong>CPU(s) orderable:</strong> 2,4 chips</td>
<td></td>
</tr>
<tr>
<td><strong>Primary Cache:</strong> 32 KB I + 32 KB D on chip per core</td>
<td></td>
</tr>
<tr>
<td><strong>Secondary Cache:</strong> 256 KB I+D on chip per core</td>
<td></td>
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</tbody>
</table>

Continued on next page
Huawei RH5885 V3 (Intel Xeon E7-4850 v4)

\[ \text{SPECfp} = \text{Not Run} \]
\[ \text{SPECfp} \text{ rate}_\text{base} = 1170 \]

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Aug-2016
Hardware Availability: Jun-2016
Software Availability: Oct-2015

L3 Cache: 40 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
Disk Subsystem: 2 x 600 GB SAS, 10K RPM
Other Hardware: None
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

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<tr>
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<td>990</td>
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<td>997</td>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with:
  cpupower -c all frequency-set -g performance
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**CPU2006 license:** 3175

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**Hardware Availability:** Jun-2016

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**Platform Notes**

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set C-State to C0/C1
Sysinfo program /home/spec2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

model name: Intel(R) Xeon(R) CPU E7-4850 v4 @ 2.10GHz
4 "physical id"s (chips)
128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 16
siblings: 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size: 40960 KB

From /proc/meminfo

MemTotal: 263844948 KB
HugePages_Total: 0
Hugepagesize: 2048 KB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

uname -a:
Linux RH5885v3 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 17 03:58

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SPEC CFP2006 Result

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Platform Notes (Continued)

SPEC is set to: /home/spec2006
Filesystem            Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   838G   63G  776G   8% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISV767 07/27/2016
Memory:
16x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1333 MHz
32x NO DIMM NO DIMM

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have two lines reading as:
16x Hynix HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1333 MHz
32x NO DIMM NO DIMM

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/spec2006/libs/32:/home/spec2006/libs/64:/home/spec2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort  -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort  -m64
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SPECfp_rate_base2006 = 1170

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.xml
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For other inquiries, please contact webmaster@spec.org.

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