Lenovo Group Limited

Lenovo System x3950 X6
(Intel Xeon E7-8860 v4, 2.20 GHz)

SPECfp®2006 = 121
SPECfp_base2006 = 113

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited
Test date: Aug-2016
Hardware Availability: Jun-2016
Software Availability: Dec-2015

Hardware

CPU Name: Intel Xeon E7-8860 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 144 cores, 8 chips, 18 cores/chip
CPU(s) orderable: 4,8 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Kernel 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds Peak</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>10.9</td>
<td>1250</td>
<td>10.9</td>
<td>1250</td>
<td>10.8</td>
<td>1260</td>
<td>10.9</td>
<td>1250</td>
<td>10.8</td>
<td>1260</td>
<td>10.8</td>
<td>1260</td>
</tr>
<tr>
<td>416.gamess</td>
<td>575</td>
<td>34.0</td>
<td>574</td>
<td>34.1</td>
<td>567</td>
<td>34.0</td>
<td>471</td>
<td>41.6</td>
<td>470</td>
<td>41.7</td>
<td>470</td>
<td>41.6</td>
</tr>
<tr>
<td>433.milc</td>
<td>145</td>
<td>63.2</td>
<td>145</td>
<td>63.2</td>
<td>146</td>
<td>63.1</td>
<td>145</td>
<td>63.2</td>
<td>145</td>
<td>63.2</td>
<td>146</td>
<td>63.1</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>65.5</td>
<td>139</td>
<td>65.2</td>
<td>140</td>
<td>65.3</td>
<td>139</td>
<td>65.5</td>
<td>139</td>
<td>65.2</td>
<td>140</td>
<td>65.3</td>
<td>139</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>184</td>
<td>38.8</td>
<td>185</td>
<td>38.7</td>
<td>183</td>
<td>38.9</td>
<td>184</td>
<td>38.8</td>
<td>185</td>
<td>38.7</td>
<td>183</td>
<td>38.9</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>11.8</td>
<td>1010</td>
<td>11.7</td>
<td>1020</td>
<td>12.0</td>
<td>994</td>
<td>11.8</td>
<td>1010</td>
<td>11.7</td>
<td>1020</td>
<td>12.0</td>
<td>994</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>41.8</td>
<td>225</td>
<td>41.6</td>
<td>226</td>
<td>41.3</td>
<td>228</td>
<td>41.8</td>
<td>225</td>
<td>41.6</td>
<td>226</td>
<td>41.3</td>
<td>228</td>
</tr>
<tr>
<td>444.namd</td>
<td>456</td>
<td>17.6</td>
<td>285</td>
<td>28.1</td>
<td>285</td>
<td>28.1</td>
<td>276</td>
<td>29.0</td>
<td>277</td>
<td>29.0</td>
<td>277</td>
<td>29.0</td>
</tr>
<tr>
<td>447.dealII</td>
<td>191</td>
<td>59.9</td>
<td>193</td>
<td>59.1</td>
<td>193</td>
<td>59.1</td>
<td>191</td>
<td>59.9</td>
<td>193</td>
<td>59.1</td>
<td>193</td>
<td>59.1</td>
</tr>
<tr>
<td>450.soplex</td>
<td>190</td>
<td>44.0</td>
<td>189</td>
<td>44.1</td>
<td>189</td>
<td>44.1</td>
<td>190</td>
<td>44.0</td>
<td>189</td>
<td>44.1</td>
<td>189</td>
<td>44.1</td>
</tr>
<tr>
<td>453.povray</td>
<td>93.4</td>
<td>57.0</td>
<td>94.0</td>
<td>56.6</td>
<td>93.1</td>
<td>57.1</td>
<td>83.7</td>
<td>63.5</td>
<td>82.8</td>
<td>64.3</td>
<td>83.0</td>
<td>64.1</td>
</tr>
<tr>
<td>454.calculix</td>
<td>168</td>
<td>49.1</td>
<td>168</td>
<td>49.0</td>
<td>168</td>
<td>49.1</td>
<td>145</td>
<td>57.1</td>
<td>145</td>
<td>57.0</td>
<td>148</td>
<td>55.7</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>77.3</td>
<td>137</td>
<td>82.2</td>
<td>129</td>
<td>83.0</td>
<td>128</td>
<td>70.1</td>
<td>151</td>
<td>69.1</td>
<td>153</td>
<td>73.4</td>
<td>145</td>
</tr>
<tr>
<td>465.tonto</td>
<td>299</td>
<td>32.9</td>
<td>289</td>
<td>34.0</td>
<td>293</td>
<td>33.6</td>
<td>183</td>
<td>53.7</td>
<td>184</td>
<td>53.4</td>
<td>187</td>
<td>52.7</td>
</tr>
<tr>
<td>470.lbm</td>
<td>3.35</td>
<td>4100</td>
<td>3.06</td>
<td>4490</td>
<td>3.20</td>
<td>4300</td>
<td>3.35</td>
<td>4100</td>
<td>3.06</td>
<td>4490</td>
<td>3.20</td>
<td>4300</td>
</tr>
<tr>
<td>481.wrf</td>
<td>106</td>
<td>105</td>
<td>107</td>
<td>105</td>
<td>106</td>
<td>105</td>
<td>106</td>
<td>105</td>
<td>107</td>
<td>105</td>
<td>106</td>
<td>105</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>333</td>
<td>58.6</td>
<td>328</td>
<td>59.5</td>
<td>330</td>
<td>59.1</td>
<td>333</td>
<td>58.6</td>
<td>328</td>
<td>59.5</td>
<td>330</td>
<td>59.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

**BIOS Configuration:**

Operating Mode set to "Maximum Performance"

Hyper-Threading set to Disable

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914

$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5ca285932ceab81e28219e1

running on X3950-01-SLES12SP1 Fri Aug 19 19:59:57 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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Platform Notes (Continued)

http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8860 v4 @ 2.20GHz
8 "physical id"s (chips)
144 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 18
siblings : 18
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 2: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 3: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 4: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 5: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 6: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 7: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB

From /proc/meminfo
MemTotal: 1058555020 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux X3950-01-SLES12SP1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC
2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Aug 19 14:14

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 512G 7.0G 505G 2% /home

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Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[A9E135CUS-3.10]- 06/16/2016
Memory:
128x NO DIMM Unknown
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "144"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort  -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort  -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
463.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -03 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -03 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -03 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -03 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Lenovo Group Limited

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CPU2006 license: 9017
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Lenovo Group Limited

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CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
        -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
        -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
        -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
              -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):
   -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml

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For other inquiries, please contact webmaster@spec.org.

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