# SPEC® CINT2006 Result

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## ACTION S.A.

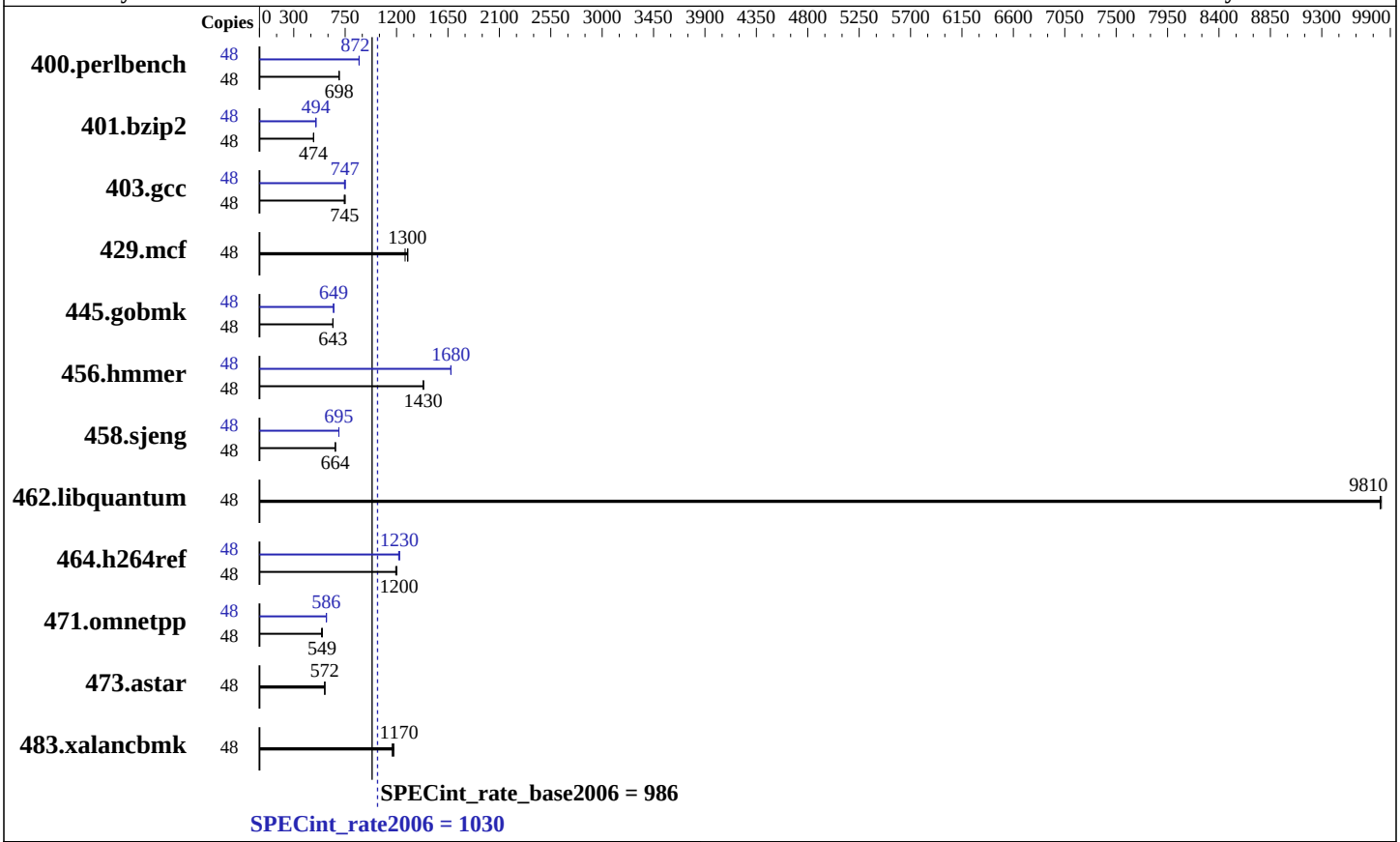
ACTINA SOLAR 210 X6 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECint®_rate2006 = 1030

SPECint_rate_base2006 = 986

**CPU2006 license:** 9008
**Test sponsor:** ACTION S.A.
**Tested by:** ACTION S.A.

**Test date:** Sep-2016
**Hardware Availability:** Mar-2016
**Software Availability:** Mar-2016




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2650 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.90 GHz |
| CPU MHz: | 2200 |
| FPU: | Integrated |
| CPU(s) enabled: | 24 cores, 2 chips, 12 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 30 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R) |
| Disk Subsystem: | 1 x 240 GB SATA II SSD |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.2 (Maipo)<br>3.10.0-327.18.2.el7.x86_64 |
| Compiler: | C/C++: Version 16.0.3.210 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 48 | **672** | **698** | 672 | 698 | 673 | 696 | 48 | 537 | 873 | 538 | 872 | **538** | **872** |
| 401.bzip2 | 48 | 978 | 473 | **978** | **474** | 976 | 475 | 48 | 939 | 493 | 937 | 494 | **937** | **494** |
| 403.gcc | 48 | **519** | **745** | 520 | 743 | 515 | 751 | 48 | **517** | **747** | 514 | 752 | 519 | 744 |
| 429.mcf | 48 | **338** | **1300** | 343 | 1270 | 337 | 1300 | 48 | **338** | **1300** | 343 | 1270 | 337 | 1300 |
| 445.gobmk | 48 | 782 | 644 | 783 | 643 | **783** | **643** | 48 | **776** | **649** | 775 | 649 | 776 | 649 |
| 456.hmmer | 48 | 312 | 1430 | 312 | 1440 | **312** | **1430** | 48 | **267** | **1680** | 267 | 1680 | 267 | 1680 |
| 458.sjeng | 48 | **874** | **664** | 874 | 664 | 874 | 664 | 48 | **835** | **695** | 835 | 695 | 836 | 695 |
| 462.libquantum | 48 | **101** | **9810** | 101 | 9820 | 101 | 9810 | 48 | **101** | **9810** | 101 | 9820 | 101 | 9810 |
| 464.h264ref | 48 | **886** | **1200** | 890 | 1190 | 883 | 1200 | 48 | 866 | 1230 | 872 | 1220 | **867** | **1230** |
| 471.omnetpp | 48 | 545 | 550 | **546** | **549** | 552 | 543 | 48 | **512** | **586** | 512 | 586 | 513 | 585 |
| 473.astar | 48 | 590 | 571 | **589** | **572** | 589 | 572 | 48 | 590 | 571 | **589** | **572** | 589 | 572 |
| 483.xalancbmk | 48 | 285 | 1160 | 282 | 1180 | **284** | **1170** | 48 | 285 | 1160 | 282 | 1180 | **284** | **1170** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
Bios Settings:
Intel(R) Hyper-Threading Tech = Enabled
Power & Performance = Performance
Enforce POR = Disabled
Memory Operating Speed Selection = Auto
Cluster-on-Die = Enabled
Set FAN Profile = Performance
Fan PWM Offset = 0

Sysinfo program /cpu2006.1.2/config/sysinfo.rev6818
$Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
running on SUT Tue Sep 13 14:25:09 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

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## Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2650 v4 @ 2.20GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 15360 KB

From /proc/meminfo
   MemTotal:       263857684 kB
   HugePages_Total:       1
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
   os-release.rpmsave:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.0 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.0"
      PRETTY_NAME="Red Hat Enterprise Linux"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
   Linux SUT 3.10.0-327.18.2.el7.x86_64 #2 SMP Wed Jun 1 17:37:13 CEST 2016
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 13 14:20

SPEC is set to: /cpu2006.1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext4  212G  118G   84G  59% /
```

Continued on next page

## Platform Notes (Continued)

```
 Additional information from dmidecode:
   BIOS Intel Corporation SE5C610.86B.11.01.0136.062220161656 06/22/2016
   Memory:
    16x   16 GB
    16x Hynix HMA42GR7AFR4N-UH 16 GB 2400 MHz 2 rank
    8x NO DIMM NO DIMM

 (End of data from sysinfo program)
 dmidecode does not properly detect memory modules
 16 modules of 16 GB were used to run the test (256 GB total)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64:/cpu2006.1.2/sh"

 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 Binaries compiled on a system with 2x Xeon E5-2650 v4 chips + 256 GB memory
 using RedHat EL 7.2
```

## Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin
```

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

Continued on next page

## Base Optimization Flags (Continued)

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/lib/ia32_lin`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-auto-ilp32`

Continued on next page

## Peak Optimization Flags (Continued)

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/cpu2006.1.2/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/ACTION.SA-Platform-Flags-RevB-aug-2015-For-Intel-Platform.xml

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For other inquiries, please contact webmaster@spec.org.

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