SPEC® CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

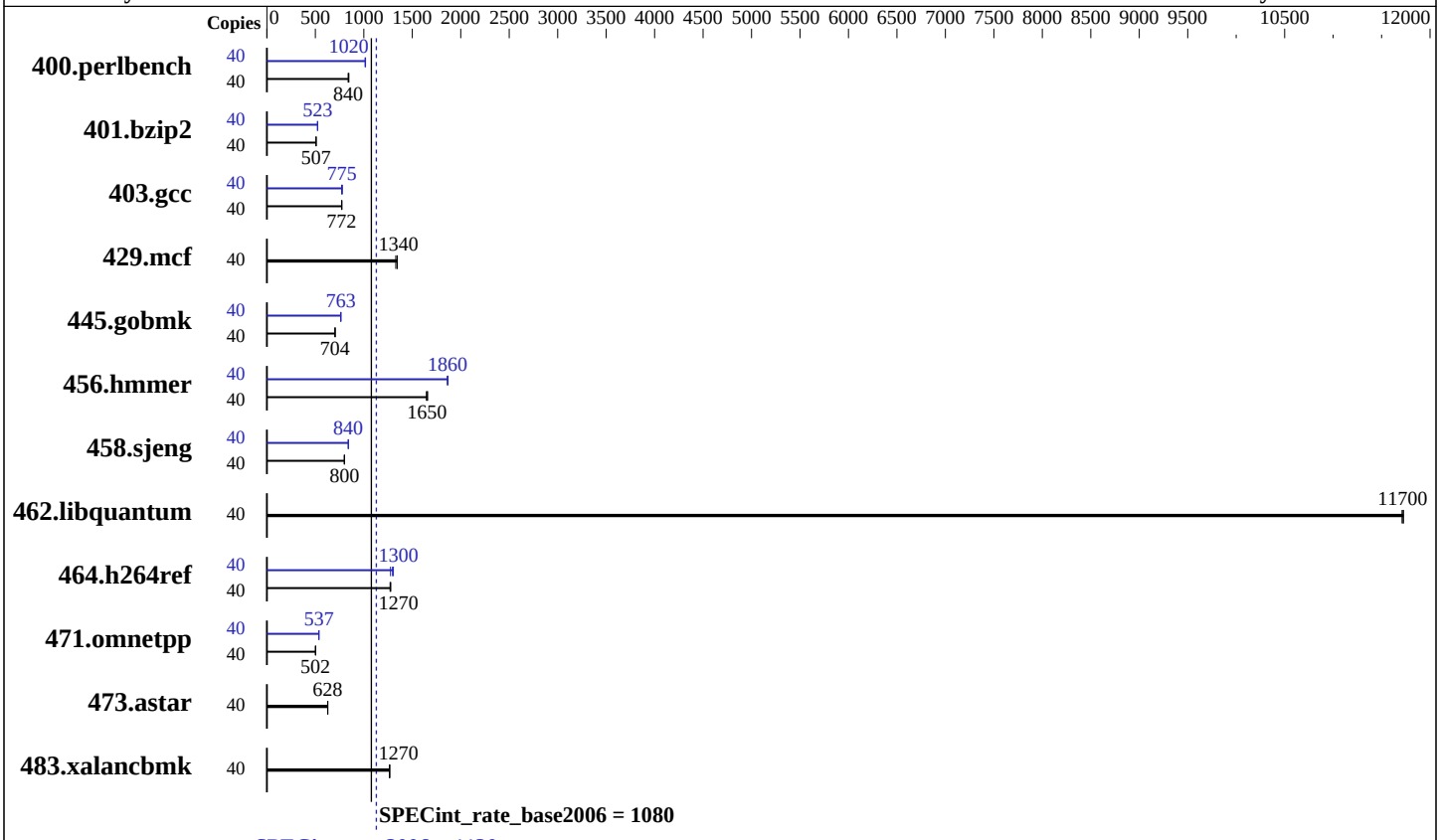
PowerEdge R730 (Intel Xeon E5-2689 v4, 3.10 GHz)

SPECint®_rate2006 = 1130

SPECint_rate_base2006 = 1080

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2016
Hardware Availability: Jun-2016
Software Availability: Mar-2016



Hardware

CPU Name:	Intel Xeon E5-2689 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	3100
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)
Disk Subsystem:	200 GB SATA SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.el7.x86_64
Compiler:	C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

SPEC CINT2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R730 (Intel Xeon E5-2689 v4, 3.10 GHz)

SPECint_rate2006 = 1130

SPECint_rate_base2006 = 1080

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2016
Hardware Availability: Jun-2016
Software Availability: Mar-2016

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	465	840	462	846	**465**	**840**	40	386	1010	**385**	**1020**	384	1020
401.bzip2	40	**761**	**507**	762	507	760	508	40	**738**	**523**	740	522	736	524
403.gcc	40	416	774	**417**	**772**	418	771	40	414	778	418	771	**415**	**775**
429.mcf	40	274	1330	271	1350	**272**	**1340**	40	274	1330	271	1350	**272**	**1340**
445.gobmk	40	597	703	596	704	**596**	**704**	40	**550**	**763**	552	760	549	765
456.hmmer	40	**226**	**1650**	225	1660	227	1650	40	201	1860	200	1870	**200**	**1860**
458.sjeng	40	605	800	606	799	**605**	**800**	40	576	841	577	839	**576**	**840**
462.libquantum	40	**70.7**	**11700**	70.7	11700	70.8	11700	40	**70.7**	**11700**	70.7	11700	70.8	11700
464.h264ref	40	696	1270	693	1280	**695**	**1270**	40	694	1280	678	1310	**683**	**1300**
471.omnetpp	40	**498**	**502**	497	503	499	501	40	467	535	**466**	**537**	465	537
473.astar	40	**447**	**628**	448	627	447	629	40	**447**	**628**	448	627	447	629
483.xalancbmk	40	219	1260	218	1270	**218**	**1270**	40	219	1260	218	1270	**218**	**1270**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS settings:
Snoop Mode set to Cluster on Die
Virtualization Technology disabled
System Profile set to custom
CPU Performance set to Hardware P States
C States set to Autonomous
C1E disabled
Energy Efficient Turbo disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Balanced Performance
Memory Patrol Scrub disabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Fri Jul 15 03:56:37 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2689 v4 @ 3.10GHz
          2 "physical id"s (chips)
          40 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 10
          siblings  : 20
          physical 0: cores 0 1 2 3 4 8 9 10 11 12
          physical 1: cores 0 1 2 3 4 8 9 10 11 12
       cache size : 12800 KB

    From /proc/meminfo
       MemTotal:       264040056 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       os-release:
          NAME="Red Hat Enterprise Linux Server"
          VERSION="7.2 (Maipo)"
          ID="rhel"
          ID_LIKE="fedora"
          VERSION_ID="7.2"
          PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
          ANSI_COLOR="0;31"
          CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
       redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

    uname -a:
       Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
       EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Jul 15 03:55

    SPEC is set to: /root/cpu2006-1.2
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/sda2      xfs   179G  8.1G  171G   5% /
    Additional information from dmidecode:

       Warning: Use caution when you interpret this section. The 'dmidecode' program
       reads system data which is "intended to allow hardware to be accurately
       determined", but the intent may not be met, as there are frequent changes to
       hardware, firmware, and the "DMTF SMBIOS" standard.

      BIOS Dell Inc. 2.1.7 06/16/2016
      Memory:
```

Continued on next page

Platform Notes (Continued)

```
      15x 00AD063200AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz
      1x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz
      8x Not Specified Not Specified

   (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
The Dell PowerEdge R730 and the PowerEdge R730xd models are electronically equivalent.
The results have been measured on a Dell PowerEdge R730xd model.
 Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB
 memory using RedHat EL 7.2 glibc 2.17
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1>       /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

Base Portability Flags

```
    400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
      401.bzip2: -D_FILE_OFFSET_BITS=64
        403.gcc: -D_FILE_OFFSET_BITS=64
        429.mcf: -D_FILE_OFFSET_BITS=64
      445.gobmk: -D_FILE_OFFSET_BITS=64
      456.hmmer: -D_FILE_OFFSET_BITS=64
      458.sjeng: -D_FILE_OFFSET_BITS=64
  462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
    464.h264ref: -D_FILE_OFFSET_BITS=64
    471.omnetpp: -D_FILE_OFFSET_BITS=64
      473.astar: -D_FILE_OFFSET_BITS=64
  483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
403.gcc: `-D_FILE_OFFSET_BITS=64`
429.mcf: `-D_FILE_OFFSET_BITS=64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
458.sjeng: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
464.h264ref: `-D_FILE_OFFSET_BITS=64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-D_FILE_OFFSET_BITS=64`

Continued on next page

Peak Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Fri Nov 11 16:32:11 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 November 2016.