Huawei

Huawei XH628 V3 (Intel Xeon E5-2643 v4)

**SPECint®2006 =** 71.6

**SPECint_base2006 =** 68.0

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

CPU Name: Intel Xeon E5-2643 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 3400
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC4-2400T-R)
Disk Subsystem: 1 x 600 GB SAS, 10K RPM
Other Hardware: None

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
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Test date: Oct-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Base</td>
<td></td>
<td></td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>226</td>
<td>43.3</td>
<td>226</td>
<td>43.2</td>
<td>228</td>
<td>42.9</td>
<td>206</td>
<td>47.4</td>
<td>206</td>
<td>47.4</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>362</td>
<td>26.6</td>
<td>361</td>
<td>26.7</td>
<td>362</td>
<td>26.7</td>
<td>355</td>
<td>27.2</td>
<td>355</td>
<td>27.2</td>
</tr>
<tr>
<td>403.gcc</td>
<td>206</td>
<td>39.1</td>
<td>205</td>
<td>39.2</td>
<td>205</td>
<td>39.2</td>
<td>206</td>
<td>39.1</td>
<td>205</td>
<td>39.2</td>
</tr>
<tr>
<td>415.mcf</td>
<td>130</td>
<td>70.0</td>
<td>132</td>
<td>69.2</td>
<td>131</td>
<td>69.5</td>
<td>130</td>
<td>70.4</td>
<td>129</td>
<td>70.8</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>332</td>
<td>31.6</td>
<td>332</td>
<td>31.6</td>
<td>332</td>
<td>31.6</td>
<td>332</td>
<td>31.6</td>
<td>332</td>
<td>31.6</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>102</td>
<td>91.1</td>
<td>102</td>
<td>91.1</td>
<td>102</td>
<td>91.3</td>
<td>102</td>
<td>91.1</td>
<td>102</td>
<td>91.1</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>333</td>
<td>36.4</td>
<td>333</td>
<td>36.4</td>
<td>333</td>
<td>36.4</td>
<td>329</td>
<td>36.7</td>
<td>329</td>
<td>36.7</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4.16</td>
<td>4980</td>
<td>4.06</td>
<td>5110</td>
<td>4.18</td>
<td>4960</td>
<td>4.16</td>
<td>4980</td>
<td>4.06</td>
<td>5110</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>347</td>
<td>63.8</td>
<td>348</td>
<td>63.6</td>
<td>348</td>
<td>63.6</td>
<td>347</td>
<td>63.8</td>
<td>348</td>
<td>63.6</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>213</td>
<td>29.3</td>
<td>199</td>
<td>31.4</td>
<td>201</td>
<td>31.0</td>
<td>139</td>
<td>44.9</td>
<td>139</td>
<td>44.8</td>
</tr>
<tr>
<td>473.astar</td>
<td>181</td>
<td>38.7</td>
<td>181</td>
<td>38.7</td>
<td>181</td>
<td>38.9</td>
<td>181</td>
<td>38.9</td>
<td>181</td>
<td>38.9</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>88.5</td>
<td>78.0</td>
<td>88.9</td>
<td>77.6</td>
<td>88.3</td>
<td>78.2</td>
<td>78.5</td>
<td>87.9</td>
<td>78.7</td>
<td>87.7</td>
</tr>
</tbody>
</table>

Submit Notes
The config file option 'submit' was used.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-1g2g Wed Oct 26 21:52:22 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6

Continued on next page
SPEC CINT2006 Result

Huawei
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Platform Notes (Continued)

siblings : 6
physical 0: cores 0 1 2 3 6 7
physical 1: cores 0 1 2 3 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal: 264078020 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux linux-1g2g 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 26 11:42

SPEC is set to: /spec16

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 ext4 542G 15G 526G 3% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016
Memory:
8x Micron 36ASF4G72PZ-2G3A1 32 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)
SPEC CINT2006 Result

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General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.
The results have been measured on a Huawei XH620 V3 model.

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
  -Wl,-z,muldefs -L/sh -lsmartheap64
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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

Continued on next page
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**Software Availability:** Mar-2016

## Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf:
- `-xCORE-AVX2`
- `-ipo`
- `-O3`
- `-no-prec-div`
- `-parallel`
- `-opt-prefetch`
- `-auto-p32`

445.gobmk: basepeak = yes
456.hmmer: basepeak = yes

458.sjeng:
- `-xCORE-AVX2(pass 2)`
- `-prof-gen:threadsafe(pass 1)`
- `-ipo(pass 2)`
- `-O3(pass 2)`
- `-no-prec-div(pass 2)`
- `-par-num-threads=1(pass 1)`
- `-prof-use(pass 2)`
- `-unroll4`

462.libquantum: basepeak = yes
464.h264ref: basepeak = yes

### C++ benchmarks:

471.omnetpp:
- `-xCORE-AVX2(pass 2)`
- `-prof-gen:threadsafe(pass 1)`
- `-ipo(pass 2)`
- `-O3(pass 2)`
- `-no-prec-div(pass 2)`
- `-par-num-threads=1(pass 1)`
- `-prof-use(pass 2)`
- `-opt-ra-region-strategy=block`
- `-ansi-alias`
- `-Wl,-z,muldefs -L/sh -lsmartheap`

473.astar:
- `-xCORE-AVX2`
- `-ipo`
- `-O3`
- `-no-prec-div`
- `-opt-prefetch`
- `-auto-p32`
- `-Wl,-z,muldefs -L/sh -lsmartheap64`

483.xalancbmk:
- `-xCORE-AVX2`
- `-ipo`
- `-O3`
- `-no-prec-div`
- `-opt-prefetch`
- `-ansi-alias`
- `-Wl,-z,muldefs -L/sh -lsmartheap`

## Peak Other Flags

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml)
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For other inquiries, please contact webmaster@spec.org.

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