**Huawei**

**Huawei 5288 V3 (Intel Xeon E5-2620 v4)**

**SPECint®2006 =** 60.0  
**SPECint_base2006 =** 57.3

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<td>Test date: Nov-2016</td>
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<td></td>
<td>Hardware Availability: Mar-2016</td>
</tr>
<tr>
<td></td>
<td>Software Availability: Dec-2015</td>
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</table>

**Software**

- **Operating System:** SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- **Auto Parallel:** Yes
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32/64-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V10.2

**Hardware**

- **CPU Name:** Intel Xeon E5-2620 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.00 GHz
- **CPU MHz:** 2100
- **FPU:** Integrated
- **CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip
- **CPU(s) orderable:** 1.2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 20 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
- **Disk Subsystem:** 1 x 1000 GB SATA, 7200 RPM
- **Other Hardware:** None

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**Results Table**

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<tr>
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<td></td>
<td></td>
<td></td>
<td>Base</td>
<td></td>
<td>Peak</td>
<td></td>
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<tr>
<td>400.perlbench</td>
<td>276</td>
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<td>35.3</td>
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<tr>
<td>401.bzip2</td>
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<td>22.1</td>
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<td>22.1</td>
<td>429</td>
<td>22.5</td>
<td>429</td>
<td>22.5</td>
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<tr>
<td>403.gcc</td>
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<td>33.6</td>
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<td>33.7</td>
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<td>33.7</td>
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<td>429.mcf</td>
<td>147</td>
<td>62.1</td>
<td>146</td>
<td>61.6</td>
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<td>62.4</td>
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<td>61.0</td>
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<td>62.6</td>
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<tr>
<td>456.hmmer</td>
<td>126</td>
<td>74.0</td>
<td>126</td>
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<td>458.sjeng</td>
<td>403</td>
<td>30.0</td>
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<td>30.0</td>
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<td>30.3</td>
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<tr>
<td>462.libquantum</td>
<td>4.61</td>
<td>4500</td>
<td>4.69</td>
<td>4420</td>
<td>4.61</td>
<td>4500</td>
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<td>4420</td>
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<td>4420</td>
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<tr>
<td>464.h264ref</td>
<td>441</td>
<td>50.2</td>
<td>445</td>
<td>49.7</td>
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<td>49.9</td>
<td>441</td>
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<td>445</td>
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<tr>
<td>471.omnetpp</td>
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<td>29.1</td>
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<td>39.8</td>
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<td>473.astar</td>
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<td>32.2</td>
<td>219</td>
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<td>219</td>
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<tr>
<td>483.xalancbmk</td>
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<td>65.7</td>
<td>106</td>
<td>64.9</td>
<td>107</td>
<td>64.4</td>
<td>94.5</td>
<td>73.0</td>
<td>94.9</td>
<td>72.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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**Submit Notes**

The config file option 'submit' was used.

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**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**Platform Notes**

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
- 2 "physical id"s (chips)
- 16 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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Platform Notes (Continued)

can be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID=sles
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 7 10:25
SPEC is set to: /spec16

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 884G 14G 870G 2% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016
Memory:
16x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc  -m64
C++ benchmarks:
  icpc  -m64

Base Portability Flags

400.perlbench:  -DSPEC_CPU_LP64  -DSPEC_CPU_LINUX_X64
401.bzip2:  -DSPEC_CPU_LP64
403.gcc:  -DSPEC_CPU_LP64
429.mcf:  -DSPEC_CPU_LP64
445.gobmk:  -DSPEC_CPU_LP64
456.hmmer:  -DSPEC_CPU_LP64
458.sjeng:  -DSPEC_CPU_LP64
462.libquantum:  -DSPEC_CPU_LP64  -DSPEC_CPU_LINUX
464.h264ref:  -DSPEC_CPU_LP64
471.omnetpp:  -DSPEC_CPU_LP64
473.astar:  -DSPEC_CPU_LP64
483.xalancbmk:  -DSPEC_CPU_LP64  -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2  -ipo  -O3  -no-prec-div  -parallel  -opt-prefetch  -auto-p32

Continued on next page
Huawei 5288 V3(Intel Xeon E5-2620 v4) SPECint2006 = 60.0
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Base Optimization Flags (Continued)
C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-W1,-z,muldefs -L/sh -lsmartheap64

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m64

Peak Portability Flags
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags
C benchmarks:

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Peak Optimization Flags (Continued)

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</tr>
<tr>
<td>401.bzip2</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias</td>
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<td>403.gcc</td>
<td>basepeak = yes</td>
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<td>429.mcf</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32</td>
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</tr>
<tr>
<td>473.astar</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64</td>
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<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap</td>
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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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