Huawei 5288 V3 (Intel Xeon E5-2640 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPECint®2006 = 68.2
SPECint_base2006 = 64.8

CPU Name: Intel Xeon E5-2640 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
CPU MHZ: 2400
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
CPU(s) orderable: 1.2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM
Other Hardware: None

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
Huawei

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Test date: Nov-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tbody>
<tr>
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<td>40.0</td>
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<td>39.8</td>
<td>244</td>
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<td>43.7</td>
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<td>401.bzip2</td>
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<td>24.7</td>
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<td>462.libquantum</td>
<td>3.86</td>
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<td>473.astar</td>
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<tr>
<td>483.xalancbmk</td>
<td>93.9</td>
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<td>71.7</td>
<td>95.1</td>
<td>72.5</td>
<td>83.8</td>
<td>82.4</td>
<td>83.7</td>
<td>82.4</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-j81m Tue Nov 15 11:20:45 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : "Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz"
  2 "physical id"s (chips)
  20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution)
Continued on next page
Huawei
Huawei 5288 V3(Intel Xeon E5-2640 v4)

<table>
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CPU2006 license: 3175
Test sponsor: Huawei
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Platform Notes (Continued)

caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal: 264077184 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux linux-j81m 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Nov 15 01:26

SPEC is set to: /spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 884G 18G 865G 3% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016
Memory:
16x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz

Continued on next page
SPEC CINT2006 Result

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Platform Notes (Continued)
(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Base Portability Flags
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags
C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

Continued on next page
SPEC CINT2006 Result

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Base Optimization Flags (Continued)

C++ benchmarks:
-xcORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc  -m64
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
473.astar: icpc  -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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Huawei

Huawei 5288 V3(Intel Xeon E5-2640 v4)  

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**Peak Optimization Flags (Continued)**

400.perlbench:  
-xCORE-AVX2(pass 2)  
-prof-gen:threadsafepass  
-ipo(pass 2)  
-03(pass 2)  
-no-prec-div(pass 2)  
-par-num-threads=1(pass 1)  
-prof-use(pass 2)  
-opt-prefetch  
-ansi-alias

401.bzip2:  
-xCORE-AVX2(pass 2)  
-prof-gen:threadsafepass  
-ipo(pass 2)  
-03(pass 2)  
-no-prec-div  
-par-num-threads=1(pass 1)  
-prof-use(pass 2)  
-auto-ilp32  
-opt-prefetch  
-ansi-alias

403.gcc:  
-basepeak = yes

429.mcf:  
-basepeak = yes

445.gobmk:  
-basepeak = yes

456.hmmer:  
-basepeak = yes

458.sjeng:  
-xCORE-AVX2(pass 2)  
-prof-gen:threadsafepass  
-ipo(pass 2)  
-03(pass 2)  
-no-prec-div(pass 2)  
-par-num-threads=1(pass 1)  
-prof-use(pass 2)  
-unroll4

462.libquantum:  
-basepeak = yes

464.h264ref:  
-basepeak = yes

**C++ benchmarks:**

471.omnetpp:  
-xCORE-AVX2(pass 2)  
-prof-gen:threadsafepass  
-ipo(pass 2)  
-03(pass 2)  
-no-prec-div(pass 2)  
-par-num-threads=1(pass 1)  
-prof-use(pass 2)  
-opt-ra-region-strategy=block  
-ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar:  
-xCORE-AVX2  
-ipo -03 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk:  
-xCORE-AVX2  
-ipo -03 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

**Peak Other Flags**

**C benchmarks:**

403.gcc:  
-Dalloca=_alloca
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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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