Huawei 5288 V3 (Intel Xeon E5-2650 v4)  

<table>
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<tr>
<th>SPECint®2006</th>
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<tr>
<td>SPECint_base2006</td>
<td>57.7</td>
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</table>

**CINT2006 Result**

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei

<table>
<thead>
<tr>
<th>Test date:</th>
<th>Nov-2016</th>
</tr>
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<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2016</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2015</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon E5-2650 v4  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 2.90 GHz  
- **CPU MHz:** 2200  
- **FPU:** Integrated  
- **CPU(s) enabled:** 24 cores, 2 chips, 12 cores/chip  
- **CPU(s) orderable:** 1.2 chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core  
- **L3 Cache:** 30 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 256 GB (16 x 16 GB 2Rx8 PC4-2400T-R)  
- **Disk Subsystem:** 1 x 600 GB SAS, 10000 RPM  
- **Other Hardware:** None

**Software**

- **Operating System:** SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
- **Auto Parallel:** Yes  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 32/64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Microquill SmartHeap V10.2
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<tbody>
<tr>
<td>400.perlbench</td>
<td>267</td>
<td>34.1</td>
<td>290</td>
<td>33.7</td>
<td>287</td>
<td>34.1</td>
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<td>58.3</td>
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<td>58.3</td>
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<td>456.hmmer</td>
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<td>71.3</td>
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<td>71.5</td>
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<td>131</td>
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<tr>
<td>458.sjeng</td>
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<td>29.2</td>
<td>415</td>
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<td>410</td>
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<tr>
<td>462.libquantum</td>
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<td>5690</td>
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<td>5720</td>
<td>3.62</td>
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<td>5690</td>
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<tr>
<td>464.h264ref</td>
<td>459</td>
<td>48.2</td>
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<td>48.3</td>
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<td>457</td>
<td>48.4</td>
<td>458</td>
<td>48.3</td>
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<tr>
<td>471.omnetpp</td>
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<td>35.1</td>
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<td>47.6</td>
<td>132</td>
<td>47.3</td>
<td>131</td>
<td>47.6</td>
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<tr>
<td>473.astar</td>
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<td>226</td>
<td>31.0</td>
<td>228</td>
<td>30.8</td>
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<tr>
<td>483.xalancbmk</td>
<td>108</td>
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<td>64.6</td>
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<td>64.3</td>
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<td>71.5</td>
<td>96.7</td>
<td>71.4</td>
<td>96.5</td>
<td>71.5</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1
running on linux-1g2g Wed Nov 16 00:53:58 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
Continued on next page
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Huawei

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Platform Notes (Continued)

cautions.)
cpu cores : 12
siblings : 12
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size : 30720 KB

From /proc/meminfo
MemTotal: 264076772 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release*/etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux linux-1g2g 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 15 12:30

SPEC is set to: /spec16

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 ext4 542G 112G 430G 21% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.31 08/22/2016
Memory:
16x Hynix HMA82GR7AFR8N-UH 16 GB 2 rank 2400 MHz

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Platform Notes (Continued)
(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "24"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled
runcspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation
C benchmarks:
  icc -m64
C++ benchmarks:
  icpc -m64

Base Portability Flags
400.perlbench: -DSPEC_CPU_LP64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64

Base Optimization Flags
C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -1smartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
464.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

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<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
<th>Base Peak</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>-xCORE-AVX2 -prof-gen:threadsafe -ipo -O3 -no-prec-div -par-num-threads=1 -prof-use -opt-prefetch</td>
<td>yes</td>
<td>(Continued)</td>
</tr>
<tr>
<td>bzip2</td>
<td>-xCORE-AVX2 -prof-gen:threadsafe -ipo -O3 -no-prec-div</td>
<td>yes</td>
<td></td>
</tr>
<tr>
<td>gcc</td>
<td>-Dalloca=_alloca</td>
<td>yes</td>
<td></td>
</tr>
<tr>
<td>mcf</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32</td>
<td>yes</td>
<td></td>
</tr>
<tr>
<td>gobmk</td>
<td>basepeak = yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>hmer</td>
<td>basepeak = yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>sjeng</td>
<td>-xCORE-AVX2 -prof-gen:threadsafe -ipo -O3 -no-prec-div -par-num-threads=1 -prof-use -opt-prefetch -unroll4</td>
<td>yes</td>
<td></td>
</tr>
<tr>
<td>libquantum</td>
<td>basepeak = yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>x264ref</td>
<td>basepeak = yes</td>
<td></td>
<td></td>
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</table>

**Peak Other Flags**

<table>
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<th>Benchmark</th>
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<th>Description</th>
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<td>gcc</td>
<td>-Dalloca=_alloca</td>
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The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml)

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.


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