## Lenovo Group Limited

Lenovo System x3250 M6  
(2.90 GHz, Intel Xeon E3-1260L v5)

<table>
<thead>
<tr>
<th>SPECfprate2006 = 192</th>
<th>SPECfprate_base2006 = 187</th>
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### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E3-1260L v5</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU MHZ:</td>
<td>2900</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
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</table>

### Software

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>SUSE Linux Enterprise Server 12 SP1 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
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</tbody>
</table>

### Test Details

- **CPU2006 license:** 9017  
- **Test sponsor:** Lenovo Group Limited  
- **Tested by:** Lenovo Group Limited  
- **Test date:** Nov-2016  
- **Hardware Availability:** Apr-2016  
- **Software Availability:** Mar-2016
Lenovo Group Limited

Lenovo System x3250 M6
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CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
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L3 Cache: 8 MB I+D on chip per chip
Base Pointers: 32/64-bit
Other Cache: None
Peak Pointers: 32/64-bit
Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-E)
Other Software: None
Disk Subsystem: 1 x 800 GB SATA SSD

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>482.sphinx3</td>
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<td>195</td>
<td>8</td>
<td>800</td>
<td>195</td>
<td>803</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Lenovo Group Limited

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SPECfp_rate2006 = 192
SPECfp_rate_base2006 = 187

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Nov-2016
Hardware Availability: Apr-2016
Software Availability: Mar-2016

Platform Notes

BIOS Configuration:
Operating Modes set to Maximum Performance
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1
running on x3250-02 Sun Nov 27 17:20:13 2016

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1260L v5 @ 2.90GHz
  1 "physical id"s (chips)
  8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 4
siblings    : 8
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:       16421272 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux x3250-02 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 27 08:20

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem Type Size Used Avail Use% Mounted on

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### Platform Notes (Continued)

/dev/sda3 xfs 691G 4.2G 687G 1% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[M3E107GUS-1.20]- 10/28/2016
Memory:
2x NO DIMM Unknown
2x Samsung M391A1G43EB1-CPB 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB memory using RedHat EL 7.2 glibc 2.17

### Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
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Base Portability Flags (Continued)

444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
## Lenovo Group Limited

**Lenovo System x3250 M6**  
(2.90 GHz, Intel Xeon E3-1260L v5)

**SPECfp_rate2006** = 192  
**SPECfp_rate_base2006** = 187

<table>
<thead>
<tr>
<th>CPU2006 license</th>
<th>Lenovo Group Limited</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test date</td>
<td>Nov-2016</td>
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<tr>
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<td>Apr-2016</td>
</tr>
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<td>Mar-2016</td>
</tr>
<tr>
<td>Tested by</td>
<td>Lenovo Group Limited</td>
</tr>
</tbody>
</table>

### Peak Portability Flags

- `410.bwaves`: `-DSPEC_CPU_LP64`
- `416.gamess`: `-DSPEC_CPU_LP64`
- `433.milc`: `-DSPEC_CPU_LP64`
- `434.zeusmp`: `-DSPEC_CPU_LP64`
- `435.gromacs`: `-DSPEC_CPU_LP64 -nofor_main`
- `436.cactusADM`: `-DSPEC_CPU_LP64 -nofor_main`
- `437.leslie3d`: `-DSPEC_CPU_LP64`
- `444.namd`: `-DSPEC_CPU_LP64 -nofor_main`
- `447.dealII`: `-DSPEC_CPU_LP64`
- `450.soplex`: `-D_FILE_OFFSET_BITS=64`
- `453.povray`: `-DSPEC_CPU_LP64`
- `454.calculix`: `-DSPEC_CPU_LP64 -nofor_main`
- `459.GemsFDTD`: `-DSPEC_CPU_LP64`
- `465.tonto`: `-DSPEC_CPU_LP64`
- `470.lbm`: `-DSPEC_CPU_LP64`
- `481.wrf`: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
- `482.sphinx3`: `-DSPEC_CPU_LP64`

### Peak Optimization Flags

**C benchmarks:**

- `433.milc`: `basepeak = yes`
- `470.lbm`: `basepeak = yes`
- `482.sphinx3`: `basepeak = yes`

**C++ benchmarks:**

- `444.namd`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilm32`
- `447.dealII`: `basepeak = yes`
- `450.soplex`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3`
- `453.povray`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias`

Continued on next page
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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml
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For questions about this result, please contact the tester. For other inquiries, please contact webmaster@spec.org.