Huawei RH5885H V3 (Intel Xeon E7-8880 v4)

SPECint\textsubscript{rate}_2006 = 3460
SPECint\textsubscript{rate}_base2006 = 3320

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

CPU Name: Intel Xeon E7-8880 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 88 cores, 4 chips, 22 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 55 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R, running at 1600 MHz)
Disk Subsystem: 2 x 600 GB SAS, 10K RPM
Other Hardware: None

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP1
Kernel 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 5 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
Huawei RH5885H V3 (Intel Xeon E7-8880 v4)

SPEC_rate2006 = 3460
SPEC_rate_base2006 = 3320

Test date: Dec-2016
Hardware Availability: Jun-2016
Software Availability: Dec-2015

Results Table

<table>
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<th>Benchmark</th>
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<th>Seconds Base</th>
<th>Seconds Peak</th>
<th>Seconds Base</th>
<th>Seconds Peak</th>
<th>Seconds Base</th>
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<th>Seconds Peak</th>
<th>Seconds Base</th>
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<tbody>
<tr>
<td>400.perlbench</td>
<td>176</td>
<td>655</td>
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<td>401.bzip2</td>
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<td>429.mcf</td>
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<td>471.omnetpp</td>
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<td>473.astar</td>
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<td>483.xalancbmk</td>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with:
cpupower -c all frequency-set -g performance

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100%
Set C-State to C0/C1
Sysinfo program /home/spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $ e3fbb8676b5a285932ceab81e28219e1
running on RH5885Hv3 Tue Dec 13 02:43:29 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8880 v4 @ 2.20GHz
Continued on next page
Huawei

Huawei RH5885H V3 (Intel Xeon E7-8880 v4)

SPECint_rate2006 = 3460
SPECint_rate_base2006 = 3320

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Platform Notes (Continued)

4 "physical id"s (chips)
176 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 22
siblings : 44
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

cache size : 28160 KB

From /proc/meminfo
MemTotal: 529088608 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.

os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME=cpe:/o:suse:sles:12:sp1"

uname -a:
 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Dec 12 22:47

SPEC is set to: /home/spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 xfs 750G 8.0G 742G 2% /home
Additional information from dmidecode:

Continued on next page
SPEC CINT2006 Result

Huawei
Huawei RH5885H V3 (Intel Xeon E7-8880 v4)

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CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei  
Test date: Dec-2016

Hardware Availability: Jun-2016  
Software Availability: Dec-2015

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISY102 11/07/2016  
Memory:  
64x NO DIMM NO DIMM  
32x Samsung M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/spec/libs/32:/home/spec/libs/64:/home/spec/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>/proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32  
401.bzip2: -D_FILE_OFFSET_BITS=64  
403.gcc: -D_FILE_OFFSET_BITS=64  
429.mcf: -D_FILE_OFFSET_BITS=64  
445.gobmk: -D_FILE_OFFSET_BITS=64  
456.hmmer: -D_FILE_OFFSET_BITS=64  
458.sjeng: -D_FILE_OFFSET_BITS=64  
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX  
464.h264ref: -D_FILE_OFFSET_BITS=64

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Huawei RH5885H V3 (Intel Xeon E7-8880 v4)

SPECint_rate2006 = 3460
SPECint_rate_base2006 = 3320

Base Portability Flags (Continued)

471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
   icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
   400.perlbench: icc -m64
   401.bzip2: icc -m64
   456.hmmer: icc -m64
   458.sjeng: icc -m64

C++ benchmarks:
   icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64

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## SPEC CINT2006 Result

**Huawei**

Huawei RH5885H V3 (Intel Xeon E7-8880 v4)

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**CPU2006 license:** 3175  
**Test date:** Dec-2016  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Hardware Availability:** Jun-2016  
**Software Availability:** Dec-2015

### Peak Portability Flags (Continued)

- `429.mcf`: `-D_FILE_OFFSET_BITS=64`
- `445.gobmk`: `-D_FILE_OFFSET_BITS=64`
- `456.hmmer`: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
- `458.sjeng`: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64`
- `462.libquantum`: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
- `464.h264ref`: `-D_FILE_OFFSET_BITS=64`
- `471.omnetpp`: `-D_FILE_OFFSET_BITS=64`
- `473.astar`: `-D_FILE_OFFSET_BITS=64`
- `483.xalancbmk`: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

### Peak Optimization Flags

**C benchmarks:**

- `400.perlbench`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32`
- `401.bzip2`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`
- `403.gcc`: `-xCORE-AVX2 -ipo -O3 -no-prec-div`
- `429.mcf`: `basepeak = yes`
- `445.gobmk`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias -opt-mem-layout-trans=3`
- `456.hmmer`: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`
- `458.sjeng`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto-ilp32`
- `462.libquantum`: `basepeak = yes`
- `464.h264ref`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12 -ansi-alias`

**C++ benchmarks:**

Continued on next page
Huawei

Huawei RH5885H V3 (Intel Xeon E7-8880 v4)

SPECint_rate2006 = 3460
SPECint_rate_base2006 = 3320

Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.xml

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