Lenovo Group Limited
Lenovo NeXtScale nx360 M5
(2.60 GHz, Intel Xeon E5-2697A v4)

SPECint®2006 = 73.7
SPECint_base2006 = 71.3

Hardware

CPU Name: Intel Xeon E5-2697A v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip
CPU(s) orderable: 1.2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 40 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Kernel 3.12.49-11-default
Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel: Yes
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
Lenovo Group Limited

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CPU2006 license: 9017
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Tested by: Lenovo Group Limited
Software Availability: Sep-2016

Results Table

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<thead>
<tr>
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<tr>
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<td>239</td>
<td>40.9</td>
<td>237</td>
<td>41.2</td>
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<td>206</td>
<td>47.4</td>
<td>207</td>
<td>47.3</td>
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<tr>
<td>401.bzip2</td>
<td>377</td>
<td>25.6</td>
<td>376</td>
<td>25.7</td>
<td>378</td>
<td>25.6</td>
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<td>374</td>
<td>25.8</td>
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<tr>
<td>403.gcc</td>
<td>213</td>
<td>37.9</td>
<td>212</td>
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<td>213</td>
<td>37.9</td>
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<tr>
<td>483.xalancbmk</td>
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<td>85.5</td>
<td>80.9</td>
<td>85.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The config file option 'submit' was used.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes
BIOS configuration:
Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
Sysinfo program /home/cpu2006-1.2-ic17.0/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on nx360 Thu Mar 9 17:30:41 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2697A v4 @ 2.60GHz
2 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with
Continued on next page
Lenovo Group Limited

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Platform Notes (Continued)

caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 40960 KB

From /proc/meminfo
MemTotal: 263965288 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Linux nx360 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0)
x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Mar 9 17:24

SPEC is set to: /home/cpu2006-1.2-ic17.0

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[THE126N-2.20]- 09/07/2016
Memory:
16x Samsung M393A2G40DB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)
SPEC CINT2006 Result

Lenovo Group Limited

Lenovo NeXtScale nx360 M5
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General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = */home/cpu2006-1.2-ic17.0/libs/32:/home/cpu2006-1.2-ic17.0/libs/64:/home/cpu2006-1.2-ic17.0/sh10.2"
OMP_NUM_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Base Portability Flags

C benchmarks:
  -DSPEC_CPU_LP64
  -Dspec_cpu_linux_x64

C++ benchmarks:
  -DSPEC_CPU_LP64
  -Dspec_cpu_linux

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
  -auto-p32

C++ benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
  -Wl,-z,muldefs -L/sh10.2 -lsmartheap64
Lenovo Group Limited
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Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -auto-ilp32 -qopt-prefetch

Continued on next page
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Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
        -qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
         -qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
        -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
        -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
        -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
        -no-prec-div(pass 2) -qopt-ra-region-strategy=block
        -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
         -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
               -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-D.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-D.xml
## Lenovo Group Limited

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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