



SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

NEC Corporation

SPECint®2006 = 75.4

Express5800/T110i (Intel Xeon E3-1230 v6)

SPECint_base2006 = 72.9

CPU2006 license: 9006

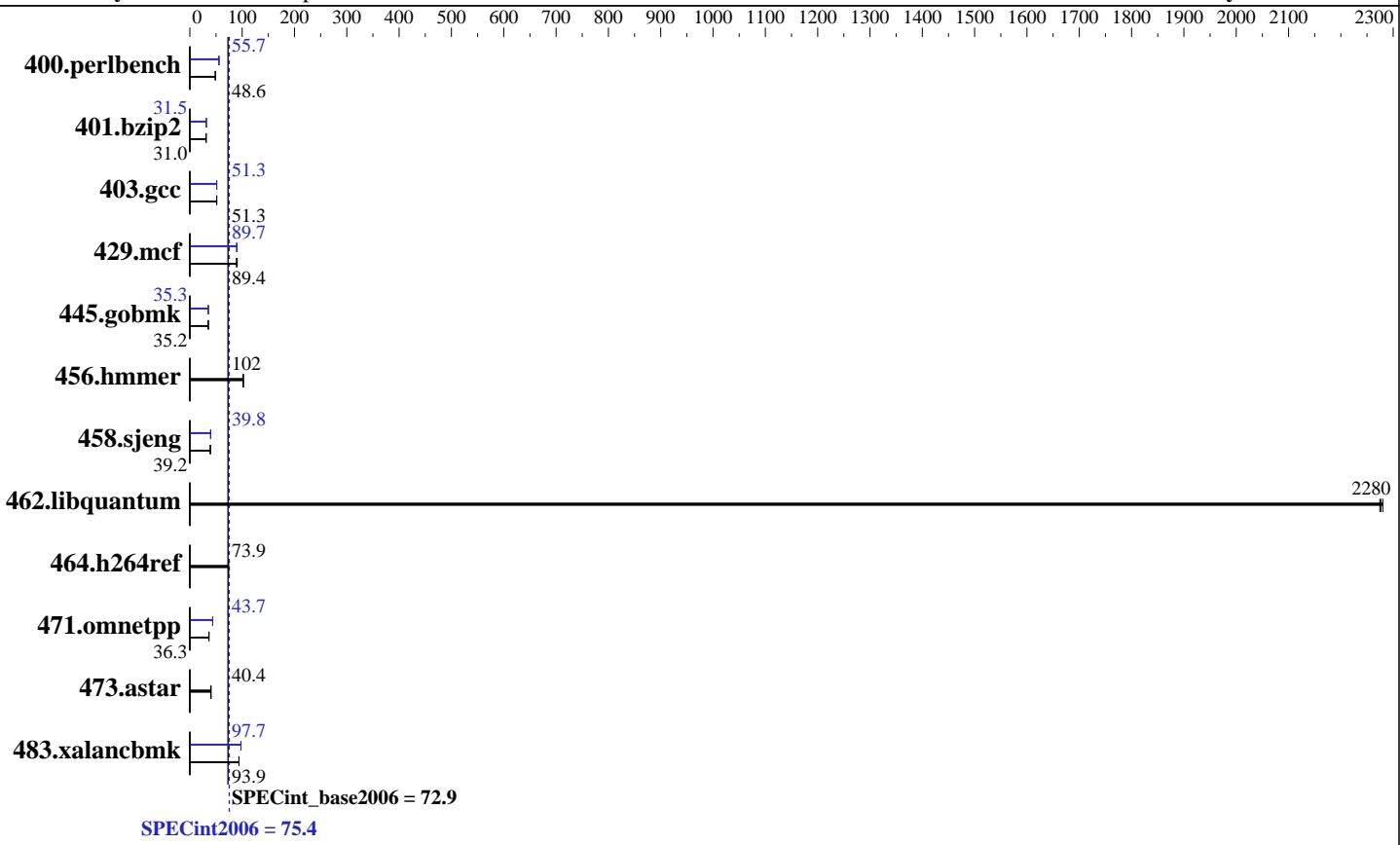
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Apr-2017

Hardware Availability: Apr-2017

Software Availability: Jan-2017



Hardware

CPU Name: Intel Xeon E3-1230 v6
CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz: 3500
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (2 x 16 GB 2Rx8 PC4-2400T-E)
Disk Subsystem: 1 x 1 TB SATA, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
Compiler: Kernel 3.10.0-514.6.1.el7.x86_64
C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel: Yes
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	199	49.1	201	48.6	201	48.5	176	55.5	175	55.7	175	55.7
401.bzip2	311	31.0	311	31.0	310	31.1	306	31.5	307	31.5	306	31.5
403.gcc	157	51.4	157	51.3	157	51.3	157	51.4	157	51.3	157	51.3
429.mcf	101	90.6	102	89.3	102	89.4	102	89.4	101	90.4	102	89.7
445.gobmk	298	35.2	298	35.2	298	35.3	298	35.3	298	35.2	297	35.3
456.hmmer	91.2	102	91.4	102	91.3	102	91.2	102	91.4	102	91.3	102
458.sjeng	309	39.2	309	39.2	309	39.2	304	39.8	305	39.7	304	39.8
462.libquantum	9.09	2280	9.11	2270	9.10	2280	9.09	2280	9.11	2270	9.10	2280
464.h264ref	300	73.8	300	73.9	299	74.0	300	73.8	300	73.9	299	74.0
471.omnetpp	172	36.3	170	36.8	172	36.3	143	43.7	143	43.6	143	43.7
473.astar	175	40.1	174	40.4	174	40.4	175	40.1	174	40.4	174	40.4
483.xalancbmk	73.6	93.7	73.5	93.9	73.4	94.1	70.5	97.8	70.6	97.7	70.7	97.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:

Power Management Policy: Custom

Energy Performance: Performance

Hyper-Threading: Disabled

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default.



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Base Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

445.gobmk: `-DSPEC_CPU_LP64`

456.hmmr: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

464.h264ref: `-DSPEC_CPU_LP64`

471.omnetpp: `-DSPEC_CPU_LP64`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32`

C++ benchmarks:

`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

C++ benchmarks (except as noted below):

`icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

445.gobmk: `-D_FILE_OFFSET_BITS=64`

456.hmmr: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

464.h264ref: `-DSPEC_CPU_LP64`

471.omnetpp: `-D_FILE_OFFSET_BITS=64`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)`
`-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2) -qopt-prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)`
`-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div -auto-ilp32 -qopt-prefetch`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc`
`-qopt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel`
`-qopt-prefetch -auto-p32`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)`
`-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)`
`-no-prec-div(pass 2)`

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes

```
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -unroll4
```

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

```
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
              -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -qopt-ra-region-strategy=block
              -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

473.astar: basepeak = yes

```
483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
                -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110i-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110i-RevA.xml>

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 For other inquiries, please contact webmaster@spec.org.

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