Cisco Systems
Cisco UCS B460 M4 (Intel Xeon E7-4830 v4 2.00 GHz)

SPECint®2006 = 58.8
SPECint_base2006 = 56.0

CPU2006 license: 9019
Test sponsor: Cisco Systems
Test date: May-2017
Tested by: Cisco Systems
Hardware Availability: Apr-2016

Legend:

400.perlbench 401.bzip2 403.gcc 429.mcf 445.gobmk 456.hmmer 458.sjeng 462.libquantum
464.h264ref 471.omnetpp 473.astar 483.xalancbmk

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2

Hardware

CPU Name: Intel Xeon E7-4830 v4
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 56 cores, 4 chips, 14 cores/chip
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (32 x 32 GB 2Rx4 PC4-2133P-R, running at 1333 MHz)
Disk Subsystem: 1 x 400 GB SAS SSD
Other Hardware: None

Software

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Software Availability: Sep-2016

Results Table

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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>Base</td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>303</td>
<td>32.2</td>
<td>303</td>
<td>32.3</td>
<td>302</td>
<td>32.3</td>
<td>263</td>
<td>37.2</td>
<td>263</td>
<td>37.1</td>
<td>263</td>
<td>37.2</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>463</td>
<td>20.8</td>
<td>465</td>
<td>20.8</td>
<td>464</td>
<td>20.8</td>
<td>465</td>
<td>20.8</td>
<td>464</td>
<td>20.8</td>
<td>464</td>
<td>20.8</td>
</tr>
<tr>
<td>403.gcc</td>
<td>267</td>
<td>30.2</td>
<td>267</td>
<td>30.2</td>
<td>267</td>
<td>30.2</td>
<td>261</td>
<td>30.8</td>
<td>262</td>
<td>30.7</td>
<td>262</td>
<td>30.7</td>
</tr>
<tr>
<td>429.mcf</td>
<td>172</td>
<td>53.0</td>
<td>173</td>
<td>52.7</td>
<td>178</td>
<td>51.1</td>
<td>174</td>
<td>52.4</td>
<td>174</td>
<td>52.4</td>
<td>173</td>
<td>52.7</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>429</td>
<td>24.4</td>
<td>429</td>
<td>24.5</td>
<td>429</td>
<td>24.5</td>
<td>423</td>
<td>24.8</td>
<td>423</td>
<td>24.8</td>
<td>423</td>
<td>24.8</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>135</td>
<td>68.9</td>
<td>136</td>
<td>68.8</td>
<td>135</td>
<td>68.9</td>
<td>135</td>
<td>68.9</td>
<td>136</td>
<td>68.8</td>
<td>135</td>
<td>68.9</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>451</td>
<td>26.8</td>
<td>451</td>
<td>26.8</td>
<td>451</td>
<td>26.9</td>
<td>439</td>
<td>27.5</td>
<td>439</td>
<td>27.6</td>
<td>439</td>
<td>27.5</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>3.01</td>
<td>6880</td>
<td>3.01</td>
<td>6890</td>
<td>3.01</td>
<td>6880</td>
<td>3.01</td>
<td>6890</td>
<td>3.01</td>
<td>6890</td>
<td>3.01</td>
<td>6890</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>467</td>
<td>47.4</td>
<td>466</td>
<td>47.5</td>
<td>467</td>
<td>47.4</td>
<td>467</td>
<td>47.4</td>
<td>466</td>
<td>47.5</td>
<td>467</td>
<td>47.4</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>177</td>
<td>35.3</td>
<td>179</td>
<td>34.9</td>
<td>176</td>
<td>35.4</td>
<td>135</td>
<td>46.4</td>
<td>135</td>
<td>46.3</td>
<td>134</td>
<td>46.7</td>
</tr>
<tr>
<td>473.astar</td>
<td>244</td>
<td>28.8</td>
<td>242</td>
<td>29.0</td>
<td>243</td>
<td>28.9</td>
<td>244</td>
<td>28.7</td>
<td>245</td>
<td>28.6</td>
<td>244</td>
<td>28.8</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>115</td>
<td>59.9</td>
<td>115</td>
<td>59.8</td>
<td>116</td>
<td>59.4</td>
<td>102</td>
<td>67.3</td>
<td>103</td>
<td>67.1</td>
<td>103</td>
<td>67.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The config file option 'submit' was used.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
BIOS Settings:
Intel Hyper-Threading Technology option set to Disabled
CPU performance set to Enterprise
Power Technology set to Energy Efficient
Energy Performance BIAS setting set to Balanced Performance
Memory RAS configuration set to Maximum Performance
Memory Power Saving Mode set to Disabled
QPI Snoop Mode set to Home Directory Snoop with OSB
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
runtime on linux-3y2r Sun May 7 23:46:48 2017
This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-4830 v4 @ 2.00GHz
4 "physical id"s (chips)
Cisco Systems
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Platform Notes (Continued)

56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 14
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From /proc/meminfo
MemTotal: 1058510992 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or
  release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
  Linux linux-3y2r 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
  (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 2 22:08

SPEC is set to: /opt/cpu2006-1.2

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 xfs 373G 23G 350G 7% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. EXM4.3.1.2c.0.080220161434 08/02/2016
Memory:
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Platform Notes (Continued)
32x 0xCE00 M393A4K40BB0-CPB 32 GB 2 rank 2133 MHz, configured at 1333 MHz
64x NO DIMM NO DIMM 2400 MHz

(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "56"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
  -auto-p32

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Base Optimization Flags (Continued)
C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m64
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
C++ benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
473.astar: icpc -m64

Peak Portability Flags
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-ra-region-strategy=block
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revD.20170404.xml

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For other inquiries, please contact webmaster@spec.org.

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