## Dell Inc.

PowerEdge R740 (Intel Xeon Gold 6152, 2.10 GHz)

### SPECint _rate2006 = Not Run

**SPECint_rate_base2006 = 1970**

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-514.el7.x86_64</td>
<td>CPU Name: Intel Xeon Gold 6152</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux</td>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz</td>
</tr>
<tr>
<td>Auto Parallel: No</td>
<td>CPU MHz: 2100</td>
</tr>
<tr>
<td>File System: xfs</td>
<td>CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>CPU(s) orderable: 1.2 chip</td>
</tr>
<tr>
<td>Base Pointers: 32-bit</td>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
<td>Secondary Cache: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Other Software: Microquill SmartHeap V10.2</td>
<td>L3 Cache: 30.25 MB I+D on chip per chip</td>
</tr>
</tbody>
</table>

### 400.perlbench

**SPECint_rate_base2006 = 1970**

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<tr>
<th>Copies</th>
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<th>300</th>
<th>450</th>
<th>600</th>
<th>750</th>
<th>900</th>
<th>1050</th>
<th>1200</th>
<th>1350</th>
<th>1500</th>
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<tbody>
<tr>
<td>400.perlbench</td>
<td>ss</td>
<td>88</td>
<td>873</td>
<td>1410</td>
<td>2640</td>
<td>1190</td>
<td>2740</td>
<td>1260</td>
<td>32300</td>
<td>2120</td>
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</table>

### 401.bzip2


### 429.mcf


### 445.gobmk


### 456.hmmer


### 458.sjeng


### 462.libquantum


### 464.h264ref


### 471.omnetpp


### 473.astar


### 483.xalancbmk


### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6152</th>
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<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.70 GHz</td>
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<tr>
<td>CPU MHz:</td>
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<tr>
<td>FPU:</td>
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<tr>
<td>CPU(s) enabled:</td>
<td>44 cores, 2 chips, 22 cores/chip, 2 threads/core</td>
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<tr>
<td>CPU(s) orderable:</td>
<td>1.2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache:</td>
<td>30.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)</td>
</tr>
<tr>
<td>Disk Subsystem:</td>
<td>1 x 960 GB SATA SSD</td>
</tr>
<tr>
<td>Other Hardware:</td>
<td>None</td>
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</tbody>
</table>
Dell Inc.

PowerEdge R740 (Intel Xeon Gold 6152, 2.10 GHz)

**SPECint_rate2006 = Not Run**

**SPECint_rate_base2006 = 1970**

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<thead>
<tr>
<th>Benchmark</th>
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<th>Ratio</th>
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<tr>
<td><strong>Base</strong></td>
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<td>1400</td>
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<td>304</td>
<td>2640</td>
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<tr>
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<tr>
<td>458.sjeng</td>
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<td>1260</td>
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<td>462.libquantum</td>
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<td>32300</td>
<td>56.5</td>
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<td>464.h264ref</td>
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<td>471.omnetpp</td>
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<td>473.astar</td>
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<td>1100</td>
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<td>1090</td>
<td>563</td>
<td>1100</td>
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<tr>
<td>483.xalancbmk</td>
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<td>2180</td>
<td>279</td>
<td>2170</td>
<td>278</td>
<td>2180</td>
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</table>

<table>
<thead>
<tr>
<th><strong>Peak</strong></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Platform Notes**

BIOS settings:
Sub NUMA Cluster enabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /cpu2006-1.2_ic17u3/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
r
running on localhost.localdomain Tue Jun 6 11:09:39 2017
Copyright 2006-2017 Standard Performance Evaluation Corporation

SPEC CINT2006 Result

Dell Inc.

PowerEdge R740 (Intel Xeon Gold 6152, 2.10 GHz)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 1970

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jun-2017
Hardware Availability: Jul-2017
Software Availability: Nov-2016

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
  2 "physical id"s (chips)
  88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 22
siblings : 44
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
cache size : 30976 KB

From /proc/meminfo
MemTotal: 394870516 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jun 6 11:05

SPEC is set to: /cpu2006-1.2_ic17u3
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 838G 7.4G 831G 1% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to
Continued on next page
SPEC CINT2006 Result

Dell Inc.

PowerEdge R740 (Intel Xeon Gold 6152, 2.10 GHz)

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SPECint_rate_base2006 = 1970

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

- hardware, firmware, and the "DMTF SMBIOS" standard.
- BIOS Dell Inc. 1.0.0 05/16/2017
- Memory: 24x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006-1.2_ic17u3/lib/ia32:/cpu2006-1.2_ic17u3/lib/intel64:/cpu2006-1.2_ic17u3/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runcspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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<tr>
<td>SPECint_rate_base2006 =</td>
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</table>

**CPU2006 license:** 55  
**Test sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

<table>
<thead>
<tr>
<th>Test date:</th>
<th>Jun-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Nov-2016</td>
</tr>
</tbody>
</table>

**Base Optimization Flags**

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

**Base Other Flags**

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revB.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.