SPEC® CFP2006 Result

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Huawei

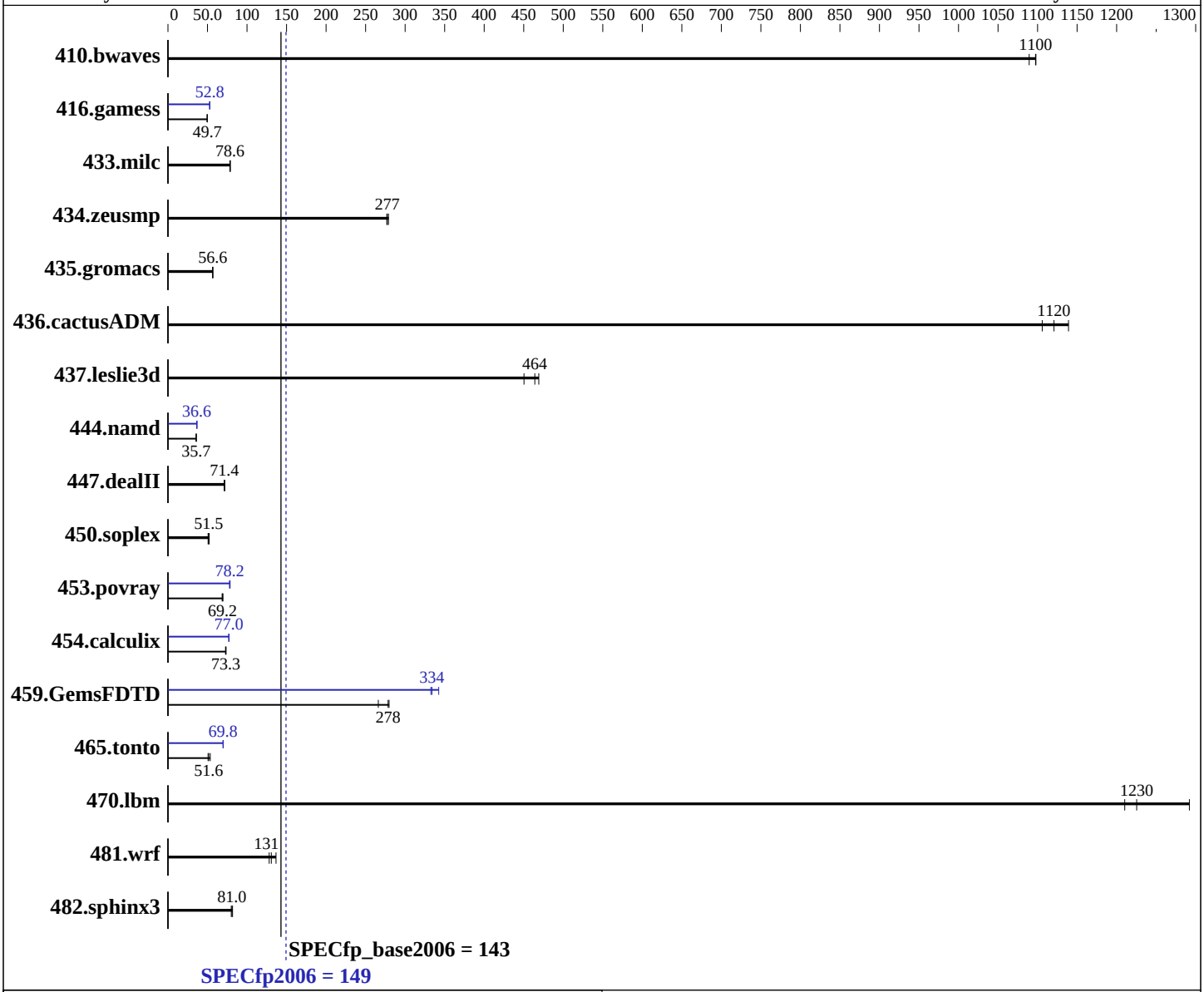
Huawei CH121 V5 (Intel Xeon Gold 6142)

SPECfp®2006 = 149

SPECfp_base2006 = 143

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Jun-2017
Hardware Availability: Aug-2017
Software Availability: Nov-2016



Hardware

CPU Name:	Intel Xeon Gold 6142
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	32 cores, 2 chips, 16 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-514.el7.x86_64
Compiler:	C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux
Auto Parallel:	Yes
File System:	xfs

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L3 Cache:	22 MB I+D on chip per chip	System State:	Run level 3 (multi-user)
Other Cache:	None	Base Pointers:	64-bit
Memory:	768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)	Peak Pointers:	32/64-bit
Disk Subsystem:	1 x 1200 GB SAS, 10000 RPM	Other Software:	None
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**12.4**	**1100**	12.5	1090	12.4	1100	**12.4**	**1100**	12.5	1090	12.4	1100
416.gamess	394	49.6	394	49.7	**394**	**49.7**	370	52.9	**371**	**52.8**	371	52.8
433.milc	**117**	**78.6**	117	78.3	116	78.9	**117**	**78.6**	117	78.3	116	78.9
434.zeusmp	**32.8**	**277**	32.9	277	32.6	279	**32.8**	**277**	32.9	277	32.6	279
435.gromacs	126	56.6	126	56.7	**126**	**56.6**	126	56.6	126	56.7	**126**	**56.6**
436.cactusADM	**10.7**	**1120**	10.8	1110	10.5	1140	**10.7**	**1120**	10.8	1110	10.5	1140
437.leslie3d	**20.3**	**464**	20.0	469	20.9	450	**20.3**	**464**	20.0	469	20.9	450
444.namd	224	35.8	**224**	**35.7**	225	35.7	219	36.6	**219**	**36.6**	219	36.6
447.dealII	161	71.2	**160**	**71.4**	159	71.9	161	71.2	**160**	**71.4**	159	71.9
450.soplex	160	52.1	164	50.8	**162**	**51.5**	160	52.1	164	50.8	**162**	**51.5**
453.povray	77.7	68.5	76.5	69.5	**76.9**	**69.2**	67.7	78.6	**68.0**	**78.2**	68.3	77.9
454.calculix	113	72.8	112	73.4	**112**	**73.3**	107	77.3	**107**	**77.0**	107	76.9
459.GemsFDTD	**38.1**	**278**	39.9	266	37.9	280	31.9	333	31.0	343	**31.8**	**334**
465.tonto	194	50.7	**191**	**51.6**	184	53.5	**141**	**69.8**	141	69.8	141	69.7
470.lbm	**11.2**	**1230**	10.6	1290	11.4	1210	**11.2**	**1230**	10.6	1290	11.4	1210
481.wrf	**85.4**	**131**	81.7	137	87.2	128	**85.4**	**131**	81.7	137	87.2	128
482.sphinx3	239	81.7	243	80.1	**241**	**81.0**	239	81.7	243	80.1	**241**	**81.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Set Hyper-Threading to Disable
Sysinfo program /spec17/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on localhost.localdomain Fri Jun 23 04:55:54 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo
```

Continued on next page

Platform Notes (Continued)

```
From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
      2 "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 16
      siblings  : 16
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
   cache size : 22528 KB

From /proc/meminfo
   MemTotal:       790482140 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.3 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.3"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
   EDT 2016 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 22 15:52

SPEC is set to: /spec17
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      xfs   898G   17G  882G   2% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS INSYDE Corp. 0.13 04/11/2017
  Memory:
   24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz
```

Continued on next page

Platform Notes (Continued)

```
(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/spec17/libs/32:/spec17/libs/64:/spec17/sh10.2"
OMP_NUM_THREADS = "32"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll2 -inline-level=0 -qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-SKL-V1.6.xml`

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