SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

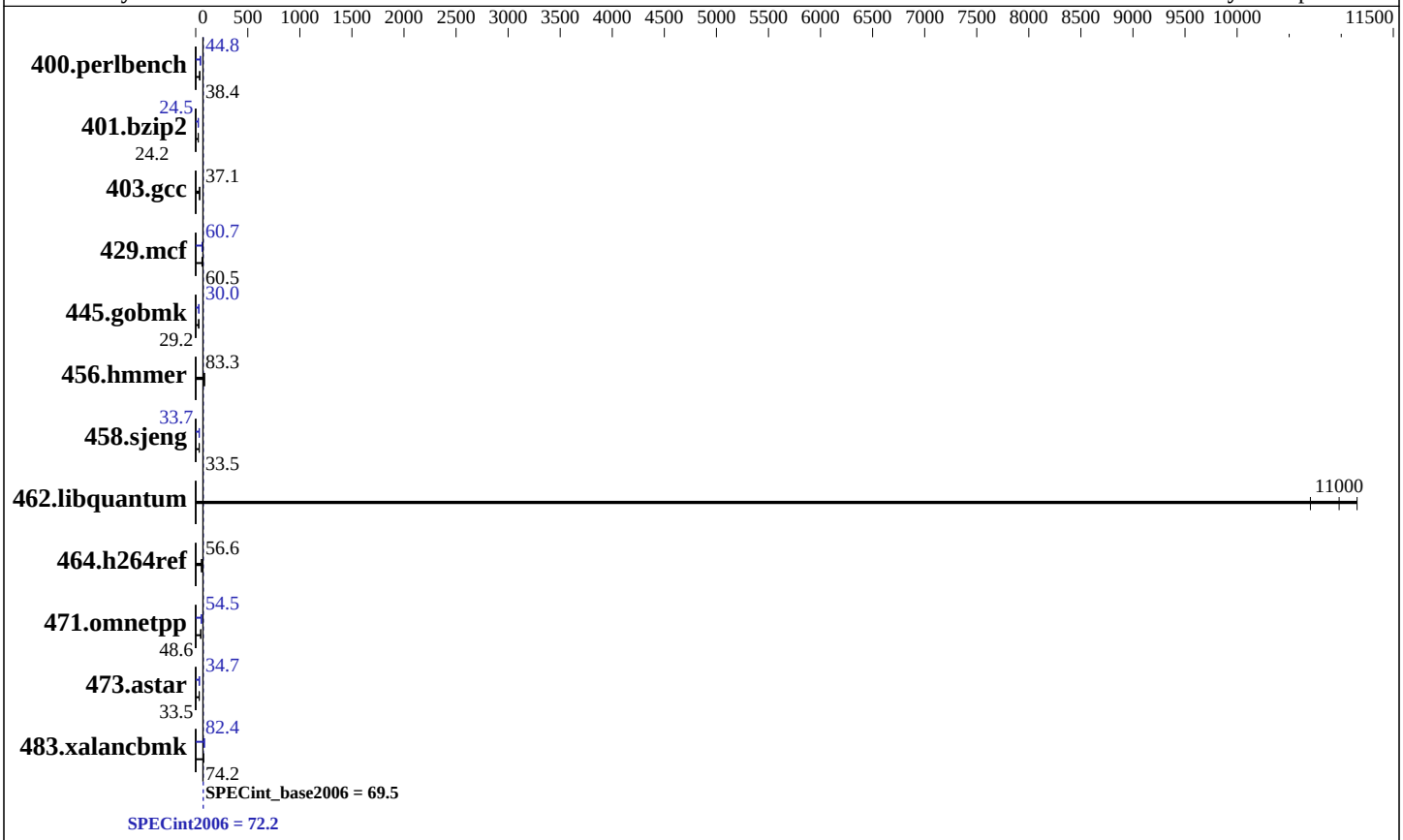
Huawei CH242 V3 (Intel Xeon E7-8894 v4)

SPECint®2006 = 72.2

SPECint_base2006 = 69.5

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Aug-2017
Hardware Availability: Mar-2017
Software Availability: Sep-2016



Hardware

CPU Name:	Intel Xeon E7-8894 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.40 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	96 cores, 4 chips, 24 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	60 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
Disk Subsystem:	1 x 960 GB SSD
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.el7.x86_64
Compiler:	C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Huawei

Huawei CH242 V3 (Intel Xeon E7-8894 v4)

SPECint2006 = 72.2

SPECint_base2006 = 69.5

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Aug-2017
Hardware Availability: Mar-2017
Software Availability: Sep-2016

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**255**	**38.4**	255	38.4	255	38.4	**218**	**44.8**	218	44.7	218	44.8
401.bzip2	401	24.1	397	24.3	**399**	**24.2**	**393**	**24.5**	393	24.6	393	24.5
403.gcc	217	37.1	217	37.1	**217**	**37.1**	217	37.1	217	37.1	**217**	**37.1**
429.mcf	153	59.7	151	60.6	**151**	**60.5**	**150**	**60.7**	152	60.1	150	60.7
445.gobmk	**359**	**29.2**	359	29.2	358	29.3	**349**	**30.0**	349	30.0	349	30.0
456.hmmer	112	83.4	112	83.3	**112**	**83.3**	112	83.4	112	83.3	**112**	**83.3**
458.sjeng	362	33.5	**361**	**33.5**	361	33.5	**359**	**33.7**	359	33.7	360	33.7
462.libquantum	**1.89**	**11000**	1.94	10700	1.86	11200	**1.89**	**11000**	1.94	10700	1.86	11200
464.h264ref	390	56.7	392	56.5	**391**	**56.6**	390	56.7	392	56.5	**391**	**56.6**
471.omnetpp	129	48.5	**129**	**48.6**	127	49.3	115	54.5	**115**	**54.5**	114	54.7
473.astar	209	33.7	211	33.3	**209**	**33.5**	202	34.7	**202**	**34.7**	202	34.7
483.xalancbmk	**93.1**	**74.2**	92.8	74.3	93.4	73.9	**83.7**	**82.4**	83.7	82.4	83.9	82.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Set Power Efficiency Mode to Custom
Set Hyper-Threading to Disabled
Set Lock_step to disabled
Baseboard Management Controller used to adjust the fan speed to 100
Sysinfo program /spec/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on localhost.localdomain Sat Aug  5 10:51:36 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E7-8894 v4 @ 2.40GHz
      4 "physical id"s (chips)
      96 "processors"
```

Continued on next page

Platform Notes (Continued)

```
      cores, siblings (Caution: counting these is hw and system dependent.  The
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 24
         siblings  : 24
         physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
         physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26
         27 28 29
      cache size : 61440 KB

   From /proc/meminfo
      MemTotal:       528073720 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   From /etc/*release* /etc/*version*
      os-release:
         NAME="Red Hat Enterprise Linux Server"
         VERSION="7.2 (Maipo)"
         ID="rhel"
         ID_LIKE="fedora"
         VERSION_ID="7.2"
         PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
         ANSI_COLOR="0;31"
         CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
      redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
      system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

   uname -a:
      Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
      EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Aug 5 10:38

   SPEC is set to: /spec
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda2      ext4  796G  387G  368G  52% /
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS American Megatrends Inc. BLISV796 03/10/2017
     Memory:
```

Continued on next page

Platform Notes (Continued)

```
      32x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz

    (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh10.2"
OMP_NUM_THREADS = "96"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
  401.bzip2: -DSPEC_CPU_LP64
    403.gcc: -DSPEC_CPU_LP64
    429.mcf: -DSPEC_CPU_LP64
  445.gobmk: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
  458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
  464.h264ref: -DSPEC_CPU_LP64
  471.omnetpp: -DSPEC_CPU_LP64
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: basepeak = yes

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2) -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -qopt-ra-region-strategy=block -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Aug 23 13:13:50 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 August 2017.