## Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Platinum 8160 2.10GHz)

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp\textsuperscript{2006} = 148</td>
<td>SPECfp\textsubscript{base}\textsuperscript{2006} = 141</td>
</tr>
</tbody>
</table>

- **CPU2006 license:** 9019  
- **Test date:** Aug-2017  
- **Test sponsor:** Cisco Systems  
- **Hardware Availability:** Aug-2017  
- **Tested by:** Cisco Systems  
- **Software Availability:** Apr-2017

### CPU Characteristics:
- **CPU Name:** Intel Xeon Platinum 8160  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
- **CPU MHz:** 2100  
- **FPU:** Integrated  
- **CPU(s) enabled:** 96 cores, 4 chips, 24 cores/chip  
- **CPU(s) orderable:** 2,4 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 1 MB I+D on chip per core

### Operating System:
- **Operating System:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
- **Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux

### Auto Parallel:
- **Auto Parallel:** Yes

### File System:
- **File System:** xfs

### System State:
- **System State:** Run level 3 (multi-user)
## SPEC CFP2006 Result

### Cisco Systems

Cisco UCS C480 M5 (Intel Xeon Platinum 8160 2.10GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Pointers</th>
<th>Peak Pointers</th>
<th>Other Cache</th>
<th>Memory</th>
<th>Disk Subsystem</th>
<th>Other Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>64-bit</td>
<td>32/64-bit</td>
<td>None</td>
<td>768 GB</td>
<td>1 x 600 GB SAS HDD, 10K RPM</td>
<td>None</td>
</tr>
</tbody>
</table>

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>bwaves</td>
<td>8.30</td>
<td>1640</td>
<td>7.97</td>
<td>1700</td>
<td>8.22</td>
<td>1650</td>
<td>8.30</td>
<td>1640</td>
<td>7.97</td>
<td>1700</td>
</tr>
<tr>
<td>gameess</td>
<td>405</td>
<td>48.3</td>
<td>406</td>
<td>48.2</td>
<td>406</td>
<td>48.2</td>
<td>378</td>
<td>51.8</td>
<td>378</td>
<td>51.8</td>
</tr>
<tr>
<td>milc</td>
<td>116</td>
<td>79.2</td>
<td>118</td>
<td>77.6</td>
<td>117</td>
<td>78.5</td>
<td>116</td>
<td>79.2</td>
<td>118</td>
<td>77.6</td>
</tr>
<tr>
<td>zeusmp</td>
<td>49.7</td>
<td>183</td>
<td>49.3</td>
<td>184</td>
<td>50.2</td>
<td>181</td>
<td>49.7</td>
<td>183</td>
<td>49.3</td>
<td>184</td>
</tr>
<tr>
<td>gromacs</td>
<td>163</td>
<td>43.7</td>
<td>161</td>
<td>44.3</td>
<td>161</td>
<td>44.3</td>
<td>163</td>
<td>43.7</td>
<td>161</td>
<td>44.3</td>
</tr>
<tr>
<td>cactusADM</td>
<td>8.24</td>
<td>1450</td>
<td>8.78</td>
<td>1360</td>
<td>8.70</td>
<td>1370</td>
<td>8.24</td>
<td>1450</td>
<td>8.78</td>
<td>1360</td>
</tr>
<tr>
<td>leslie3d</td>
<td>22.4</td>
<td>420</td>
<td>22.2</td>
<td>423</td>
<td>22.2</td>
<td>423</td>
<td>22.4</td>
<td>420</td>
<td>22.2</td>
<td>423</td>
</tr>
<tr>
<td>namd</td>
<td>224</td>
<td>35.7</td>
<td>224</td>
<td>35.7</td>
<td>225</td>
<td>35.7</td>
<td>219</td>
<td>36.6</td>
<td>219</td>
<td>36.6</td>
</tr>
<tr>
<td>dealII</td>
<td>157</td>
<td>72.9</td>
<td>157</td>
<td>72.9</td>
<td>157</td>
<td>72.9</td>
<td>157</td>
<td>72.9</td>
<td>157</td>
<td>72.9</td>
</tr>
<tr>
<td>soplex</td>
<td>159</td>
<td>52.5</td>
<td>157</td>
<td>53.1</td>
<td>158</td>
<td>52.9</td>
<td>159</td>
<td>52.5</td>
<td>157</td>
<td>53.1</td>
</tr>
<tr>
<td>povray</td>
<td>76.5</td>
<td>69.5</td>
<td>76.3</td>
<td>69.7</td>
<td>76.1</td>
<td>69.9</td>
<td>67.2</td>
<td>79.1</td>
<td>67.1</td>
<td>79.3</td>
</tr>
<tr>
<td>calculix</td>
<td>115</td>
<td>71.6</td>
<td>116</td>
<td>71.4</td>
<td>116</td>
<td>71.3</td>
<td>107</td>
<td>77.2</td>
<td>107</td>
<td>77.3</td>
</tr>
<tr>
<td>GemsFDTD</td>
<td>79.4</td>
<td>134</td>
<td>81.2</td>
<td>131</td>
<td>78.6</td>
<td>135</td>
<td>72.9</td>
<td>146</td>
<td>72.7</td>
<td>146</td>
</tr>
<tr>
<td>tonto</td>
<td>218</td>
<td>45.1</td>
<td>232</td>
<td>42.5</td>
<td>229</td>
<td>43.0</td>
<td>149</td>
<td>66.3</td>
<td>148</td>
<td>66.4</td>
</tr>
<tr>
<td>lbm</td>
<td>4.14</td>
<td>3320</td>
<td>4.29</td>
<td>3200</td>
<td>4.20</td>
<td>3270</td>
<td>4.14</td>
<td>3320</td>
<td>4.29</td>
<td>3200</td>
</tr>
<tr>
<td>wrf</td>
<td>84.8</td>
<td>132</td>
<td>84.4</td>
<td>132</td>
<td>83.7</td>
<td>134</td>
<td>84.8</td>
<td>132</td>
<td>84.4</td>
<td>132</td>
</tr>
<tr>
<td>sphinx3</td>
<td>295</td>
<td>66.2</td>
<td>294</td>
<td>66.3</td>
<td>298</td>
<td>65.5</td>
<td>295</td>
<td>66.2</td>
<td>294</td>
<td>66.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

**BIOS Settings:**
- Intel HyperThreading Technology set to Disabled
- CPU performance set to Enterprise
- Power Performance Tuning set to OS
- SNC set to Disabled
- IMC Interleaving set to Auto
- Patrol Scrub set to Disabled

Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-nvug Wed Aug  9 11:20:04 2017

Continued on next page
Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Platinum 8160 2.10GHz)  

SPECfp2006 = 148
SPECfp_base2006 = 141

CPU2006 license: 9019
Test sponsor: Cisco Systems
Test date: Aug-2017
Hardware Availability: Aug-2017
Tested by: Cisco Systems
Software Availability: Apr-2017

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8160 CPU @ 2.10GHz
4 "physical id"s (chips)
96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
cache size : 33792 KB

From /proc/meminfo
MemTotal: 791193120 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 9 11:14

Continued on next page
SPEC CFP2006 Result

Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Platinum 8160 2.10GHz)

SPECfp2006 = 148
SPECfp_base2006 = 141

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Aug-2017
Hardware Availability: Aug-2017
Software Availability: Apr-2017

Platform Notes (Continued)

SPEC is set to: /home/cpu2006-1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 644G 96G 549G 15% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C480M5.3.1.0.248.0518171057 05/18/2017
Memory:
48x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "*/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "96"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

Continued on next page
Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Platinum 8160 2.10GHz)

SPECfp2006 = 148
SPECfp_base2006 = 141

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Aug-2017
Hardware Availability: Aug-2017
Software Availability: Apr-2017

Base Portability Flags (Continued)

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Platinum 8160 2.10GHz)

SPECfp2006 = 148
SPECfp_base2006 = 141

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Aug-2017
Hardware Availability: Aug-2017
Software Availability: Apr-2017

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-iip32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
-auto -unroll4

Continued on next page
Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Platinum 8160 2.10GHz)

SPECfp2006 = 148
SPECfp_base2006 = 141

CPU2006 license: 9019
Test sponsor: Cisco Systems
Test date: Aug-2017
Tested by: Cisco Systems
Hardware Availability: Aug-2017
Software Availability: Apr-2017

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 5 September 2017.