Cisco Systems
Cisco UCS C480 M5 (Intel Xeon Gold 6148 2.40GHz)

**SPECint®2006 =** Not Run
**SPECint_base2006 =** 76.8

<table>
<thead>
<tr>
<th>CPU2006 license</th>
<th>9019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor</td>
<td>Cisco Systems</td>
</tr>
<tr>
<td>Tested by</td>
<td>Cisco Systems</td>
</tr>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 6148</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>Intel Turbo Boost Technology up to 3.70 GHz</td>
</tr>
<tr>
<td>CPU MHZ</td>
<td>2400</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>80 cores, 4 chips, 20 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>2.4 chips</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache</td>
<td>27.5 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>768 GB (48 x 16 GB 2Rx4 PC4-2666V-R)</td>
</tr>
<tr>
<td>Disk Subsystem</td>
<td>1 x 600 GB SAS HDD, 10K RPM</td>
</tr>
<tr>
<td>Other Hardware</td>
<td>None</td>
</tr>
</tbody>
</table>

**Software**

- Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-514.el7.x86_64
- Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
- Auto Parallel: Yes
- File System: xfs
- System State: Run level 3 (multi-user)
- Base Pointers: 32/64-bit
- Peak Pointers: 32/64-bit
- Other Software: Microquill SmartHeap V10.2
SPEC CINT2006 Result

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CPU2006 license: 9019  
Test sponsor: Cisco Systems  
Tested by: Cisco Systems

Test date: Aug-2017  
Hardware Availability: Aug-2017  
Software Availability: Apr-2017

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>210</td>
<td>46.6</td>
<td>209</td>
<td>46.8</td>
<td>209</td>
<td>46.8</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>340</td>
<td>28.4</td>
<td>340</td>
<td>28.4</td>
<td>340</td>
<td>28.4</td>
</tr>
<tr>
<td>403.gcc</td>
<td>185</td>
<td>43.5</td>
<td>186</td>
<td>43.3</td>
<td>185</td>
<td>43.4</td>
</tr>
<tr>
<td>429.mcf</td>
<td>113</td>
<td>80.9</td>
<td>114</td>
<td>80.2</td>
<td>116</td>
<td>78.9</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>312</td>
<td>33.6</td>
<td>311</td>
<td>33.7</td>
<td>311</td>
<td>33.7</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>96.6</td>
<td>96.6</td>
<td>96.6</td>
<td>96.6</td>
<td>96.3</td>
<td>96.9</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>326</td>
<td>37.2</td>
<td>325</td>
<td>37.3</td>
<td>325</td>
<td>37.3</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>2.33</td>
<td>8880</td>
<td>2.26</td>
<td>9170</td>
<td>2.43</td>
<td>8540</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>320</td>
<td>69.1</td>
<td>320</td>
<td>69.1</td>
<td>322</td>
<td>68.7</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>160</td>
<td>39.1</td>
<td>161</td>
<td>38.9</td>
<td>156</td>
<td>40.0</td>
</tr>
<tr>
<td>473.astar</td>
<td>180</td>
<td>39.0</td>
<td>179</td>
<td>39.2</td>
<td>179</td>
<td>39.2</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>85.8</td>
<td>80.4</td>
<td>85.4</td>
<td>80.8</td>
<td>85.8</td>
<td>80.5</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Intel HyperThreading Technology set to Disabled
CPU performance set to Enterprise
Power Performance Tuning set to OS
SNC set to Disabled
IMC Interleaving set to Auto
Patrol Scrub set to Disabled
Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on rhe173-spec Fri Aug 18 02:16:24 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz
4 "physical id"s (chips)
80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20

Continued on next page
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Platform Notes (Continued)

siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 2: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 3: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From /proc/meminfo
MemTotal: 790981128 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
Linux rhel73-spec 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 18 01:44

SPEC is set to: /home/cpu2006-1.2
Files system Type Size Used Avail Use% Mounted on
/dev/sdb5 xfs 503G 28G 476G 6% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C480M5.3.1.0.272.0613172154 06/13/2017Cisco Systems,
Inc. C480M5.3.1.0.272.0613172154 06/13/2017
Memory:
96x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)
The correct amount of Memory installed is 768 GB (48 x 16 GB)
and the dmidecode is reporting invalid number of DIMMs installed
Installed Memory:
48x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz
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General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "*/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "80"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation
C benchmarks:
   icc -m64
C++ benchmarks:
   icpc -m64

Base Portability Flags
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags
C benchmarks:
   -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32
C++ benchmarks:
   -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -W1,-z,muldefs -L/sh10.2 -lsmartheap64
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Base Other Flags

C benchmarks:

403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml