Cisco Systems
Cisco UCS C220 M5 (Intel Xeon Gold 6150, 2.70GHz)

<table>
<thead>
<tr>
<th>SPECfp®2006</th>
<th>SPECfp_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>150</td>
<td>145</td>
</tr>
</tbody>
</table>

CPU2006 license: 9019  Test date: Aug-2017
Test sponsor: Cisco Systems  Hardware Availability: Aug-2017
Tested by: Cisco Systems  Software Availability: Apr-2017

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Result</th>
</tr>
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<tbody>
<tr>
<td>410.bwaves</td>
<td>51.6</td>
</tr>
<tr>
<td>416.gamess</td>
<td>48.4</td>
</tr>
<tr>
<td>433.milc</td>
<td>78.0</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>263</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>57.3</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>1380</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>481</td>
</tr>
<tr>
<td>444.namd</td>
<td>36.5</td>
</tr>
<tr>
<td>447.dealII</td>
<td>72.3</td>
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<tr>
<td>450.soplex</td>
<td>51.6</td>
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<td>453.povray</td>
<td>78.8</td>
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<tr>
<td>459.GemsFDTD</td>
<td>1490</td>
</tr>
<tr>
<td>465.tonto</td>
<td>66.4</td>
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<tr>
<td>470.lbm</td>
<td>53.3</td>
</tr>
<tr>
<td>481.wrf</td>
<td>129</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>74.9</td>
</tr>
</tbody>
</table>

**Hardware:**
- CPU Name: Intel Xeon Gold 6150
- CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
- CPU MHz: 2700
- FPU: Integrated
- CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip
- CPU(s) orderable: 1,2 chips
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 1 MB I+D on chip per core

**Software:**
- Operating System: SUSE Linux Enterprise Server 12 SP2
- Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
  Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
- Auto Parallel: Yes
- File System: xfs
- System State: Run level 5 (multi-user)
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SPECf2006 = 150
SPECf_base2006 = 145

CPU2006 license: 9019
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L3 Cache: 24.75 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)
Disk Subsystem: 1 x 600 GB SAS 10K RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
BIOS Settings:
Intel HyperThreading Technology set to Disabled
CPU performance set to Enterprise
Power Performance Tuning set to OS
SNC set to Disabled
IMC Interleaving set to Auto
Patrol Scrub set to Disabled
Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-ox2h Thu Aug 17 13:34:35 2017

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th>Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Seconds</td>
<td>Ratio</td>
</tr>
<tr>
<td>410.bwaves</td>
<td>12.7</td>
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<td>453.povray</td>
<td>76.5</td>
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</tr>
<tr>
<td>454.calcuiX</td>
<td>112</td>
<td>73.8</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>42.0</td>
<td>253</td>
</tr>
<tr>
<td>465.tonto</td>
<td>184</td>
<td>53.3</td>
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<td>470.lbm</td>
<td>9.20</td>
<td>1490</td>
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<tr>
<td>481.wrf</td>
<td>88.4</td>
<td>126</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>259</td>
<td>75.4</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6150 CPU @ 2.70GHz
  2 "physical id"s (chips)
  36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
siblings : 18
  physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 25344 KB

From /proc/meminfo
MemTotal:       394653848 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-ox2h 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Aug 17 13:29

SPEC is set to: /home/cpu2006-1.2

Filesystem     Type Size Used Avail Use% Mounted on
/dev/sdb5      xfs 317G 8.2G 309G 3% /home
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Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C220M5.3.1.1d.0.0615170645 06/15/2017
Memory:
24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main

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Base Portability Flags (Continued)

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
463.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Cisco Systems
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Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilo32

447.dealII: basepeak = yes
450.soplex: basepeak = yes

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0
-qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
-auto -unroll4

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<td>454.calculix</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32</td>
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<td>481.wrf</td>
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### Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

- 435.gromacs: basepeak = yes
- 436.cactusADM: basepeak = yes
- 454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32
- 481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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