## Lenovo Global Technology

**ThinkSystem SR550**  
(2.40 GHz, Intel Xeon Gold 5115)

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Gold 5115</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>Intel Turbo Boost Technology up to 3.20 GHz</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>2400</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>20 cores, 2 chips, 10 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Primary Cache</td>
<td>32 KB L1 + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache</td>
<td>1 MB L1+D on chip per core</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System</td>
<td>SUSE Linux Enterprise Server 12 SP2 (x86_64)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Auto Parallel</td>
<td>Yes</td>
</tr>
<tr>
<td>File System</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
</tbody>
</table>

### SPEC® CFP2006 Result

**SPECfp®_rate2006 = 859**  
**SPECfp_rate_base2006 = 843**

### Test Details

- **Test Sponsor:** Lenovo Global Technology  
- **Test Date:** Sep-2017  
- **Hardware Availability:** Aug-2017  
- **Software Availability:** Apr-2017

### Benchmarks

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Time (milliseconds)</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>20</td>
<td>784</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>760</td>
</tr>
<tr>
<td>416.gamess</td>
<td>20</td>
<td>828</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>803</td>
</tr>
<tr>
<td>433.milc</td>
<td>20</td>
<td>776</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>776</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>20</td>
<td>1040</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>20</td>
<td>980</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>967</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>20</td>
<td>1100</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>1100</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>20</td>
<td>591</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>536</td>
</tr>
<tr>
<td>444.namd</td>
<td>20</td>
<td>668</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>665</td>
</tr>
<tr>
<td>447.dealII</td>
<td>20</td>
<td>1270</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>1270</td>
</tr>
<tr>
<td>450.soplex</td>
<td>20</td>
<td>547</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>544</td>
</tr>
<tr>
<td>453.povray</td>
<td>20</td>
<td>1320</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>1320</td>
</tr>
<tr>
<td>454.calculix</td>
<td>20</td>
<td>1130</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>1220</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>20</td>
<td>471</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>509</td>
</tr>
<tr>
<td>465.tonto</td>
<td>20</td>
<td>997</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>956</td>
</tr>
<tr>
<td>470.lbm</td>
<td>20</td>
<td>923</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>923</td>
</tr>
<tr>
<td>481.wrf</td>
<td>20</td>
<td>770</td>
</tr>
<tr>
<td></td>
<td>40</td>
<td>770</td>
</tr>
</tbody>
</table>

**SPECfp_rate_base2006 = 843**
Lenovo Global Technology

ThinkSystem SR550
(2.40 GHz, Intel Xeon Gold 5115)

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**SPEC CFP2006 Result**

**Lenovo Global Technology**

**CPU2006 license:** 9017

**Test sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**L3 Cache:** 13.75 MB I+D on chip per chip

**Other Cache:** None

**Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400 MHz)

**Disk Subsystem:** 1 x 800 GB SATA SSD

**Other Hardware:** None

**Test date:** Sep-2017

**Hardware Availability:** Aug-2017

**Test sponsor:** Lenovo Global Technology

**Software Availability:** Apr-2017

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>40</td>
<td>715</td>
<td>760</td>
<td>715</td>
<td>760</td>
<td>715</td>
<td>760</td>
<td></td>
<td>20</td>
<td>347</td>
<td>784</td>
<td>346</td>
<td>785</td>
<td>346</td>
</tr>
<tr>
<td>416.gamess</td>
<td>40</td>
<td>975</td>
<td>803</td>
<td>975</td>
<td>804</td>
<td>976</td>
<td>803</td>
<td></td>
<td>40</td>
<td>945</td>
<td>828</td>
<td>945</td>
<td>829</td>
<td>947</td>
</tr>
<tr>
<td>433.milc</td>
<td>40</td>
<td>473</td>
<td>776</td>
<td>473</td>
<td>776</td>
<td>473</td>
<td>776</td>
<td></td>
<td>40</td>
<td>473</td>
<td>776</td>
<td>473</td>
<td>776</td>
<td>473</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>40</td>
<td>350</td>
<td>1040</td>
<td>351</td>
<td>1040</td>
<td>351</td>
<td>1040</td>
<td></td>
<td>40</td>
<td>350</td>
<td>1040</td>
<td>351</td>
<td>1040</td>
<td>351</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>40</td>
<td>297</td>
<td>975</td>
<td>297</td>
<td>975</td>
<td>297</td>
<td>975</td>
<td></td>
<td>40</td>
<td>297</td>
<td>975</td>
<td>297</td>
<td>975</td>
<td>297</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>40</td>
<td>436</td>
<td>1100</td>
<td>436</td>
<td>1100</td>
<td>436</td>
<td>1100</td>
<td></td>
<td>40</td>
<td>436</td>
<td>1100</td>
<td>436</td>
<td>1100</td>
<td>436</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>40</td>
<td>704</td>
<td>534</td>
<td>701</td>
<td>536</td>
<td>700</td>
<td>537</td>
<td></td>
<td>20</td>
<td>318</td>
<td>591</td>
<td>318</td>
<td>592</td>
<td>318</td>
</tr>
<tr>
<td>444.namd</td>
<td>40</td>
<td>482</td>
<td>665</td>
<td>480</td>
<td>668</td>
<td>483</td>
<td>665</td>
<td></td>
<td>40</td>
<td>484</td>
<td>663</td>
<td>480</td>
<td>668</td>
<td>480</td>
</tr>
<tr>
<td>447.dealII</td>
<td>40</td>
<td>357</td>
<td>1280</td>
<td>360</td>
<td>1270</td>
<td>361</td>
<td>1270</td>
<td></td>
<td>40</td>
<td>357</td>
<td>1280</td>
<td>360</td>
<td>1270</td>
<td>361</td>
</tr>
<tr>
<td>450.soplex</td>
<td>40</td>
<td>627</td>
<td>532</td>
<td>613</td>
<td>544</td>
<td>613</td>
<td>545</td>
<td></td>
<td>40</td>
<td>307</td>
<td>543</td>
<td>305</td>
<td>547</td>
<td>305</td>
</tr>
<tr>
<td>453.povray</td>
<td>40</td>
<td>187</td>
<td>1140</td>
<td>188</td>
<td>1130</td>
<td>188</td>
<td>1130</td>
<td></td>
<td>40</td>
<td>160</td>
<td>1330</td>
<td>161</td>
<td>1320</td>
<td>161</td>
</tr>
<tr>
<td>454.calculix</td>
<td>40</td>
<td>270</td>
<td>1220</td>
<td>270</td>
<td>1220</td>
<td>269</td>
<td>1230</td>
<td></td>
<td>40</td>
<td>270</td>
<td>1220</td>
<td>270</td>
<td>1220</td>
<td>269</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>40</td>
<td>834</td>
<td>509</td>
<td>836</td>
<td>508</td>
<td>834</td>
<td>509</td>
<td></td>
<td>20</td>
<td>450</td>
<td>471</td>
<td>450</td>
<td>471</td>
<td>450</td>
</tr>
<tr>
<td>465.tonto</td>
<td>40</td>
<td>460</td>
<td>856</td>
<td>460</td>
<td>855</td>
<td>455</td>
<td>866</td>
<td></td>
<td>40</td>
<td>433</td>
<td>909</td>
<td>432</td>
<td>910</td>
<td>433</td>
</tr>
<tr>
<td>470.lbm</td>
<td>40</td>
<td>551</td>
<td>997</td>
<td>552</td>
<td>996</td>
<td>551</td>
<td>998</td>
<td></td>
<td>40</td>
<td>551</td>
<td>997</td>
<td>552</td>
<td>996</td>
<td>551</td>
</tr>
<tr>
<td>481.wrf</td>
<td>40</td>
<td>484</td>
<td>923</td>
<td>484</td>
<td>924</td>
<td>487</td>
<td>917</td>
<td></td>
<td>40</td>
<td>484</td>
<td>923</td>
<td>484</td>
<td>924</td>
<td>487</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>40</td>
<td>1025</td>
<td>760</td>
<td>1013</td>
<td>770</td>
<td>1012</td>
<td>770</td>
<td></td>
<td>40</td>
<td>1025</td>
<td>760</td>
<td>1013</td>
<td>770</td>
<td>1012</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

**BIOS configuration:**
- Operating Mode set to Maximum Performance
- DCU Streamer Prefetcher set to Disable

Continued on next page
Platform Notes (Continued)

XPT Prefetcher set to Disable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-g50d Wed Sep 6 18:07:09 2017

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
2 "physical id"s (chips)
40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 14080 KB

From /proc/meminfo
MemTotal:       395882572 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-g50d 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 6 18:06

SPEC is set to: /home/cpu2006-1.2-ic17.0u3

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Lenovo Global Technology
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SPECfp_rate2006 = 859
SPECfp_rate_base2006 = 843

Platform Notes (Continued)

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 744G 79G 664G 11% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE105Z-1.00]- 04/27/2017
Memory:
12x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: --SPEC_CPU_LP64
416.gamess: --SPEC_CPU_LP64

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Lenovo Global Technology
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SPECfp_rate2006 = 859
SPECfp_rate_base2006 = 843

CPU2006 license: 9017  Test date: Sep-2017
Test sponsor: Lenovo Global Technology  Hardware Availability: Aug-2017
Tested by: Lenovo Global Technology  Software Availability: Apr-2017

Base Portability Flags (Continued)

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Fortran benchmarks:
ifort -m64

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CPU2006 license: 9017
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology
Test date: Sep-2017
Hardware Availability: Aug-2017
Software Availability: Apr-2017

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.games: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -fno-alias -auto-ilp32
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes
450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-malloc-options=3
-qopt-mem-layout-trans=3

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SPECfp_rate2006 = 859
SPECfp_rate_base2006 = 843

CPU2006 license: 9017
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Peak Optimization Flags (Continued)

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml
## Lenovo Global Technology

**ThinkSystem SR550**  
(2.40 GHz, Intel Xeon Gold 5115)

<table>
<thead>
<tr>
<th>SPECfp_rate2006</th>
<th>859</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>843</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2006 license</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by</td>
<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

**Test date:** Sep-2017  
**Hardware Availability:** Aug-2017  
**Software Availability:** Apr-2017

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.  
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