# SPEC® CINT2006 Result

## Lenovo Global Technology

**ThinkSystem SR630**  
(2.10 GHz, Intel Xeon Gold 6130T)

### SPECint® Result

<table>
<thead>
<tr>
<th>SPECint® Rate2006</th>
<th>SPECint Rate2006 Base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>1620</td>
<td>1550</td>
</tr>
</tbody>
</table>

**CPU2006 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Sep-2017  
**Hardware Availability:** Aug-2017  
**Software Availability:** Apr-2017

### Hardware

- **CPU Name:** Intel Xeon Gold 6130T  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
- **CPU MHz:** 2100  
- **FPU:** Integrated  
- **CPU(s) enabled:** 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1.2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 1 MB I+D on chip per core  
- **L3 Cache:** 22 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
- **Disk Subsystem:** 1 x 800 GB SAS SSD  
- **Other Hardware:** None

### Software

- **Operating System:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
  Kernel 4.4.21-69-default  
- **Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
- **Auto Parallel:** Yes  
- **File System:** btrfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Microquill SmartHeap V10.2

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**Copies**

<table>
<thead>
<tr>
<th>SPECint Rate2006 Base2006</th>
<th>15300</th>
</tr>
</thead>
<tbody>
<tr>
<td>1500</td>
<td></td>
</tr>
<tr>
<td>2000</td>
<td></td>
</tr>
<tr>
<td>2500</td>
<td></td>
</tr>
<tr>
<td>3000</td>
<td></td>
</tr>
<tr>
<td>3500</td>
<td></td>
</tr>
<tr>
<td>4000</td>
<td></td>
</tr>
<tr>
<td>4500</td>
<td></td>
</tr>
<tr>
<td>5000</td>
<td></td>
</tr>
<tr>
<td>5500</td>
<td></td>
</tr>
<tr>
<td>6000</td>
<td>13801</td>
</tr>
<tr>
<td>6500</td>
<td>1160</td>
</tr>
<tr>
<td>7000</td>
<td>726</td>
</tr>
<tr>
<td>7500</td>
<td>700</td>
</tr>
<tr>
<td>8000</td>
<td>1130</td>
</tr>
<tr>
<td>8500</td>
<td>1130</td>
</tr>
<tr>
<td>9000</td>
<td>1380</td>
</tr>
<tr>
<td>9500</td>
<td>1160</td>
</tr>
<tr>
<td>10000</td>
<td>1000</td>
</tr>
<tr>
<td>10500</td>
<td>995</td>
</tr>
<tr>
<td>11000</td>
<td>1110</td>
</tr>
<tr>
<td>11500</td>
<td>1070</td>
</tr>
<tr>
<td>12000</td>
<td>1920</td>
</tr>
<tr>
<td>12500</td>
<td>1840</td>
</tr>
<tr>
<td>13000</td>
<td>885</td>
</tr>
<tr>
<td>13500</td>
<td>815</td>
</tr>
<tr>
<td>14000</td>
<td>879</td>
</tr>
<tr>
<td>14500</td>
<td>1870</td>
</tr>
<tr>
<td>15000</td>
<td></td>
</tr>
</tbody>
</table>

**Software Availability:** Apr-2017  
**Test Sponsor:** Lenovo Global Technology  
**Hardware Availability:** Aug-2017  
**Tested by:** Lenovo Global Technology  
**CPU2006 License:** 9017  
**Test Date:** Sep-2017

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**Lenovo Global Technology**  
(2.10 GHz, Intel Xeon Gold 6130T)

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**info@spec.org**  
**http://www.spec.org/**
Lenovo Global Technology

ThinkSystem SR630
(2.10 GHz, Intel Xeon Gold 6130T)

SPECint_rate2006 = 1620
SPECint_rate_base2006 = 1550

CPU2006 license: 9017
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>64</td>
<td>542</td>
<td>1150</td>
<td>540</td>
<td>1160</td>
<td>541</td>
<td>1160</td>
<td>64</td>
<td>449</td>
<td>1390</td>
<td>453</td>
<td>1380</td>
<td>455</td>
<td>1370</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>64</td>
<td>880</td>
<td>702</td>
<td>877</td>
<td>696</td>
<td>882</td>
<td>700</td>
<td>64</td>
<td>851</td>
<td>726</td>
<td>851</td>
<td>726</td>
<td>849</td>
<td>727</td>
</tr>
<tr>
<td>403.mcf</td>
<td>64</td>
<td>457</td>
<td>1130</td>
<td>457</td>
<td>1130</td>
<td>456</td>
<td>1130</td>
<td>64</td>
<td>454</td>
<td>1130</td>
<td>455</td>
<td>1130</td>
<td>455</td>
<td>1130</td>
</tr>
<tr>
<td>429.mcf</td>
<td>64</td>
<td>265</td>
<td>2200</td>
<td>264</td>
<td>2210</td>
<td>265</td>
<td>2200</td>
<td>64</td>
<td>265</td>
<td>2200</td>
<td>264</td>
<td>2210</td>
<td>265</td>
<td>2200</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>64</td>
<td>669</td>
<td>1000</td>
<td>669</td>
<td>1000</td>
<td>669</td>
<td>1000</td>
<td>64</td>
<td>675</td>
<td>995</td>
<td>675</td>
<td>995</td>
<td>674</td>
<td>996</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>64</td>
<td>262</td>
<td>2280</td>
<td>263</td>
<td>2270</td>
<td>263</td>
<td>2270</td>
<td>64</td>
<td>223</td>
<td>2680</td>
<td>224</td>
<td>2670</td>
<td>224</td>
<td>2670</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>64</td>
<td>721</td>
<td>1070</td>
<td>722</td>
<td>1070</td>
<td>722</td>
<td>1070</td>
<td>64</td>
<td>695</td>
<td>1120</td>
<td>695</td>
<td>1110</td>
<td>695</td>
<td>1110</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>64</td>
<td>85.9</td>
<td>15400</td>
<td>86.6</td>
<td>15300</td>
<td>86.5</td>
<td>15300</td>
<td>64</td>
<td>85.9</td>
<td>15400</td>
<td>86.6</td>
<td>15300</td>
<td>86.5</td>
<td>15300</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>64</td>
<td>768</td>
<td>1840</td>
<td>770</td>
<td>1840</td>
<td>742</td>
<td>1910</td>
<td>64</td>
<td>748</td>
<td>1890</td>
<td>736</td>
<td>1930</td>
<td>738</td>
<td>1920</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>64</td>
<td>491</td>
<td>815</td>
<td>490</td>
<td>816</td>
<td>491</td>
<td>815</td>
<td>64</td>
<td>452</td>
<td>885</td>
<td>450</td>
<td>889</td>
<td>452</td>
<td>885</td>
</tr>
<tr>
<td>473.astar</td>
<td>64</td>
<td>512</td>
<td>878</td>
<td>511</td>
<td>879</td>
<td>511</td>
<td>879</td>
<td>64</td>
<td>512</td>
<td>878</td>
<td>511</td>
<td>879</td>
<td>511</td>
<td>879</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>64</td>
<td>236</td>
<td>1870</td>
<td>238</td>
<td>1860</td>
<td>236</td>
<td>1870</td>
<td>64</td>
<td>236</td>
<td>1870</td>
<td>238</td>
<td>1860</td>
<td>236</td>
<td>1870</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to Enable
DCU Streamer Prefetcher set to Disable
Stale AtoS set to Enable
LLC dead alloc set to Disable
Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4e51ed28d7f986b6cbe290c1)
running on Cable-SPECcpu2017-SUSE12SP2 Tue Sep 5 05:01:04 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6130T CPU @ 2.10GHz
2 "physical id"s (chips)
Platform Notes (Continued)

64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB

From /proc/meminfo
MemTotal: 395892860 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
uname -a:
Linux Cable-SPECcpu2017-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25
10:58:20 UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Sep 4 17:42
SPEC is set to: /home/cpu2006-1.2-ic17.0u3
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 744G 147G 595G 20% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE109Q-1.00]- 06/28/2017
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz

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Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = */home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
    icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
    icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
    -qopt-mem-layout-trans=3

Continued on next page
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Base Optimization Flags (Continued)
C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64
C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Peak Portability Flags
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3
   -qopt-ra-region-strategy=block
   -Wl,-z,muldefs
   -L/sh10.2 -lsmartheap

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -qopt-prefetch -auto-ilp32
   -qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
   -qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
   -qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -unroll4 -auto-ilp32
   -qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
   -no-prec-div(pass 2)
   -qopt-ra-region-strategy=block
   -qopt-mem-layout-trans=3 -Wl,-z,muldefs
   -L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
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**Peak Other Flags**

C benchmarks:

```
403.gcc -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html](http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml](http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml)

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