Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL580 Gen10
(2.50 GHz, Intel Xeon Platinum 8180)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 5100

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Sep-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

400.perlbench 224 4070
401.bzip2 224 2360
403.gcc 224 3600
429.mcf 224 6340
445.gobmk 224 3450
456.hmmer 224 6890
458.sjeng 224 3660
462.libquantum 224 85300
464.h264ref 224 6220
471.omnetpp 224 5070
473.astar 224 2690
483.xalancbmk 224 5070

Hardware
CPU Name: Intel Xeon Platinum 8180
CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz: 2500
FPU: Integrated
CPU(s) enabled: 112 cores, 4 chips, 28 cores/chip, 2 threads/core
CPU(s) orderable: 2.4 chip(s)
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 38.5 MB I+D on chip per chip
Other Cache: None
Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 800 GB SATA SSD, RAID 0
Other Hardware: None

Software
Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP3
Kernel 4.4.73-5-default
Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
SPEC CINT2006 Result

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th></th>
<th></th>
<th>Peak</th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Copies</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
</tr>
<tr>
<td>400.perlbench</td>
<td>224</td>
<td>539</td>
<td>4060</td>
<td>537</td>
<td>4080</td>
<td>537</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>224</td>
<td>917</td>
<td>3600</td>
<td>918</td>
<td>3550</td>
<td>912</td>
</tr>
<tr>
<td>403.gcc</td>
<td>224</td>
<td>501</td>
<td>3600</td>
<td>501</td>
<td>3600</td>
<td>501</td>
</tr>
<tr>
<td>429.mcf</td>
<td>224</td>
<td>322</td>
<td>6340</td>
<td>322</td>
<td>6340</td>
<td>321</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>224</td>
<td>681</td>
<td>3450</td>
<td>680</td>
<td>3450</td>
<td>678</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>224</td>
<td>306</td>
<td>6830</td>
<td>303</td>
<td>6900</td>
<td>303</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>224</td>
<td>736</td>
<td>3680</td>
<td>741</td>
<td>3660</td>
<td>741</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>224</td>
<td>54.4</td>
<td>85300</td>
<td>54.4</td>
<td>85300</td>
<td>54.5</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>224</td>
<td>799</td>
<td>6210</td>
<td>795</td>
<td>6240</td>
<td>797</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>224</td>
<td>620</td>
<td>2260</td>
<td>621</td>
<td>2260</td>
<td>621</td>
</tr>
<tr>
<td>473.astar</td>
<td>224</td>
<td>585</td>
<td>2690</td>
<td>585</td>
<td>2690</td>
<td>585</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>224</td>
<td>304</td>
<td>5080</td>
<td>305</td>
<td>5070</td>
<td>305</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"

Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
LLC prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Stale A to S set to Enabled
Memory patrol scrubbing set to disabled
AHS PCI logging level set to Minimal logging
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

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Platform Notes (Continued)

running on DL580G10 Sun Sep 17 00:30:02 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
4 "physical id"s (chips)
 224 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 56
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
cache size : 39424 KB

From /proc/meminfo
MemTotal: 792260404 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux DL580G10 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4)
x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Sep 16 07:39

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Platform Notes (Continued)

SPEC is set to: /home/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb4 xfs 703G 126G 578G 18% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 08/18/2017
Memory:
48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64

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Base Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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