Huawei
Kunlun 9008 (Intel Xeon E7-8891 v4)

**SPECint®_rate2006** =  Not Run

**SPECint_rate_base2006** = 4170

|---------------|-----------|---------|---------|-----------|-----------|-----------|----------------|------------|-------------|----------|-------------|

**CPU2006 license:** 3175

**Test sponsor:** Huawei

**Test date:** Apr-2017

**Tested by:** Huawei

**Hardware Availability:** Jan-2016

**Software Availability:** Dec-2015

**SPECint_rate_base2006 =** 4170

| 400.perlbench | 1500 | 160 | 2000 | 2500 | 3000 | 3500 | 4000 | 4500 | 5000 | 5500 | 6000 | 6500 | 7000 | 7500 | 8000 | 8500 | 9000 | 9500 | 10000 | 10500 | 11000 | 11500 | 12000 | 12500 | 13000 | 13500 | 14000 | 14500 | 15000 | 15500 | 16000 | 16500 | 17000 | 17500 | 18000 | 18500 | 19000 | 19500 | 20000 | 20500 | 21000 | 21500 | 22000 | 22500 | 23000 | 23500 | 24000 | 24500 | 25000 | 25500 | 26000 | 26500 | 27000 | 27500 | 28000 | 28500 | 29000 | 29500 | 30000 | 30500 | 31000 | 31500 | 32000 | 32500 | 33000 | 33500 | 34000 | 34500 | 35000 | 35500 | 36000 | 36500 | 37000 | 37500 | 38000 | 38500 | 39000 | 39500 | 40000 | 40500 | 41000 | 41500 | 42000 | 42500 |

| 400.perlbench | 160 | 3500 |
| 401.bzip2     | 160 | 2240 |
| 403.gcc       | 160 | 2990 |
| 429.mcf       | 160 | 5030 |
| 445.gobmk     | 160 | 2700 |
| 456.hmmer     | 160 | 6610 |
| 458.sjeng     | 160 | 2820 |
| 462.libquantum| 160 | 42400|
| 464.h264ref   | 160 | 5040 |
| 471.omnetpp   | 160 | 1880 |
| 473.astar     | 160 | 2500 |
| 483.xalancbmk | 160 | 5370 |

**Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP1
**Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
**Auto Parallel:** No
**File System:** ext4
**System State:** Run level 5 (multi-user)
**Base Pointers:** 32-bit
**Peak Pointers:** 32/64-bit
**Other Software:** Microquill SmartHeap V10.2

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**Hardware**
- **CPU Name:** Intel Xeon E7-8891 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.50 GHz
- **CPU MHz:** 2800
- **FPU:** Integrated
- **CPU(s) enabled:** 80 cores, 8 chips, 10 cores/chip, 2 threads/core
- **CPU(s) orderable:** 4.8 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 60 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
- **Disk Subsystem:** 2 x 600 GB SAS, 10K RPM
- **Other Hardware:** None

**Software**
- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP1
  **Kernel 3.12.49-11-default**
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
- **Auto Parallel:** No
- **File System:** ext4
- **System State:** Run level 5 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V10.2

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Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Huawei

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CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Results Table

<table>
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<tr>
<th>Benchmark</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>403.gcc</td>
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<td>207</td>
<td>5320</td>
<td>206</td>
<td>5370</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Turbo mode set with: cpupower -c all frequency-set -g performance

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-ew80 Thu Mar 30 22:03:11 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8891 v4 @ 2.80GHz
@ "physical id"s (chips)
160 "processors"
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Test sponsor: Huawei
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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 10
siblings : 20
physical 0: cores 5 9 10 11 13 18 24 26 28 29
physical 1: cores 5 9 10 11 13 18 24 26 28 29
physical 2: cores 5 9 10 11 13 18 24 26 28 29
physical 3: cores 5 9 10 11 13 18 24 26 28 29
physical 4: cores 5 9 10 11 13 18 24 26 28 29
physical 5: cores 5 9 10 11 13 18 24 26 28 29
physical 6: cores 5 9 10 11 13 18 24 26 28 29
physical 7: cores 5 9 10 11 13 18 24 26 28 29

cache size : 61440 KB

From /proc/meminfo
MemTotal: 1058050152 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Mar 30 22:01

SPEC is set to: /spec
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 1.1T 27G 1.1T 3% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
Continued on next page
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Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 5.11 02/21/2017
Memory:
  64x Micron 36ASF2G72PZ-2G1A2 16 GB 2 rank 2133 MHz, configured at 1600 MHz
  128x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>/proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64

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</table>

**Base Portability Flags (Continued)**

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

**Base Optimization Flags**

C benchmarks:
-xCORE-AVX2 -ipo -03 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -03 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

**Base Other Flags**

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.2-BDW-RevG.20170404.xml

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