**Cisco Systems**

Cisco UCS B200 M5 (Intel Xeon Gold 6152, 2.10GHz)

<table>
<thead>
<tr>
<th>SPECint®2006 =</th>
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<td>SPECint_base2006 =</td>
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</table>

**CPU2006 license:** 9019  
**Test date:** Jan-2010  
**Test sponsor:** Cisco Systems  
**Hardware Availability:** Aug-2017  
**Tested by:** Cisco Systems  
**Software Availability:** Apr-2017

**SPECint_base2006 = 75.4**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Result</th>
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<tr>
<td>400.perlbench</td>
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</tr>
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<tr>
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<td>98.0</td>
</tr>
<tr>
<td>473.astar</td>
<td>77.7</td>
</tr>
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**CPU Name:** Intel Xeon Gold 6152  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
**CPU MHZ:** 2100  
**FPU:** Integrated  
**CPU(s) enabled:** 44 cores, 2 chips, 22 cores/chip  
**CPU(s) orderable:** 1.2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 1 MB I+D on chip per core  
**L3 Cache:** 30.25 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
**Disk Subsystem:** 1 x 300 GB SAS SSD  
**Other Hardware:** None

**Operating System:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
**Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
**Auto Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** Not Applicable  
**Other Software:** Microquill SmartHeap V10.2
## SPEC CINT2006 Result

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>400.perlbench</td>
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<td>46.8</td>
<td>208</td>
<td>47.0</td>
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<td>46.7</td>
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<tr>
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<td>28.4</td>
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<tr>
<td>429.mcf</td>
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<td>77.7</td>
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<tr>
<td>445.gobmk</td>
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<td>37.2</td>
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<td>37.1</td>
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<td>462.libquantum</td>
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<td>464.h264ref</td>
<td>326</td>
<td>67.9</td>
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<td>67.3</td>
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<td>471.omnetpp</td>
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<td>473.astar</td>
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<td>39.4</td>
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<tr>
<td>483.xalancbmk</td>
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<td>82.6</td>
<td>83.4</td>
<td>82.8</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS Settings:
- Intel HyperThreading Technology set to Disabled
- CPU performance set to Enterprise
- Power Performance Tuning set to OS
- SNC set to Disabled
- IMC Interleaving set to Auto
- Patrol Scrub set to Disabled

Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux Wed Jan 6 22:48:46 2010

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz  
2 "physical id"s (chips)  
44 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 22

Continued on next page
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Platform Notes (Continued)

siblings : 22
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
cache size : 30976 KB

From /proc/meminfo
MemTotal: 791029204 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 6 18:27

SPEC is set to: /home/cpu2006-1.2

filesystem Type Size Used Avail Use% Mounted on
/dev/sdal xfs 280G 21G 260G 8% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017
Memory:
24x 0xCE00 M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)
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General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "*/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "44"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
  -auto-p32

C++ benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
  -Wl,-z,muldefs -L/sh10.2 -lsmartheap64
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Base Other Flags

C benchmarks:
403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml