# SPEC® CINT2006 Result



## Cisco Systems

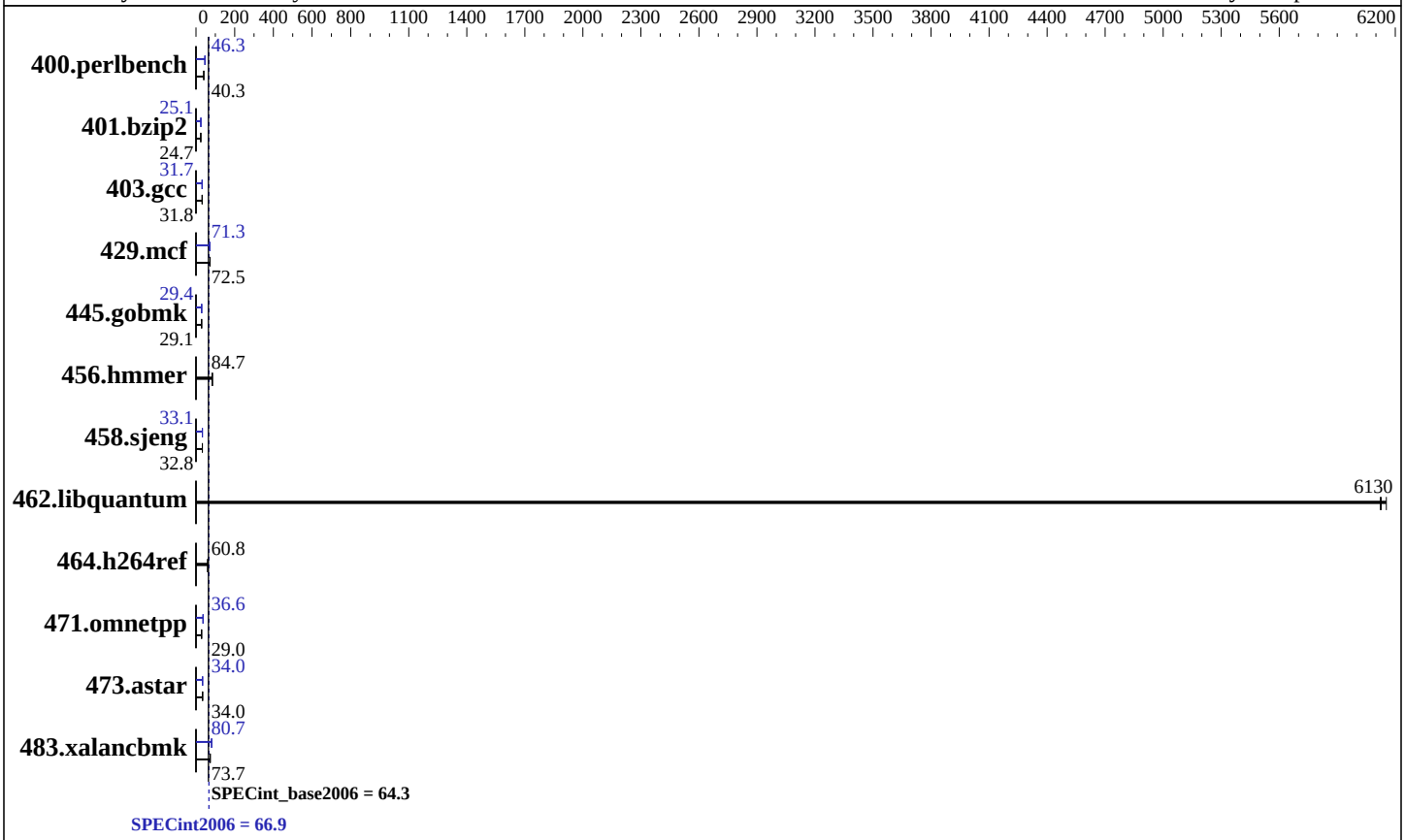
Cisco UCS B200 M5 (Intel Xeon Gold 5118, 2.30GHz)

SPECint®2006 = 66.9

SPECint_base2006 = 64.3

**CPU2006 license:** 9019
**Test sponsor:** Cisco Systems
**Tested by:** Cisco Systems

**Test date:** Sep-2017
**Hardware Availability:** Aug-2017
**Software Availability:** Apr-2017




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Gold 5118 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.20 GHz |
| CPU MHz: | 2300 |
| FPU: | Integrated |
| CPU(s) enabled: | 24 cores, 2 chips, 12 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 1 MB I+D on chip per core |
| L3 Cache: | 16.5 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R, running at 2400 MHz) |
| Disk Subsystem: | 1 x 240 GB M.2 SATA SSD |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.21-69-default |
| Compiler: | C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux |
| Auto Parallel: | Yes |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.2 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 243 | 40.2 | 242 | 40.3 | **243** | **40.3** | **211** | **46.3** | 213 | 46.0 | 211 | 46.3 |
| 401.bzip2 | 389 | 24.8 | **390** | **24.7** | 391 | 24.7 | 384 | 25.1 | **384** | **25.1** | 383 | 25.2 |
| 403.gcc | 253 | 31.8 | **253** | **31.8** | 253 | 31.8 | 257 | 31.3 | 253 | 31.9 | **254** | **31.7** |
| 429.mcf | 126 | 72.1 | **126** | **72.5** | 125 | 73.2 | **128** | **71.3** | 127 | 71.6 | 130 | 70.3 |
| 445.gobmk | 361 | 29.1 | **361** | **29.1** | 361 | 29.1 | 358 | 29.3 | **357** | **29.4** | 357 | 29.4 |
| 456.hmmer | 110 | 84.9 | 111 | 83.9 | **110** | **84.7** | 110 | 84.9 | 111 | 83.9 | **110** | **84.7** |
| 458.sjeng | 369 | 32.8 | **369** | **32.8** | 368 | 32.9 | 366 | 33.1 | **366** | **33.1** | 366 | 33.1 |
| 462.libquantum | 3.38 | 6120 | **3.38** | **6130** | 3.37 | 6150 | 3.38 | 6120 | **3.38** | **6130** | 3.37 | 6150 |
| 464.h264ref | 365 | 60.7 | **364** | **60.8** | 364 | 60.8 | 365 | 60.7 | **364** | **60.8** | 364 | 60.8 |
| 471.omnetpp | **216** | **29.0** | 219 | 28.5 | 214 | 29.2 | **171** | **36.6** | 168 | 37.2 | 175 | 35.7 |
| 473.astar | 206 | 34.1 | 207 | 34.0 | **206** | **34.0** | 206 | 34.0 | **207** | **34.0** | 207 | 34.0 |
| 483.xalancbmk | 93.5 | 73.8 | 94.5 | 73.0 | **93.6** | **73.7** | 85.1 | 81.0 | **85.6** | **80.7** | 85.9 | 80.4 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS Settings:
Intel HyperThreading Technology set to Disabled
CPU performance set to Enterprise
Power Performance Tuning set to OS
SNC set to Disabled
IMC Interleaving set to Auto
Patrol Scrub set to Disabled
 Sysinfo program /home/cpu2006-ic18/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on linux-mys2 Wed Sep 27 11:41:07 2017

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 5118 CPU @ 2.30GHz
       2 "physical id"s (chips)
       24 "processors"
```

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## Platform Notes (Continued)

```
      cores, siblings (Caution: counting these is hw and system dependent.  The
      following excerpts from /proc/cpuinfo might not be reliable.  Use with
      caution.)
         cpu cores : 12
         siblings  : 12
         physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
         physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
      cache size : 16896 KB

   From /proc/meminfo
      MemTotal:       394832636 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      SUSE Linux Enterprise Server 12 SP2

   From /etc/*release* /etc/*version*
      SuSE-release:
         SUSE Linux Enterprise Server 12 (x86_64)
         VERSION = 12
         PATCHLEVEL = 2
         # This file is deprecated and will be removed in a future service pack or
         release.
         # Please check /etc/os-release for details about this release.
      os-release:
         NAME="SLES"
         VERSION="12-SP2"
         VERSION_ID="12.2"
         PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
         ID="sles"
         ANSI_COLOR="0;32"
         CPE_NAME="cpe:/o:suse:sles:12:sp2"

   uname -a:
      Linux linux-mys2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
      (9464f67) x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Sep 25 19:49

   SPEC is set to: /home/cpu2006-ic18
      Filesystem     Type  Size  Used Avail Use% Mounted on
      /dev/sda3      xfs   182G   25G  158G  14% /home
   Additional information from dmidecode:

      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

     BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017
     Memory:
```

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## Platform Notes (Continued)

```
      24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz, configured at 2400 MHz

    (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/intel/compilers_and_libraries_2018.0.082/linux/compiler/lib/ia32:/home/intel/compilers_and_libraries_2018.0.082/linux/compiler/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "24"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

## Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
      401.bzip2: -DSPEC_CPU_LP64
        403.gcc: -DSPEC_CPU_LP64
        429.mcf: -DSPEC_CPU_LP64
      445.gobmk: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
      473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32
```

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## Base Optimization Flags (Continued)

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/home/cpu2006-1.2/sh10.2 -lsmartheap64`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

445.gobmk: `icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

C++ benchmarks (except as noted below):
`icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-qopt-prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div -auto-ilp32 -qopt-prefetch`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -qopt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)

456.hmmer: `basepeak = yes`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xCORE-AVX2`(pass 2) `-par-num-threads=1`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-qopt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cpu2006-1.2/sh10.2 -lsmartheap`

473.astar: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32 -Wl,-z,muldefs -L/home/cpu2006-1.2/sh10.2 -lsmartheap64`

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -Wl,-z,muldefs -L/home/cpu2006-1.2/sh10.2 -lsmartheap`

## Peak Other Flags

C benchmarks:

Continued on next page

## Peak Other Flags (Continued)

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml`
`http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml`



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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