Cisco Systems
Cisco UCS B200 M5 (Intel Xeon Gold 6154, 3.00GHz)

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Hardware
CPU Name: Intel Xeon Gold 6154
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core

Software
Operating System: SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.21-69-default
Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)

SPECfp®2006 = 157
SPECfp_base2006 = 151

410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.lelie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFDTD
465.tonto
470.lbm
481.wrf
482.sphinx3

SPECfp_base2006 = 151
## SPEC CFP2006 Result

### Cisco Systems

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<table>
<thead>
<tr>
<th>CPU2006 license:</th>
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<tr>
<td>Tested by:</td>
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</tr>
</tbody>
</table>

| L3 Cache: | 24.75 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R) |
| Disk Subsystem: | 1 x 240 GB M.2 SATA SSD |
| Other Hardware: | None |

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>12.7</td>
<td>1070</td>
<td>12.7</td>
<td>1070</td>
<td>12.7</td>
<td>1070</td>
<td>12.7</td>
<td>1070</td>
<td>12.7</td>
<td>1070</td>
<td>12.7</td>
<td>1070</td>
</tr>
<tr>
<td>416.gamess</td>
<td>397</td>
<td>49.4</td>
<td>396</td>
<td>49.4</td>
<td>397</td>
<td>49.3</td>
<td>378</td>
<td>51.8</td>
<td>378</td>
<td>51.8</td>
<td>379</td>
<td>51.7</td>
</tr>
<tr>
<td>433.milc</td>
<td>118</td>
<td>77.8</td>
<td>117</td>
<td>78.4</td>
<td>117</td>
<td>78.4</td>
<td>118</td>
<td>77.8</td>
<td>117</td>
<td>78.4</td>
<td>117</td>
<td>78.4</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>32.8</td>
<td>278</td>
<td>32.9</td>
<td>277</td>
<td>33.0</td>
<td>276</td>
<td>32.8</td>
<td>278</td>
<td>32.9</td>
<td>277</td>
<td>33.0</td>
<td>276</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>113</td>
<td>63.4</td>
<td>113</td>
<td>63.2</td>
<td>113</td>
<td>63.0</td>
<td>113</td>
<td>63.4</td>
<td>113</td>
<td>63.2</td>
<td>113</td>
<td>63.0</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>8.04</td>
<td>1490</td>
<td>7.92</td>
<td>1510</td>
<td>8.06</td>
<td>1480</td>
<td>8.04</td>
<td>1490</td>
<td>7.92</td>
<td>1510</td>
<td>8.06</td>
<td>1480</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>18.5</td>
<td>508</td>
<td>18.5</td>
<td>509</td>
<td>18.6</td>
<td>506</td>
<td>18.5</td>
<td>508</td>
<td>18.5</td>
<td>509</td>
<td>18.6</td>
<td>506</td>
</tr>
<tr>
<td>444.namd</td>
<td>226</td>
<td>35.5</td>
<td>226</td>
<td>35.5</td>
<td>226</td>
<td>35.5</td>
<td>220</td>
<td>36.5</td>
<td>219</td>
<td>36.5</td>
<td>219</td>
<td>36.5</td>
</tr>
<tr>
<td>447.dealII</td>
<td>158</td>
<td>72.5</td>
<td>157</td>
<td>73.0</td>
<td>158</td>
<td>72.3</td>
<td>158</td>
<td>72.5</td>
<td>157</td>
<td>73.0</td>
<td>158</td>
<td>72.3</td>
</tr>
<tr>
<td>450.soplex</td>
<td>163</td>
<td>51.3</td>
<td>164</td>
<td>51.0</td>
<td>163</td>
<td>51.3</td>
<td>163</td>
<td>51.3</td>
<td>164</td>
<td>51.0</td>
<td>163</td>
<td>51.3</td>
</tr>
<tr>
<td>453.povray</td>
<td>76.3</td>
<td>69.8</td>
<td>76.4</td>
<td>69.7</td>
<td>76.2</td>
<td>69.8</td>
<td>67.4</td>
<td>78.9</td>
<td>67.5</td>
<td>78.8</td>
<td>67.4</td>
<td>78.9</td>
</tr>
<tr>
<td>454.calculix</td>
<td>109</td>
<td>75.6</td>
<td>109</td>
<td>75.3</td>
<td>110</td>
<td>75.1</td>
<td>107</td>
<td>76.7</td>
<td>108</td>
<td>76.6</td>
<td>108</td>
<td>76.5</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>35.5</td>
<td>299</td>
<td>34.8</td>
<td>305</td>
<td>34.9</td>
<td>304</td>
<td>29.0</td>
<td>365</td>
<td>29.0</td>
<td>366</td>
<td>29.0</td>
<td>366</td>
</tr>
<tr>
<td>465.tonto</td>
<td>180</td>
<td>54.8</td>
<td>180</td>
<td>54.8</td>
<td>182</td>
<td>54.2</td>
<td>146</td>
<td>67.6</td>
<td>145</td>
<td>67.7</td>
<td>145</td>
<td>67.6</td>
</tr>
<tr>
<td>470.lbm</td>
<td>9.11</td>
<td>1510</td>
<td>9.16</td>
<td>1500</td>
<td>9.15</td>
<td>1500</td>
<td>9.13</td>
<td>1500</td>
<td>9.15</td>
<td>1500</td>
<td>9.15</td>
<td>1500</td>
</tr>
<tr>
<td>481.wrf</td>
<td>76.9</td>
<td>145</td>
<td>77.1</td>
<td>145</td>
<td>77.0</td>
<td>145</td>
<td>76.9</td>
<td>145</td>
<td>77.1</td>
<td>145</td>
<td>77.0</td>
<td>145</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>238</td>
<td>81.8</td>
<td>240</td>
<td>81.3</td>
<td>240</td>
<td>81.4</td>
<td>238</td>
<td>81.8</td>
<td>240</td>
<td>81.3</td>
<td>240</td>
<td>81.4</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS Settings:
- Intel HyperThreading Technology set to Disabled
- CPU performance set to Enterprise
- Power Performance Tuning set to OS
- SNC set to Disabled
- IMC Interleaving set to Auto
- Patrol Scrub set to Disabled

Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-3u87 Wed Sep 27 07:19:00 2017
Continued on next page
Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6154 CPU @ 3.00GHz
  2 "physical id"s (chips)
  36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 18
siblings : 18
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 25344 KB

From /proc/meminfo
MemTotal: 394832580 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or
  release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
  Linux linux-3u87 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
    (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 9 20:37

SPEC is set to: /opt/cpu2006-1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb1 xfs 224G 37G 187G 17% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
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Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017
Memory:
24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/opt/intel/compilers_and_libraries_2018.0.128/linux/compiler/lib/ia32:/opt/intel/compilers_and_libraries_2018.0.128/linux/compiler/lib/intel64:/opt/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "36"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64

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Cisco Systems  
Cisco UCS B200 M5 (Intel Xeon Gold 6154, 3.00GHz)  

<table>
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<tr>
<th>Specification</th>
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**CPU2006 license:** 9019  
**Test date:** Sep-2017  
**Test sponsor:** Cisco Systems  
**Tested by:** Cisco Systems  
**Hardware Availability:** Aug-2017  
**Software Availability:** Apr-2017

### Base Portability Flags (Continued)

- dealII: DSPEC_CPU_LP64
- soplex: DSPEC_CPU_LP64
- povray: DSPEC_CPU_LP64
- calculix: DSPEC_CPU_LP64
- GemFDTD: DSPEC_CPU_LP64
- tonto: DSPEC_CPU_LP64
- lbm: DSPEC_CPU_LP64
- wrf: DSPEC_CPU_LP64
- sphinx3: DSPEC_CPU_LP64
- dealII: DSPEC_CPU_CASE_FLAG
- dealII: DSPEC_CPU_LINUX

### Base Optimization Flags

- **C benchmarks:**
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
- **C++ benchmarks:**
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
- **Fortran benchmarks:**
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
- **Benchmarks using both Fortran and C:**
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

### Peak Compiler Invocation

- **C benchmarks:**
  icc -m64
- **C++ benchmarks:**
  icpc -m64
- **Fortran benchmarks:**
  ifort -m64
- **Benchmarks using both Fortran and C:**
  icc -m64 ifort -m64

### Peak Portability Flags

Same as Base Portability Flags
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Tested by: Cisco Systems

Test date: Sep-2017
Hardware Availability: Aug-2017
Software Availability: Apr-2017

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -fno-alias -auto-llp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
               -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -unroll2 -inline-level=0 -qopt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
           -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
           -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes

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Hardware Availability: Aug-2017
Software Availability: Apr-2017

Peak Optimization Flags (Continued)

454.calculix:
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -auto-ilp32

481.wrf:
basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml