**SPEC® CINT2006 Result**

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL580 Gen10
(2.10 GHz, Intel Xeon Gold 6130)

**Software**

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP3
Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V10.2

**Hardware**

CPU Name: Intel Xeon Gold 6130
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz: 2100
FPU: Integrated
CPU(s) enabled: 64 cores, 4 chips, 16 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2, 4 chip(s)
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 22 MB I+D on chip per chip
Other Cache: None
Memory: 1 TB (16 x 64 GB 4Rx4 PC4-2666V-L)
Disk Subsystem: 1 x 600 GB SSD SATA, RAID 0
Other Hardware: None

**SPECint®_rate2006 = Not Run**

**SPECint_rate_base2006 = 2760**

Test date: Oct-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

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CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration:
    Thermal Configuration set to Maximum Cooling
    Memory Patrol Scrubbing set to Disabled
    LLC Prefetcher set to Enabled
    LLC Dead Line Allocation set to Disabled
    Stale A to S set to Enabled
    Workload Profile set to Throughput Frequency Compute
    Minimum Processor Idle Power Core C-State set to C1E State

Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-0nki Fri Oct 27 06:18:23 2017

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
  4 "physical id"s (chips)
  128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 16
siblings : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB

From /proc/meminfo
MemTotal:       1056517240 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or
release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
Linux linux-0nki 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017
(b7ce4e4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 27 06:15
SPEC is set to: /home/cpu2006

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Platform Notes (Continued)

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<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
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</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>517G</td>
<td>92G</td>
<td>426G</td>
<td>18%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 08/18/2017
Memory:
32x UNKNOWN NOTAVAILABLE
16x UNKNOWN NOTAVAILABLE 64 GB 4 rank 2666 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 1 TB and the dmidecode description should have one line reading as:
16x UNKNOWN NOTAVAILABLE 64 GB 4 rank 2666

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64

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**Base Portability Flags (Continued)**

<table>
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<tr>
<th>Benchmark</th>
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<tbody>
<tr>
<td>471.omnetpp</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>473.astar</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX</td>
</tr>
</tbody>
</table>

**Base Optimization Flags**

**C benchmarks**:
- -xCORE-AVX512
- -ipo
- -O3
- -no-prec-div
- -qopt-prefetch
- -qopt-mem-layout-trans=3

**C++ benchmarks**:
- -xCORE-AVX512
- -ipo
- -O3
- -no-prec-div
- -qopt-prefetch
- -qopt-mem-layout-trans=3
- -Wl,-z,muldefs
- -L/sh10.2 -lsmartheap

**Base Other Flags**

**C benchmarks**:
- 403.gcc: -Dalloca=alloca

The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html](http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html)

You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml](http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml)