## SPEC® CINT2006 Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
**ProLiant DL560 Gen10**  
(2.60 GHz, Intel Xeon Gold 6142)

### CPU2006 license: 3  
Test sponsor: HPE  
Tested by: HPE

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECint_rate_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>2000</td>
<td>3260</td>
</tr>
<tr>
<td>2440</td>
<td>3260</td>
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<tr>
<td>2440</td>
<td>3260</td>
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</tbody>
</table>

**SPECint_rate2006 = Not Run**  
**SPECint_rate_base2006 = 3260**

### Hardware

- **CPU Name:** Intel Xeon Gold 6142  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
- **CPU MHz:** 2600  
- **FPU:** Integrated  
- **CPU(s) enabled:** 64 cores, 4 chips, 16 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1, 2, 4 chip(s)  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 1 MB I+D on chip per core  
- **L3 Cache:** None  
- **Other Cache:** None  
- **Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
- **Disk Subsystem:** 2 x 480 GB SATA SSD, RAID 0  
- **Other Hardware:** None

### Software

- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP2  
- **Kernel:** 4.4.21-69-default  
- **Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
- **Auto Parallel:** No  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 32/64-bit  
- **Peak Pointers:** Not Applicable  
- **Other Software:** Microquill SmartHeap V10.2

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**Test date:** Nov-2017  
**Hardware Availability:** Oct-2017  
**Software Availability:** Apr-2017
## Hewlett Packard Enterprise

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>128</td>
<td>513</td>
<td>2440</td>
<td>514</td>
<td>2430</td>
<td>513</td>
<td>2440</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>128</td>
<td>871</td>
<td>1420</td>
<td>866</td>
<td>1430</td>
<td>866</td>
<td>1430</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>403.gcc</td>
<td>128</td>
<td>443</td>
<td>2330</td>
<td>442</td>
<td>2330</td>
<td>446</td>
<td>2310</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>128</td>
<td>272</td>
<td>4280</td>
<td>272</td>
<td>4290</td>
<td>273</td>
<td>4280</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>128</td>
<td>658</td>
<td>2040</td>
<td>658</td>
<td>2040</td>
<td>659</td>
<td>2040</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>456.hmmer</td>
<td>128</td>
<td>267</td>
<td>4470</td>
<td>265</td>
<td>4500</td>
<td>268</td>
<td>4460</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>458.sjeng</td>
<td>128</td>
<td>713</td>
<td>2170</td>
<td>712</td>
<td>2180</td>
<td>713</td>
<td>2170</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>128</td>
<td>48.7</td>
<td>54500</td>
<td>48.6</td>
<td>54600</td>
<td>48.8</td>
<td>54400</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>128</td>
<td>771</td>
<td>3680</td>
<td>769</td>
<td>3680</td>
<td>767</td>
<td>3690</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>128</td>
<td>500</td>
<td>1600</td>
<td>499</td>
<td>1600</td>
<td>500</td>
<td>1600</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>128</td>
<td>494</td>
<td>1820</td>
<td>494</td>
<td>1820</td>
<td>495</td>
<td>1820</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>128</td>
<td>242</td>
<td>3650</td>
<td>241</td>
<td>3660</td>
<td>242</td>
<td>3660</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
irqbalance disabled with "service irqbalance stop"  
tuned profile set with "tuned-adm profile throughput-performance"  
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"  
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

### Platform Notes

BIOS Configuration:  
 Thermal Configuration set to Maximum Cooling  
 LLC Prefetch set to Enabled  
 LLC Dead Line Allocation set to Disabled  
 Stale A to S set to Enabled  
 Memory Patrol Scrubbing set to Disabled  
 Workload Profile set to General Throughput Compute  
 Minimum Processor Idle Power Core C-State set to C1E State  
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Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(2.60 GHz, Intel Xeon Gold 6142)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 3260

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Nov-2017
Hardware Availability: Oct-2017
Software Availability: Apr-2017

Platform Notes (Continued)

Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-smfo Thu Nov 30 10:46:22 2017

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6142 CPU @ 2.60GHz
4 "physical id"s (chips)
128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB

From /proc/meminfo
MemTotal: 792275632 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release*/etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

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Platform Notes (Continued)

run-level 3 Nov 30 10:44

SPEC is set to: /home/cpu2006

Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   852G  165G  687G  20% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017
Memory:
48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(Base of data from sysinfo program)

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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

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Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

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Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64

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SpecINT rate

Test date: Nov-2017
Hardware Availability: Oct-2017
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Base Portability Flags (Continued)

473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

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