Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL360 Gen10
(2.80 GHz, Intel Xeon Gold 6143)

SPECint\textsuperscript{®}2006 = \textbf{Not Run}
SPECint\textsubscript{base2006} = \textbf{74.6}

\begin{tabular}{|l|l|}
\hline
\textbf{CPU2006 license} & 3 \\
\textbf{Test sponsor} & HPE \\
\textbf{Tested by} & HPE \\
\hline
\end{tabular}

\begin{tabular}{|l|l|}
\hline
\textbf{Test date} & Dec-2017 \\
\textbf{Hardware Availability} & Dec-2017 \\
\textbf{Software Availability} & Apr-2017 \\
\hline
\end{tabular}

\begin{tabular}{|l|l|}
\hline
\textbf{CPU Name} & Intel Xeon Gold 6143 \\
\textbf{CPU Characteristics} & Intel Turbo Boost Technology up to 4.00 GHz \\
\textbf{CPU MHz} & 2800 \\
\textbf{FPU} & Integrated \\
\textbf{CPU(s) enabled} & 32 cores, 2 chips, 16 cores/chip \\
\textbf{CPU(s) orderable} & 1, 2 chip(s) \\
\textbf{Primary Cache} & 32 KB I + 32 KB D on chip per core \\
\textbf{Secondary Cache} & 1 MB I+D on chip per core \\
\textbf{L3 Cache} & 22 MB I+D on chip per chip \\
\textbf{Other Cache} & None \\
\textbf{Memory} & 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R) \\
\textbf{Disk Subsystem} & 1 x 480 GB SATA SSD, RAID 0 \\
\textbf{Other Hardware} & None \\
\hline
\end{tabular}

\begin{tabular}{|l|l|}
\hline
\textbf{Hardware} & \textbf{Software} \\
\textbf{Operating System} & Red Hat Enterprise Linux Server release 7.3 (Maipo) \\
\textbf{Compiler} & C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux \\
\textbf{Auto Parallel} & Yes \\
\textbf{File System} & xfs \\
\textbf{System State} & Run level 3 (multi-user) \\
\textbf{Base Pointers} & 32/64-bit \\
\textbf{Peak Pointers} & Not Applicable \\
\textbf{Other Software} & Microquill SmartHeap V10.2 \\
\hline
\end{tabular}

\begin{itemize}
\item 400.perlbench
\item 401.bzip2
\item 403.gcc
\item 429.mcf
\item 445.gobmk
\item 456.hmmer
\item 458.sjeng
\item 462.libquantum
\item 464.h264ref
\item 471.omnetpp
\item 473.astar
\item 483.xalancbmk
\end{itemize}

\begin{itemize}
\item SPECint\textsubscript{base2006} = 74.6
\end{itemize}
Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
IRQ balance service was stop using "service irqbalance stop"
Tuned-adm profile was set to Throughtput-Performance

Platform Notes

BIOS Configuration:
Intel Hyperthreading set to Disabled
Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Peak Frequency Compute
Energy/Performance Bias set to Maximum Performance
Workload Profile set to Custom
NUMA Group Size Optimization set to Flat
Sysinfo program /home/specuser/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on dl360Gen10rhel73Unit2 Fri Dec 8 02:12:50 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/docs/config.html#sysinfo

Continued on next page
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ProLiant DL360 Gen10
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SPECint2006 = Not Run
SPECint_base2006 = 74.6

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Dec-2017
Hardware Availability: Dec-2017
Software Availability: Apr-2017

Platform Notes (Continued)

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6143 CPU @ 2.80GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB

From /proc/meminfo
MemTotal: 197572720 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
Linux dl360Gen10rhel73Unit2 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13
EDT 2016 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Dec 7 14:21

SPEC is set to: /home/specuser/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 392G 36G 357G 10% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U32 09/29/2017
Memory:
  24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz

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SPEC CINT2006 Result

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CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/specuser/cpu2006/lib/ia32:/home/specuser/cpu2006/lib/intel64:/home/specuser/cpu2006/sh10.2"
OMP_NUM_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
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456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch -auto-p32

C++ benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
  -Wl,-z,muldefs -L/sh10.2 -lsmartheap64
### SPEC CINT2006 Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant DL360 Gen10  
(2.80 GHz, Intel Xeon Gold 6143)

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- **CPU2006 license:** 3  
- **Test sponsor:** HPE  
- **Tested by:** HPE

### Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html](http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml](http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml)

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For other inquiries, please contact webmaster@spec.org.

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