Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
Synergy 480 Gen10  
(2.20 GHz, Intel Xeon Silver 4114)  

**SPECfp®_rate2006 = Not Run**  
**SPECfp_rate_base2006 = 820**

<table>
<thead>
<tr>
<th>Test date:</th>
<th>Nov-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Oct-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Apr-2017</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECfp_rate_base2006</th>
<th>820</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECfp_rate</th>
<th>SPECfp_rate_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>40</td>
<td>747</td>
</tr>
<tr>
<td>416.gamess</td>
<td>40</td>
<td>732</td>
</tr>
<tr>
<td>433.milc</td>
<td>40</td>
<td>773</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>40</td>
<td></td>
</tr>
<tr>
<td>435.gromacs</td>
<td>40</td>
<td>1050</td>
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<tr>
<td>436.cactusADM</td>
<td>40</td>
<td>1080</td>
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<tr>
<td>437.leslie3d</td>
<td>40</td>
<td>564</td>
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<td>444.namd</td>
<td>40</td>
<td>596</td>
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<td>40</td>
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<td>40</td>
<td>1060</td>
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<tr>
<td>453.povray</td>
<td>40</td>
<td>1120</td>
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<tr>
<td>454.calculix</td>
<td>40</td>
<td></td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>40</td>
<td>524</td>
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<tr>
<td>465.tonto</td>
<td>40</td>
<td>816</td>
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<tr>
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<td>40</td>
<td>1040</td>
</tr>
<tr>
<td>481.wrf</td>
<td>40</td>
<td>964</td>
</tr>
<tr>
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<td>40</td>
<td>725</td>
</tr>
</tbody>
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**Hardware**

- **CPU Name:** Intel Xeon Silver 4114
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.00 GHz
- **CPU MHz:** 2200
- **FPU:** Integrated
- **CPU(s) enabled:** 20 cores, 2 chips, 10 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1, 2 chip(s)
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 1 MB I+D on chip per core

**Software**

- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP2 Kernel 4.4.21-69-default
- **Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
- **Auto Parallel:** No
- **File System:** xfs
- **System State:** Run level 3 (multi-user)

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Continued on next page
## SPEC CFP2006 Result

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**CPU2006 license:** 3  
**Test sponsor:** HPE  
**Tested by:** HPE  

- **L3 Cache:** 13.75 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Disk Subsystem:** 1 x 480 GB SATA SSD, RAID 0  
- **Other Hardware:** None  
- **Base Pointers:** 32/64-bit  
- **Peak Pointers:** Not Applicable  
- **Other Software:** None  

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Base Copies</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Copies</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>40</td>
<td>728</td>
<td>747</td>
<td>727</td>
<td>747</td>
<td>728</td>
<td>747</td>
<td></td>
<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>416.gamess</td>
<td>40</td>
<td>1070</td>
<td>732</td>
<td>1071</td>
<td>732</td>
<td>1071</td>
<td>732</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>433.milc</td>
<td>40</td>
<td>475</td>
<td>773</td>
<td>475</td>
<td>773</td>
<td>475</td>
<td>773</td>
<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>40</td>
<td>348</td>
<td>1050</td>
<td>348</td>
<td>1050</td>
<td>346</td>
<td>1050</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>435.gromacs</td>
<td>40</td>
<td>316</td>
<td>904</td>
<td>323</td>
<td>884</td>
<td>324</td>
<td>883</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>40</td>
<td>441</td>
<td>1080</td>
<td>443</td>
<td>1080</td>
<td>444</td>
<td>1080</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>40</td>
<td>667</td>
<td>564</td>
<td>667</td>
<td>564</td>
<td>668</td>
<td>563</td>
<td></td>
<td></td>
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<td></td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>444.namd</td>
<td>40</td>
<td>537</td>
<td>598</td>
<td>539</td>
<td>595</td>
<td>538</td>
<td>596</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>447.dealII</td>
<td>40</td>
<td>377</td>
<td>1220</td>
<td>377</td>
<td>1210</td>
<td>378</td>
<td>1210</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>450.soplex</td>
<td>40</td>
<td>620</td>
<td>538</td>
<td>618</td>
<td>539</td>
<td>619</td>
<td>539</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>453.povray</td>
<td>40</td>
<td>201</td>
<td>1060</td>
<td>198</td>
<td>1070</td>
<td>201</td>
<td>1060</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>454.calculix</td>
<td>40</td>
<td>291</td>
<td>1130</td>
<td>295</td>
<td>1120</td>
<td>294</td>
<td>1120</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>40</td>
<td>810</td>
<td>524</td>
<td>810</td>
<td>524</td>
<td>810</td>
<td>524</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
<td></td>
</tr>
<tr>
<td>465.tonto</td>
<td>40</td>
<td>482</td>
<td>816</td>
<td>482</td>
<td>817</td>
<td>482</td>
<td>816</td>
<td></td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>470.lbm</td>
<td>40</td>
<td>531</td>
<td>1040</td>
<td>531</td>
<td>1030</td>
<td>531</td>
<td>1040</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>481.wrf</td>
<td>40</td>
<td>464</td>
<td>962</td>
<td>464</td>
<td>964</td>
<td>464</td>
<td>964</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>40</td>
<td>1075</td>
<td>725</td>
<td>1075</td>
<td>725</td>
<td>1072</td>
<td>727</td>
<td></td>
<td></td>
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<td></td>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

- Stack size set to unlimited using "ulimit -s unlimited"  
- Transparent Huge Pages enabled by default  
- Filesystem page cache cleared with:  
  - shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run  
  - runspec command invoked through numactl i.e.:  
  - numactl --interleave=all runspec <etc>  
- Irqbalance disabled with "service irqbalance stop"

Continued on next page
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CPU2006 license: 3
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Operating System Notes (Continued)

- tuned profile set with "tuned-adm profile throughput-performance"
- VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
- Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:
- Thermal Configuration set to Maximum Cooling
- LLC Prefetch set to Enabled
- LLC Dead Line Allocation set to Disabled
- Memory Patrol Scrubbing set to Disabled
- Workload Profile set to General Throughput Compute
- Minimum Processor Idle Power Core C-State set to C1E State
- Workload Profile set to Custom
- Sub-NUMA Clustering set to Disabled
- Sysinfo program /home/cpu2006/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on sy480_hjp_suse_machine1 Tue Nov 21 18:14:36 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
- 2 "physical id"s (chips)
- 40 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 10
  - siblings : 20
  - physical 0: cores 0 1 2 3 4 8 9 10 11 12
  - physical 1: cores 0 1 2 3 4 8 9 10 11 12
- cache size : 14080 KB

From /proc/meminfo
- MemTotal: 395928228 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- SuSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
- os-release:
  - NAME="SLES"

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Tested by: HPE

Platform Notes (Continued)

VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux sy480_hjp_suse_machine1 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20
UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 21 18:13
SPEC is set to: /home/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 405G 65G 341G 16% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 09/27/2017
Memory:
24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH="/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:
   icc -m64

C++ benchmarks:
   icpc -m64

Fortran benchmarks:
   ifort -m64

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Test date: Nov-2017
Hardware Availability: Oct-2017
Software Availability: Apr-2017

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
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450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html
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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

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