**SPEC® CINT2006 Result**

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
**Synergy 480 Gen10**  
(2.00 GHz, Intel Xeon Platinum 8164)

| Test Date: | Nov-2017 |
| Hardware Availability: | Oct-2017 |
| Software Availability: | Sep-2017 |

**SPECint®_rate2006 = Not Run**  
**SPECint_rate_base2006 = 2190**

| CPU2006 license: 3 | Test sponsor: HPE |
| Tested by: HPE |

| Copies | 0 | 1500 | 3500 | 5500 | 7500 | 9500 | 11500 | 13500 | 15500 | 17500 | 19500 | 21500 | 23500 | 25500 | 27500 | 29500 | 31500 | 33500 | 35500 | 40000 |
| 400.perlbench | 104 | 1620 |
| 401.bzip2 | 104 | 968 |
| 403.gcc | 104 | 1540 |
| 429.mcf | 104 | 2850 |
| 445.gobmk | 104 | 1440 |
| 456.hmmer | 104 | 2940 |
| 458.sjeng | 104 | 1470 |
| 462.libquantum | 104 | 2220 |
| 464.h264ref | 104 | 39700 |
| 471.omnetpp | 104 | 1080 |
| 473.astar | 104 | 1160 |
| 483.xalancbmk | 104 | 2280 |

| Hardware | Software |
| CPU Name: Intel Xeon Platinum 8164 | Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2 |
| CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz | Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux |
| CPU MHz: 2000 | Auto Parallel: No |
| FPU: Integrated | File System: xfs |
| CPU(s) enabled: 52 cores, 2 chips, 26 cores/chip, 2 threads/core | System State: Run level 3 (multi-user) |
| CPU(s) orderable: 1, 2 chip(s) | Base Pointers: 32-bit |
| Primary Cache: 32 KB I + 32 KB D on chip per core | Peak Pointers: Not Applicable |
| Secondary Cache: 1 MB I+D on chip per core | Other Software: Microquill SmartHeap V10.2 |
| L3 Cache: 35.75 MB I+D on chip per chip | |
| Other Cache: None | |
| Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R) | |
| Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0 | |
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run  
runcspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>  
irqbalance disabled with "service irqbalance stop"  
tuned profile set with "tuned-adm profile throughput-performance"  
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"  
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:  
Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Memory Patrol Scrubbing set to Disabled  
Workload Profile set to General Throughput Compute  
Minimum Processor Idle Power Core C-State set to C1E State  
Sysinfo program /home/cpu2006/config/sysinfo.rev6993

Continued on next page
Platform Notes (Continued)

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1) running on sy480_hjp_suse Fri Nov 24 19:22:18 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHz
- 2 "physical id"s (chips)
- 104 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 26
  - siblings: 52
  - physical 0: cores 0 1 2 3 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
  - physical 1: cores 0 1 2 3 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
- cache size: 36608 KB

From /proc/meminfo
- MemTotal: 395918220 KB
- HugePages_Total: 0
- Hugepagesize: 2048 KB

From /etc/*release*/etc/*version*
- SUSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 2
  - # This file is deprecated and will be removed in a future service pack or release.
  - # Please check /etc/os-release for details about this release.
- os-release:
  - NAME="SLES"
  - VERSION="12-SP2"
  - VERSION_ID="12.2"
  - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  - ID=sles
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
- (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 24 19:21

SPEC is set to: /home/cpu2006
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Platform Notes (Continued)
/dev/sda3 xfs 407G 113G 294G 28% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 09/27/2017
Memory:
24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation
C benchmarks:
  icc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32
C++ benchmarks:
  icpc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32

Base Portability Flags
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSETBITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs
-L/home/cpu2006/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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