Cisco Systems
Cisco UCS C220 M5 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp®2006 = 138
SPECfp_base2006 = 135

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Test date: Sep-2017
Hardware Availability: Aug-2017
Software Availability: Jun-2017

Hardware

<table>
<thead>
<tr>
<th>SPECfp_base2006 = 135</th>
</tr>
</thead>
</table>

Software

| SPECfp2006 = 138 |

Hardware

- **CPU Name:** Intel Xeon Gold 6128
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz
- **CPU MHz:** 3400
- **FPU:** Integrated
- **CPU(s) enabled:** 12 cores, 2 chips, 6 cores/chip
- **CPU(s) orderable:** 1,2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 1 MB I+D on chip per core

Software

- **Operating System:** SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.21-69-default
- **Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
- **Auto Parallel:** Yes
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
SPEC CFP2006 Result

Cisco Systems
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SPECfp2006 = 138
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L3 Cache: 19.25 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R)
Disk Subsystem: 1 x 600 GB SAS HDD, 10K RPM
Other Hardware: None
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>17.9</td>
<td>761</td>
<td>17.8</td>
<td>765</td>
<td>17.7</td>
<td>767</td>
<td>17.9</td>
<td>761</td>
<td>17.8</td>
<td>765</td>
</tr>
<tr>
<td>416.gamess</td>
<td>399</td>
<td>49.0</td>
<td>399</td>
<td>49.0</td>
<td>399</td>
<td>49.0</td>
<td>380</td>
<td>51.5</td>
<td>381</td>
<td>51.4</td>
</tr>
<tr>
<td>433.milc</td>
<td>118</td>
<td>77.6</td>
<td>120</td>
<td>76.4</td>
<td>118</td>
<td>77.8</td>
<td>118</td>
<td>77.6</td>
<td>120</td>
<td>76.4</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>36.4</td>
<td>250</td>
<td>36.6</td>
<td>249</td>
<td>36.5</td>
<td>249</td>
<td>36.4</td>
<td>250</td>
<td>36.6</td>
<td>249</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>104</td>
<td>68.6</td>
<td>103</td>
<td>69.1</td>
<td>106</td>
<td>67.6</td>
<td>104</td>
<td>68.6</td>
<td>103</td>
<td>69.1</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>11.6</td>
<td>1030</td>
<td>11.9</td>
<td>1000</td>
<td>12.0</td>
<td>999</td>
<td>11.6</td>
<td>1030</td>
<td>11.9</td>
<td>1000</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>23.7</td>
<td>396</td>
<td>23.8</td>
<td>395</td>
<td>23.9</td>
<td>393</td>
<td>23.7</td>
<td>396</td>
<td>23.8</td>
<td>395</td>
</tr>
<tr>
<td>444.namd</td>
<td>225</td>
<td>35.6</td>
<td>225</td>
<td>35.6</td>
<td>225</td>
<td>35.6</td>
<td>220</td>
<td>36.4</td>
<td>220</td>
<td>36.5</td>
</tr>
<tr>
<td>447.dealII</td>
<td>159</td>
<td>72.2</td>
<td>159</td>
<td>72.0</td>
<td>159</td>
<td>71.8</td>
<td>159</td>
<td>72.2</td>
<td>159</td>
<td>72.0</td>
</tr>
<tr>
<td>450.soplex</td>
<td>168</td>
<td>49.5</td>
<td>169</td>
<td>49.2</td>
<td>171</td>
<td>48.8</td>
<td>168</td>
<td>49.5</td>
<td>169</td>
<td>49.2</td>
</tr>
<tr>
<td>453.povray</td>
<td>76.5</td>
<td>69.5</td>
<td>76.5</td>
<td>69.6</td>
<td>76.6</td>
<td>69.4</td>
<td>67.4</td>
<td>79.0</td>
<td>67.5</td>
<td>78.9</td>
</tr>
<tr>
<td>454.calculix</td>
<td>108</td>
<td>76.6</td>
<td>108</td>
<td>76.5</td>
<td>108</td>
<td>76.4</td>
<td>108</td>
<td>76.6</td>
<td>108</td>
<td>76.6</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>42.3</td>
<td>251</td>
<td>42.7</td>
<td>248</td>
<td>42.7</td>
<td>249</td>
<td>37.4</td>
<td>283</td>
<td>37.5</td>
<td>283</td>
</tr>
<tr>
<td>465.tonto</td>
<td>160</td>
<td>61.4</td>
<td>160</td>
<td>61.3</td>
<td>160</td>
<td>61.6</td>
<td>148</td>
<td>66.4</td>
<td>148</td>
<td>66.3</td>
</tr>
<tr>
<td>470.lbm</td>
<td>15.4</td>
<td>891</td>
<td>15.5</td>
<td>888</td>
<td>15.4</td>
<td>891</td>
<td>15.4</td>
<td>891</td>
<td>15.5</td>
<td>888</td>
</tr>
<tr>
<td>481.wrf</td>
<td>106</td>
<td>105</td>
<td>106</td>
<td>107</td>
<td>105</td>
<td>106</td>
<td>106</td>
<td>106</td>
<td>105</td>
<td>106</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>230</td>
<td>84.9</td>
<td>229</td>
<td>85.0</td>
<td>229</td>
<td>85.1</td>
<td>230</td>
<td>84.9</td>
<td>229</td>
<td>85.0</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes
BIOS Settings:
Intel HyperThreading Technology set to Disabled
CPU performance set to Enterprise
Power Performance Tuning set to OS Controls
SNC set to Disabled
Patrol Scrub set to Disabled
Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-3joc Thu Sep 7 04:02:30 2017

Continued on next page
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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6128 CPU @ 3.40GHz
2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 6 9 10 11 13
physical 1: cores 0 6 9 10 11 13
cache size : 19712 KB

From /proc/meminfo
MemTotal: 394864840 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-3joc 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(x86_64) x86_64 x86_64 GNU/Linux

run-level 3 Sep 7 00:09

SPEC is set to: /home/cpu2006-1.2
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 516G 29G 488G 6% /home
Additional information from dmidecode:
Continued on next page
Cisco Systems
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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C220M5.3.1.1d.0.0615170645 06/15/2017
Memory:
24x 0xCE00 M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transient_hugepage/enabled
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Base Compiler Invocation

C benchmarks:
icc -m64

Continued on next page
Cisco Systems  
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Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
450.soplex: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64 -nofor_main  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
470.lbm: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX  
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
Cisco Systems
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Tested by: Cisco Systems

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Hardware Availability: Aug-2017
Software Availability: Jun-2017

Peak Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
  433.milc: basepeak = yes
  470.lbm: basepeak = yes
  482.sphinx3: basepeak = yes

C++ benchmarks:
  444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
  -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -fno-alias -auto-ilp32
  447.dealII: basepeak = yes
  450.soplex: basepeak = yes
  453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
  -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:
  410.bwaves: basepeak = yes
  416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
  -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-
SPEC CFP2006 Result

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
  -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -unroll2 -inline-level=0
  -qopt-prefetch -parallel
465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
  -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
  -no-prec-div(pass 2) -inline-calloc -qopt-malloc-options=3
  -auto -unroll4

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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