## SPEC® CFP2006 Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant BL460c Gen10  
(1.70 GHz, Intel Xeon Bronze 3106)

**SPECfp®2006 =** Not Run  
**SPECfp_base2006 =** 46.4

<table>
<thead>
<tr>
<th>Test Sponsor: HPE</th>
<th>Test Date: Dec-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>HPE</td>
<td>Hardware Availability: Oct-2017</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Software</th>
<th>Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Kernel 3.10.0-693.e17.x86_64</td>
</tr>
<tr>
<td></td>
<td>Compiler: C/C++: Version 17.0.1.132 of Intel C/C++</td>
</tr>
<tr>
<td></td>
<td>Compiler for Linux; Fortran: Version 17.0.1.132 of Intel Fortran</td>
</tr>
<tr>
<td></td>
<td>Compiler for Linux</td>
</tr>
<tr>
<td></td>
<td>Auto Parallel: Yes</td>
</tr>
<tr>
<td></td>
<td>File System: xfs</td>
</tr>
</tbody>
</table>

**CPU Name:** Intel Xeon Bronze 3106

**CPU Characteristics:**
- **CPU MHz:** 1700
- **FPU:** Integrated
- **CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip
- **CPU(s) orderable:** 1, 2 chip(s)
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 1 MB I+D on chip per core

### Benchmark Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Code</th>
<th>SPECfp_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
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</tr>
<tr>
<td>416.gamess</td>
<td>11.4</td>
<td></td>
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<tr>
<td>433.milc</td>
<td>41.4</td>
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<tr>
<td>434.zeusmp</td>
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<td>435.gromacs</td>
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<td>436.cactusADM</td>
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<td>367</td>
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<tr>
<td>437.leslie3d</td>
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<td>204</td>
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<td>444.namd</td>
<td>12.1</td>
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<tr>
<td>447.dealII</td>
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<td>450.soplex</td>
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<td>453.povray</td>
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<td>454.calculix</td>
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<tr>
<td>459.GemsFDTD</td>
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<td>130</td>
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<td>465.tonto</td>
<td>14.1</td>
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**SPECfp_base2006 = 46.4**
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen10
(1.70 GHz, Intel Xeon Bronze 3106)

SPEC CFP2006 Result
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SPECFp2006 = Not Run
SPECFp_base2006 = 46.4

Test date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: None

L3 Cache: 11 MB I+D on chip per chip
Other Cache: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)
Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0
Other Hardware: None

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<th>Peak Seconds</th>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled

Continued on next page
Platform Notes (Continued)

Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Peak Frequency Compute
Energy/Performance Bias set to Maximum Performance
Workload Profile set to Custom
NUMA Group Size Optimization set to Flat
Uncore Frequency Scaling set to Auto
Sysinfo program /root/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on localhost.localdomain Wed Dec 20 23:09:32 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 11264 KB

From /proc/meminfo
MemTotal:       197446416 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release*/etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.3 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.3"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
  Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 20 15:29

Continued on next page
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CPU2006 license: 3  
Test sponsor: HPE  
Tested by: HPE

Platform Notes (Continued)

SPEC is set to: /root/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 442G 30G 413G 7% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I41 09/29/2017
Memory:
4x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:  
KMP_AFFINITY = "granularity=fine,compact,1,0"  
LD_LIBRARY_PATH = "/root/cpu2006/lib/ia32:/root/cpu2006/lib/intel64:/root/cpu2006/sh10.2"  
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.htm.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.
SPEC CFP2006 Result

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ProLiant BL460c Gen10
(1.70 GHz, Intel Xeon Bronze 3106)

SPECFp2006 = Not Run
SPECFp_base2006 = 46.4

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
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453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

C++ benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

Fortran benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -parallel -qopt-prefetch

Benchmarks using both Fortran and C:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
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CPU2006 license: 3
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The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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