### SPEC® CFP2006 Result

**Hewlett Packard Enterprise**
(Test Sponsor: HPE)

**ProLiant BL460c Gen10**
(1.70 GHz, Intel Xeon Bronze 3106)

**SPECfp®_rate2006 = Not Run**

**SPECfp_rate_base2006 = 415**

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>HPE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability</td>
<td>Oct-2017</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Test date</td>
<td>Dec-2017</td>
</tr>
<tr>
<td>CPU2006 license</td>
<td>3</td>
</tr>
<tr>
<td>Tested by</td>
<td>HPE</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECfp</th>
<th>Rate2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>415</td>
</tr>
</tbody>
</table>

### Hardware

**CPU Name:** Intel Xeon Bronze 3106

**CPU Characteristics:**
- CPU MHz: 1700
- FPU: Integrated
- CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
- CPU(s) orderable: 1, 2 chip(s)
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 1 MB I+D on chip per core

### Software

**Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP2
- Kernel 3.10.0-693.e17.x86_64
**Compiler:**
- C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
- Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
**Auto Parallel:** No
**File System:** xfs
**System State:** Run level 3 (multi-user)

---

Continued on next page
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen10
(1.70 GHz, Intel Xeon Bronze 3106)

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

L3 Cache: 11 MB I+D on chip per chip
Other Cache: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)
Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>16</td>
<td>481</td>
<td>452</td>
<td>498</td>
<td>437</td>
<td>397</td>
<td>548</td>
</tr>
<tr>
<td>416.gamess</td>
<td>16</td>
<td>887</td>
<td>353</td>
<td>887</td>
<td>353</td>
<td>888</td>
<td>353</td>
</tr>
<tr>
<td>433.milc</td>
<td>16</td>
<td>344</td>
<td>427</td>
<td>348</td>
<td>422</td>
<td>281</td>
<td>522</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>16</td>
<td>276</td>
<td>527</td>
<td>277</td>
<td>526</td>
<td>263</td>
<td>553</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>16</td>
<td>308</td>
<td>371</td>
<td>310</td>
<td>369</td>
<td>308</td>
<td>370</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>16</td>
<td>295</td>
<td>647</td>
<td>296</td>
<td>645</td>
<td>281</td>
<td>680</td>
</tr>
<tr>
<td>438.leslie3d</td>
<td>16</td>
<td>471</td>
<td>319</td>
<td>471</td>
<td>319</td>
<td>434</td>
<td>347</td>
</tr>
<tr>
<td>444.namd</td>
<td>16</td>
<td>490</td>
<td>262</td>
<td>491</td>
<td>262</td>
<td>491</td>
<td>262</td>
</tr>
<tr>
<td>447.dealII</td>
<td>16</td>
<td>352</td>
<td>520</td>
<td>349</td>
<td>525</td>
<td>349</td>
<td>524</td>
</tr>
<tr>
<td>450.soplex</td>
<td>16</td>
<td>452</td>
<td>295</td>
<td>452</td>
<td>295</td>
<td>440</td>
<td>303</td>
</tr>
<tr>
<td>453.povray</td>
<td>16</td>
<td>164</td>
<td>518</td>
<td>164</td>
<td>519</td>
<td>164</td>
<td>518</td>
</tr>
<tr>
<td>454.calculix</td>
<td>16</td>
<td>270</td>
<td>489</td>
<td>270</td>
<td>488</td>
<td>270</td>
<td>488</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>16</td>
<td>669</td>
<td>254</td>
<td>597</td>
<td>284</td>
<td>559</td>
<td>304</td>
</tr>
<tr>
<td>465.tonto</td>
<td>16</td>
<td>404</td>
<td>390</td>
<td>403</td>
<td>391</td>
<td>403</td>
<td>391</td>
</tr>
<tr>
<td>470.lbm</td>
<td>16</td>
<td>417</td>
<td>527</td>
<td>379</td>
<td>580</td>
<td>345</td>
<td>637</td>
</tr>
<tr>
<td>481.wrf</td>
<td>16</td>
<td>362</td>
<td>494</td>
<td>350</td>
<td>511</td>
<td>339</td>
<td>527</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>16</td>
<td>893</td>
<td>349</td>
<td>902</td>
<td>347</td>
<td>898</td>
<td>347</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
irqbalance disabled with "systemctl stop irqbalance"

Continued on next page
Operating System Notes (Continued)

tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E State
Workload Profile set to Custom
Sub-NUMA Clustering set to Disabled
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-h3xn Thu Dec 14 22:46:51 2017

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
        2 "physical id"s (chips)
        16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 11264 KB

From /proc/meminfo
    MemTotal:       197621936 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

    /usr/bin/lsb_release -d
    SUSE Linux Enterprise Server 12 SP2

    From /etc/*release* /etc/*version*
    SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2
        # This file is deprecated and will be removed in a future service pack or
        release.
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen10
(1.70 GHz, Intel Xeon Bronze 3106)

SPECfp_rate2006 =  Not Run
SPECfp_rate_base2006 =  415

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-h3xn 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 14 15:39
SPEC is set to: /home/cpu2006
Filesistem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 405G 112G 293G 28% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I41 09/29/2017
Memory:
4x UNKNOWN NOT AVAILABLE
12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on
past performance using the historical hardware and/or
Continued on next page
SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen10
(1.70 GHz, Intel Xeon Bronze 3106)

SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 415

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

General Notes (Continued)

software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.htm.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen10
(1.70 GHz, Intel Xeon Bronze 3106)

SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 415

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 13 June 2018.