**SPEC® CINT2006 Result**

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant BL460c Gen10  
(2.10 GHz, Intel Xeon Silver 4110)

**SPECint_rate2006 = Not Run**  
**SPECint_rate_base2006 = 622**

<table>
<thead>
<tr>
<th>CPU2006 license: 3</th>
<th>Test date: Dec-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor: HPE</td>
<td>Hardware Availability: Nov-2017</td>
</tr>
<tr>
<td>Tested by: HPE</td>
<td>Software Availability: Apr-2017</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
</tr>
</tbody>
</table>

| SPECint_rate_base2006 = 622 |

**CPU**

- **CPU Name:** Intel Xeon Silver 4110  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.00 GHz  
- **CPU MHz:** 2100  
- **FPU:** Integrated  
- **CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1, 2 chip(s)  
- **Primary Cache:** 32 KB I+ 32 KB D on chip per core  
- **Secondary Cache:** 1 MB I+D on chip per core  
- **L3 Cache:** 11 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Disk Subsystem:** 1 x 480 GB SATA SSD, RAID 0  
- **Other Hardware:** None

**Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP3  
**Compiler:** C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
**Auto Parallel:** No  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32-bit  
**Peak Pointers:** Not Applicable  
**Other Software:** Microquill SmartHeap V10.2

---

Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
<th>Seconds Peak</th>
<th>Ratio Peak</th>
<th>Copies</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
<th>Seconds Peak</th>
<th>Ratio Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>32</td>
<td>696</td>
<td>449</td>
<td>709</td>
<td>441</td>
<td>706</td>
<td>443</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>32</td>
<td>1110</td>
<td>278</td>
<td>1117</td>
<td>277</td>
<td>1112</td>
<td>278</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>403.gcc</td>
<td>32</td>
<td>541</td>
<td>476</td>
<td>538</td>
<td>479</td>
<td>539</td>
<td>478</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>32</td>
<td>311</td>
<td>938</td>
<td><strong>310</strong></td>
<td><strong>941</strong></td>
<td>310</td>
<td>942</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>32</td>
<td>1028</td>
<td>327</td>
<td><strong>1033</strong></td>
<td><strong>325</strong></td>
<td>1040</td>
<td>323</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>456.hmmer</td>
<td>32</td>
<td>327</td>
<td>914</td>
<td>327</td>
<td>912</td>
<td>327</td>
<td>912</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>458.sjeng</td>
<td>32</td>
<td>1100</td>
<td>352</td>
<td><strong>1112</strong></td>
<td><strong>348</strong></td>
<td>1113</td>
<td>348</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>32</td>
<td>70.0</td>
<td>9470</td>
<td><strong>70.0</strong></td>
<td><strong>9470</strong></td>
<td>70.1</td>
<td>9460</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>32</td>
<td>1229</td>
<td>576</td>
<td>1227</td>
<td>577</td>
<td><strong>1228</strong></td>
<td><strong>576</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>32</td>
<td>567</td>
<td>353</td>
<td>568</td>
<td>352</td>
<td>567</td>
<td>353</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>32</td>
<td>602</td>
<td>373</td>
<td>602</td>
<td>373</td>
<td>602</td>
<td>373</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>32</td>
<td>271</td>
<td>813</td>
<td>272</td>
<td>813</td>
<td>270</td>
<td>818</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:
  Thermal Configuration set to Maximum Cooling
  Memory Patrol Scrubbing set to Disabled
  LLC Prefetch set to Enabled
  LLC Dead Line Allocation set to Disabled
  Workload Profile set to General Throughput Compute
  Minimum Processor Idle Power Core C-State set to C1E State
  Workload Profile set to Custom

Continued on next page
Platform Notes (Continued)

Sub-NUMA Clustering set to Disabled
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on bl460c107 Fri Dec 15 18:47:25 2017

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
  2 "physical id"s (chips)
  32 "processers"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 11264 KB

From /proc/meminfo
MemTotal:       197750024 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux bl460c107 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 15 18:46

SPEC is set to: /home/cpu2006
Filesystem    Type  Size  Used Avail Use% Mounted on
Continued on next page
SPEC CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen10
(2.10 GHz, Intel Xeon Silver 4110)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 622

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Apr-2017

Platform Notes (Continued)
/dev/sda4      xfs  300G  140G  161G  47% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I41 11/14/2017
Memory:
  4x UNKNOWN NOT AVAILABLE
  12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on
past performance using the historical hardware and/or
software described on this result page.

The system as described on this result page was formerly
generally available. At the time of this publication, it may
not be shipping, and/or may not be supported, and/or may fail
to meet other tests of General Availability described in the

This measured result may not be representative of the result
that would be measured were this benchmark run with hardware
and software available as of the publication date.
Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml
SPEC CINT2006 Result
Copyright 2006-2018 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen10
(2.10 GHz, Intel Xeon Silver 4110)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 622

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Dec-2017
Hardware Availability: Nov-2017
Software Availability: Apr-2017

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 13 June 2018.