## SPEC® CFP2006 Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
Synergy 480 Gen10  
(3.20 GHz, Intel Xeon Gold 6134)

<table>
<thead>
<tr>
<th>Category</th>
<th>Details</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU2006 license</strong></td>
<td>3</td>
<td></td>
</tr>
<tr>
<td><strong>Test sponsor</strong></td>
<td>HPE</td>
<td></td>
</tr>
<tr>
<td><strong>Tested by</strong></td>
<td>HPE</td>
<td></td>
</tr>
<tr>
<td><strong>Test date</strong></td>
<td>Dec-2017</td>
<td></td>
</tr>
<tr>
<td><strong>Hardware Availability</strong></td>
<td>Oct-2017</td>
<td></td>
</tr>
<tr>
<td><strong>Software Availability</strong></td>
<td>Apr-2017</td>
<td></td>
</tr>
<tr>
<td><strong>SPECfp_rate2006</strong></td>
<td>Not Run</td>
<td></td>
</tr>
<tr>
<td><strong>SPECfp_rate_base2006</strong></td>
<td>942</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Test</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name</strong></td>
<td>Intel Xeon Gold 6134</td>
</tr>
<tr>
<td><strong>CPU Characteristics</strong></td>
<td>Intel Turbo Boost Technology up to 3.70 GHz</td>
</tr>
<tr>
<td><strong>CPU MHz</strong></td>
<td>3200</td>
</tr>
<tr>
<td><strong>GPU</strong></td>
<td>Integrated</td>
</tr>
<tr>
<td><strong>CPU(s) enabled</strong></td>
<td>16 cores, 2 chips, 8 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td><strong>CPU(s) orderable</strong></td>
<td>1, 2 chip(s)</td>
</tr>
<tr>
<td><strong>Primary Cache</strong></td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td><strong>Secondary Cache</strong></td>
<td>1 MB I+D on chip per core</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Test</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Operating System</strong></td>
<td>SUSE Linux Enterprise Server 12 (x86_64) SP2 Kernel 4.4.21-69-default</td>
</tr>
<tr>
<td><strong>Compiler</strong></td>
<td>C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td><strong>Auto Parallel</strong></td>
<td>No</td>
</tr>
<tr>
<td><strong>File System</strong></td>
<td>xfs</td>
</tr>
<tr>
<td><strong>System State</strong></td>
<td>Run level 3 (multi-user)</td>
</tr>
</tbody>
</table>
SPEC CFP2006 Result

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(Test Sponsor: HPE)
Synergy 480 Gen10
(3.20 GHz, Intel Xeon Gold 6134)

SPECfp_rate2006 = Not Run
SPECfp_rate_base2006 = 942

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

L3 Cache: 24.75 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 960 GB SATA SSD, RAID 0
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>32</td>
<td>439</td>
<td>990</td>
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<td>990</td>
<td>439</td>
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<tr>
<td>416.gamess</td>
<td>32</td>
<td>741</td>
<td>846</td>
<td>749</td>
<td>837</td>
<td>749</td>
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<tr>
<td>433.milc</td>
<td>32</td>
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<td>1010</td>
<td>291</td>
<td>1010</td>
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<tr>
<td>434.reusmp</td>
<td>32</td>
<td>262</td>
<td>1110</td>
<td>260</td>
<td>1120</td>
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<td>1120</td>
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<td>435.gromacs</td>
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<td>1060</td>
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<td>436.cactusADM</td>
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<td>1200</td>
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<td>437.leslie3d</td>
<td>32</td>
<td>488</td>
<td>616</td>
<td>487</td>
<td>617</td>
<td>486</td>
<td>619</td>
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<tr>
<td>444.namd</td>
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<td>376</td>
<td>683</td>
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<td>447.dealII</td>
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<td>450.soplex</td>
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<td>453.povray</td>
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<td>454.calculix</td>
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<td>459.GemsFDTD</td>
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<td>465.tonto</td>
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<tr>
<td>482.sphinx3</td>
<td>32</td>
<td>730</td>
<td>855</td>
<td>733</td>
<td>851</td>
<td>734</td>
<td>850</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
Continued on next page
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Test date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Apr-2017

Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E State
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e9d4b4eb51ed28d7f98696cbe290c1)
running on linux-vjuj Mon Dec 4 15:43:29 2017

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 16
  physical 0: cores 0 2 3 9 16 19 26 27
  physical 1: cores 0 4 5 16 17 20 21
cache size : 25344 KB

From /proc/meminfo
MemTotal:       395928748 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"

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Platform Notes (Continued)

```
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
          (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 4 11:39

SPEC is set to: /home/cpu2006
Filesystem   Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   852G   21G  832G   3% /home

Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 09/27/2017
Memory:
   24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may
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General Notes (Continued)

not be shipping, and/or may not be supported, and/or may fail
to meet other tests of General Availability described in the

This measured result may not be representative of the result
that would be measured were this benchmark run with hardware
and software available as of the publication date.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
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Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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