Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(3.50 GHz, Intel Xeon Gold 6144)

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<thead>
<tr>
<th>SPECint_rate2006</th>
<th>=</th>
<th>Not Run</th>
</tr>
</thead>
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<td>SPECint_rate_base2006</td>
<td>=</td>
<td>2170</td>
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</table>

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

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<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon Gold 6144</td>
<td>Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2</td>
</tr>
<tr>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz</td>
<td>Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux; Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>CPU MHz: 3500</td>
<td>Auto Parallel: No</td>
</tr>
<tr>
<td>FPU: Integrated</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>CPU(s) orderable: 1, 2, 4 chip(s)</td>
<td>Base Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
<td>Peak Pointers: Not Applicable</td>
</tr>
<tr>
<td>Secondary Cache: 1 MB I+D on chip per core</td>
<td>Other Software: Microquill SmartHeap V10.2</td>
</tr>
<tr>
<td>L3 Cache: 24.75 MB I+D on chip per chip</td>
<td>Other Hardware: None</td>
</tr>
<tr>
<td>Other Cache: None</td>
<td>Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)</td>
</tr>
<tr>
<td>Disk Subsystem: 2 x 480 GB SATA SSD, RAID 0</td>
<td></td>
</tr>
</tbody>
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</tr>
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<td></td>
</tr>
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<td></td>
</tr>
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<td></td>
</tr>
<tr>
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<td></td>
</tr>
<tr>
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<td></td>
</tr>
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<td></td>
</tr>
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<th></th>
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<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>64 Copies:</td>
<td>991</td>
<td>1640</td>
<td>3010</td>
<td>1240</td>
<td>3100</td>
<td>1320</td>
<td>2260</td>
<td>986</td>
<td>1270</td>
<td>2790</td>
<td></td>
</tr>
</tbody>
</table>

Test date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017
SPEC CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL560 Gen10
(3.50 GHz, Intel Xeon Gold 6144)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 2170

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>64</td>
<td>396</td>
<td>1580</td>
<td>397</td>
<td>1580</td>
<td>397</td>
<td>1580</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>64</td>
<td>622</td>
<td>992</td>
<td>626</td>
<td>987</td>
<td>623</td>
<td>991</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>403.gcc</td>
<td>64</td>
<td>315</td>
<td>1630</td>
<td>314</td>
<td>1640</td>
<td>315</td>
<td>1640</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>64</td>
<td>194</td>
<td>3010</td>
<td>194</td>
<td>3020</td>
<td>195</td>
<td>3000</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>64</td>
<td>543</td>
<td>1240</td>
<td>542</td>
<td>1240</td>
<td>543</td>
<td>1240</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>456.hmmer</td>
<td>64</td>
<td>192</td>
<td>3110</td>
<td>193</td>
<td>3100</td>
<td>193</td>
<td>3100</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>458.sjeng</td>
<td>64</td>
<td>589</td>
<td>1310</td>
<td>588</td>
<td>1320</td>
<td>588</td>
<td>1320</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>462.libquantum</td>
<td>64</td>
<td>37.0</td>
<td>35900</td>
<td>36.9</td>
<td>35900</td>
<td>37.0</td>
<td>35900</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>64</td>
<td>628</td>
<td>2260</td>
<td>629</td>
<td>2250</td>
<td>627</td>
<td>2260</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>64</td>
<td>406</td>
<td>986</td>
<td>406</td>
<td>986</td>
<td>406</td>
<td>985</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>64</td>
<td>353</td>
<td>1270</td>
<td>354</td>
<td>1270</td>
<td>353</td>
<td>1270</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>64</td>
<td>158</td>
<td>2790</td>
<td>159</td>
<td>2790</td>
<td>159</td>
<td>2770</td>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

Platform Notes

BIOS Configuration:
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Workload Profile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E State
Sysinfo program /home/cpu2006/config/sysinfo.rev6993

Continued on next page
SPEC CINT2006 Result

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SPECint_rate_base2006 = 2170

Platform Notes (Continued)

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-e6az Thu Dec 7 11:21:40 2017

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
4 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 8
siblings : 16
physical 0: cores 0 2 3 9 16 19 26 27
physical 1: cores 0 2 3 9 16 19 26 27
physical 2: cores 0 2 3 9 16 19 26 27
physical 3: cores 0 2 3 9 16 19 26 27
cache size : 25344 KB

From /proc/meminfo
MemTotal: 792283168 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 6 16:09

Continued on next page
**SPEC CINT2006 Result**

**Hewlett Packard Enterprise**
(Test Sponsor: HPE)

**ProLiant DL560 Gen10**
(3.50 GHz, Intel Xeon Gold 6144)

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CPU2006 license: 3
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**Platform Notes (Continued)**

SPEC is set to: /home/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 852G 89G 764G 11% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017
Memory:
48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

**General Notes**

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/lib/ia32:/home/cpu2006/lib/intel64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.htm.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.
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CPU2006 license: 3  
Test sponsor: HPE  
Tested by: HPE  
Test date: Dec-2017  
Hardware Availability: Oct-2017  
Software Availability: Sep-2017

Base Compiler Invocation

C benchmarks:
  icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks:
  icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -qopt-mem-layout-trans=3

C++ benchmarks:
  -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
  -qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:
  403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml
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</tr>
<tr>
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</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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