Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 480 Gen10
(2.20 GHz, Intel Xeon Gold 5120)

| SPECint_rate2006 = Not Run |
| SPECint_rate_base2006 = 1330 |

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

| SPECint_rate_base2006 = 1330 |

### Hardware
- **CPU Name:** Intel Xeon Gold 5120
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.20 GHz
- **CPU MHz:** 2200
- **FPU:** Integrated
- **CPU(s) enabled:** 28 cores, 2 chips, 14 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1, 2 chip(s)
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 1 MB I+D on chip per core
- **L3 Cache:** 19.25 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)
- **Disk Subsystem:** 1 x 480 GB SATA SSD, RAID 0
- **Other Hardware:** None

### Software
- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP2
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux
- **Auto Parallel:** No
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** Not Applicable
- **Other Software:** Microquill SmartHeap V10.2
SPEC CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 480 Gen10
(2.20 GHz, Intel Xeon Gold 5120)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 1330

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE
Test date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Base</td>
<td></td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>56</td>
<td>567</td>
<td>966</td>
<td>571</td>
<td>958</td>
<td>572</td>
<td>957</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>56</td>
<td>946</td>
<td>871</td>
<td>952</td>
<td>568</td>
<td>943</td>
<td>573</td>
</tr>
<tr>
<td>403.gcc</td>
<td>56</td>
<td>482</td>
<td>936</td>
<td>482</td>
<td>935</td>
<td>478</td>
<td>942</td>
</tr>
<tr>
<td>429.mcf</td>
<td>56</td>
<td>286</td>
<td>1780</td>
<td>288</td>
<td>1780</td>
<td>286</td>
<td>1780</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>56</td>
<td>726</td>
<td>809</td>
<td>723</td>
<td>813</td>
<td>722</td>
<td>814</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>56</td>
<td>283</td>
<td>1850</td>
<td>281</td>
<td>1860</td>
<td>279</td>
<td>1870</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>56</td>
<td>776</td>
<td>874</td>
<td>775</td>
<td>874</td>
<td>775</td>
<td>875</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>56</td>
<td>48.7</td>
<td>23800</td>
<td>48.6</td>
<td>23900</td>
<td>48.6</td>
<td>23900</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>56</td>
<td>873</td>
<td>1420</td>
<td>876</td>
<td>1410</td>
<td>874</td>
<td>1420</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>56</td>
<td>515</td>
<td>679</td>
<td>515</td>
<td>680</td>
<td>515</td>
<td>680</td>
</tr>
<tr>
<td>473.astar</td>
<td>56</td>
<td>540</td>
<td>729</td>
<td>540</td>
<td>728</td>
<td>538</td>
<td>731</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>56</td>
<td>249</td>
<td>1550</td>
<td>249</td>
<td>1550</td>
<td>249</td>
<td>1550</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesytem page cache cleared with:
   shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
   numactl --interleave=all runspec <etc>
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/ numa_balancing"

Platform Notes

BIOS Configuration:
   Thermal Configuration set to Maximum Cooling
   Memory Patrol Scrubbing set to Disabled
   LLC Prefetch set to Enabled
   LLC Dead Line Allocation set to Disabled
   Workload Profile set to General Throughput Compute
   Minimum Processor Idle Power Core C-State set to C1E State
   Sysinfo program /home/cpu2006/config/sysinfo.rev6993
Continued on next page
### SPEC CINT2006 Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
Synergy 480 Gen10  
(2.20 GHz, Intel Xeon Gold 5120)

| SPECint_rate2006 = Not Run | SPECint_rate_base2006 = 1330 |

**CPU2006 license:** 3  
**Test sponsor:** HPE  
**Tested by:** HPE  
**Test date:** Dec-2017  
**Hardware Availability:** Oct-2017  
**Software Availability:** Sep-2017

### Platform Notes (Continued)

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on sy480_hjp_suse_machine1 Tue Dec 19 23:13:00 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo  
- model name : Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz  
- 2 "physical id"s (chips)  
- 56 "processors"  
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
  - cpu cores : 14  
  - siblings : 28  
  - physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14  
  - physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14  
- cache size : 19712 KB

From /proc/meminfo  
- MemTotal: 395925576 kB  
- HugePages_Total: 0  
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*  
- SuSE-release:  
  - SUSE Linux Enterprise Server 12 (x86_64)  
  - VERSION = 12  
  - PATCHLEVEL = 2  
  - # This file is deprecated and will be removed in a future service pack or release.  
  - # Please check /etc/os-release for details about this release.  
- os-release:  
  - NAME="SLES"  
  - VERSION="12-SP2"  
  - VERSION_ID="12.2"  
  - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"  
  - ID="sles"  
  - ANSI_COLOR="0;32"  
  - CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:  
Linux sy480_hjp_suse_machine1 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 19 23:11

SPEC is set to: /home/cpu2006  
Files used:

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>405G</td>
<td>78G</td>
<td>328G</td>
<td>20%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode:  
Continued on next page
SPEC CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
Synergy 480 Gen10
(2.20 GHz, Intel Xeon Gold 5120)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 1330

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Dec-2017
Hardware Availability: Oct-2017
Software Availability: Sep-2017

Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE I42 09/27/2017
Memory:
24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.htm.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32

Continued on next page
### SPEC CINT2006 Result

Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
Synergy 480 Gen10  
(2.20 GHz, Intel Xeon Gold 5120)

<table>
<thead>
<tr>
<th>CPU2006 license: 3</th>
<th>Test date: Dec-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>Test sponsor: HPE</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Hardware Availability: Oct-2017</td>
</tr>
<tr>
<td></td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

**SPECint_rate2006 = Not Run**  
**SPECint_rate_base2006 = 1330**

### Base Compiler Invocation (Continued)

**C++ benchmarks:**  
```shell  
icpc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32  
```

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_1A32</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>403.gcc</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>429.mcf</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>473.astar</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX</td>
</tr>
</tbody>
</table>

### Base Optimization Flags

**C benchmarks:**  
```bash  
xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3  
```

**C++ benchmarks:**  
```bash  
xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/home/cpu2006/sh10.2 -lsmartheap  
```

### Base Other Flags

**C benchmarks:**  
```bash  
403.gcc -Dalloca=_alloca  
```

The flags files that were used to format this result can be browsed at  
[http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html](http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html)

You can also download the XML flags sources by saving the following links:  
[http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml](http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml)
### SPEC CINT2006 Result

**Hewlett Packard Enterprise**
(Test Sponsor: HPE)

**Synergy 480 Gen10**
(2.20 GHz, Intel Xeon Gold 5120)

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECint_rate2006</td>
<td>Not Run</td>
</tr>
<tr>
<td>SPECint_rate_base2006</td>
<td>1330</td>
</tr>
</tbody>
</table>

CPU2006 license: 3  
Test sponsor: HPE  
Tested by: HPE  
Test date: Dec-2017  
Hardware Availability: Oct-2017  
Software Availability: Sep-2017

---

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.  
Originally published on 13 June 2018.