**SPEC® CPU2017 Floating Point Speed Result**

**Dell Inc.**

PowerEdge R940  
(Intel Xeon Gold 6132, 2.60 GHz)

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base = 146</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>56</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>56</td>
</tr>
<tr>
<td>619.lbm_s</td>
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<tr>
<td>621.wrf_s</td>
<td>56</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>56</td>
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<tr>
<td>628.pop2_s</td>
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<tr>
<td>638.imagick_s</td>
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</tr>
<tr>
<td>644.nab_s</td>
<td>56</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>56</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>56</td>
</tr>
</tbody>
</table>

**Hardware**

**CPU Name:** Intel Xeon Gold 6132  
**Max MHz.:** 3700  
**Nominal:** 2600  
**Enabled:** 56 cores, 4 chips  
**Orderable:** 2,4 chip  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 19.25 MB I+D on chip per core  
**Memory:** 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
**Storage:** 1 x 900 GB 15K RPM SAS12  
**Other:** None

**Software**

**OS:** SUSE Linux Enterprise Server 12 SP2  
**Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
**Parallel:** Yes  
**File System:** xfs  
**System State:** Run level 3 (multi-user)

---

**SPECspeed2017_fp_peak = 148**

Test Date: Oct-2017

Hardware Availability: Sep-2017

Test Sponsor: Dell Inc.

Software Availability: Sep-2017

Tested by: Dell Inc.

Hardware Availability: Sep-2017

Test Sponsor: Dell Inc.

Software Availability: Sep-2017

Tested by: Dell Inc.

Hardware Availability: Sep-2017

Test Sponsor: Dell Inc.
SPEC CPU2017 Floating Point Speed Result

Dell Inc.
PowerEdge R940
(Intel Xeon Gold 6132, 2.60 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed2017_fp_base = 146
SPECspeed2017_fp_peak = 148

Results Table

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<td>76.9</td>
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<td>77.2</td>
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<tr>
<td>638.imagick_s</td>
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<td>56</td>
<td>82.7</td>
<td>190</td>
<td>82.1</td>
<td>192</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Platform Notes

BIOS settings:
Logical Processor Disabled
Virtualization Technology Disabled
Sub NUMA Cluster Disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C1E Disabled
C States set to Autonomous
Uncore Frequency set to Dynamic

(Continued on next page)
## Dell Inc.  
**PowerEdge R940**  
(Intel Xeon Gold 6132, 2.60 GHz)  

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>146</td>
<td>148</td>
</tr>
</tbody>
</table>

### Platform Notes (Continued)

- Memory Patrol Scrub Disabled
- Energy Efficiency Policy set to Performance
- CPU Interconnect Bus Link Power Management Disabled
- PCI ASPM L1 Link Power Management Disabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b0c091c0f
- running on linux-2h8y Sat Oct 14 00:53:15 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Gold 6132 CPU @ 2.60GHz
- 4 "physical id"s (chips)
- 56 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 14
  - siblings : 14
  - physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  - physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  - physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  - physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 56
- On-line CPU(s) list: 0-55
- Thread(s) per core: 1
- Core(s) per socket: 14
- Socket(s): 4
- NUMA node(s): 4
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6132 CPU @ 2.60GHz
- Stepping: 4
- CPU MHz: 2593.932
- BogoMIPS: 5187.86
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 19712K

*(Continued on next page)*
SPEC CPU2017 Floating Point Speed Result

Dell Inc.
PowerEdge R940
(Intel Xeon Gold 6132, 2.60 GHz)

SPECspeed2017_fp_base = 146
SPECspeed2017_fp_peak = 148

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
        pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpte1gb rdtsscp
        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
        aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
        fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
        xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat pmls dtherm intel_pt
        tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
        erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
        avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52
node 0 size: 192119 MB
node 0 free: 191522 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53
node 1 size: 193521 MB
node 1 free: 192924 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54
node 2 size: 193521 MB
node 2 free: 192970 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55
node 3 size: 193518 MB
node 3 free: 193007 MB
node distances:
  node 0 1 2 3
  0: 10 21 21 21
  1: 21 10 21 21
  2: 21 21 10 21
  3: 21 21 21 10

From /proc/meminfo
MemTotal: 791225344 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2

(Continued on next page)
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Test Date: Oct-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)

From /etc/*release* /etc/*version*

SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
  Linux linux-2h8y 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Oct 13 19:42

SPEC is set to: /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>796G</td>
<td>17G</td>
<td>779G</td>
<td>3%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.1.7 08/10/2017
Memory:
  48x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
 CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

(Continued on next page)
<table>
<thead>
<tr>
<th>Compiler Version Notes (Continued)</th>
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</thead>
<tbody>
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<td>CC 619.lbm_s(peak)</td>
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CPU2017 License: 55
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Test Date: Oct-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
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## Dell Inc.
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(Intel Xeon Gold 6132, 2.60 GHz)

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**SPECspeed2017_fp_base = 146**  
**SPECspeed2017_fp_peak = 148**

---

### Base Optimization Flags

**C benchmarks:**
- `-xCORE-AVX512`  
- `-ipo -O3 -no-prec-div -qopt-prefetch`  
- `-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`

**Fortran benchmarks:**
- `-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`  
- `-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp`  
- `-nostandard-realloc-lhs -align array32byte`

**Benchmarks using both Fortran and C:**
- `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`  
- `-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`  
- `-nostandard-realloc-lhs -align array32byte`

**Benchmarks using Fortran, C, and C++:**
- `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`  
- `-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`  
- `-nostandard-realloc-lhs -align array32byte`

---

### Base Other Flags

**C benchmarks:**
- `-m64 -std=c11`

**Fortran benchmarks:**
- `-m64`

**Benchmarks using both Fortran and C:**
- `-m64 -std=c11`

**Benchmarks using Fortran, C, and C++:**
- `-m64 -std=c11`

---

### Peak Compiler Invocation

**C benchmarks:**
- `icc`

**Fortran benchmarks:**
- `ifort`

(Continued on next page)
Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Peak Optimization Flags (Continued)

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
- -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch
- -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
- -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
- -align array32byte

Peak Other Flags

C benchmarks:
- -m64 -std=c11

Fortran benchmarks:
- -m64

Benchmarks using both Fortran and C:
- -m64 -std=c11

Benchmarks using Fortran, C, and C++:
- -m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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