## Fujitsu

**PRIMERGY TX2550 M4, Intel Xeon Silver 4114, 2.20GHz**

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu  
**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017

### SPECrate2017_int_base = 44.9  
SPECrate2017_int_peak = 47.8

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</table>

### Hardware

- **CPU Name:** Intel Xeon Silver 4114  
- **Max MHz.:** 3000  
- **Nominal:** 2200  
- **Enabled:** 10 cores, 1 chip, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 1 MB I+D on chip per core  
- **Cache L3:** 13.75 MB I+D on chip per chip  
- **Memory:** 96 GB (6 x 16 GB 1Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x SATA HDD, 1000 GB, 7200 RPM  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP2  
  4.4.21-69-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
  Compiler for Linux:  
  Fortran: Version 18.0.0.128 of Intel Fortran  
  Compiler for Linux
- **Parallel:** No  
- **Firmware:** Fujitsu BIOS Version V5.0.0.12 R1.13.0 for D3386-A1x released Nov-2017  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1;  
  jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;  
  jemalloc: built with the RedHat Enterprise 7.4,  
  and the system compiler gcc 4.8.5;  
  jemalloc: sources available via jemalloc.net or releases
Fujitsu
PRIMERGY TX2550 M4, Intel Xeon Silver 4114,
2.20GHz

SPEC CPU2017 Integer Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_int_base = 44.9
SPECrate2017_int_peak = 47.8

Results Table

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SPECrate2017_int_base = 44.9
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with : nohz_full=1-19
Turbo mode set with:
cpupower -c all frequency-set -g performance
cpu idle state set with:
cpupower idle-set -d 1
Process tunning setting:
echo 0 > /proc/sys/kernel/numa_balancing

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-fixflags-confirm/lib/ia32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/Benchmark/speccpu2017-fixflags-confirm/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/Benchmark/speccpu2017-fixflags-confirm/je5.0.1-32"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/Benchmark/speccpu2017-fixflags-confirm/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Platform Notes

BIOS configuration:
DCU Streamer Prefetcher = Disabled
Override OS Energy Performance = Enabled
Energy Performance = Performance
Utilization Profile = Unbalanced
Package C State limit = C0
Stale AtoS = Enabled
IMC Interleaving = 2-way
Sub NUMA Clustering = Disabled
FAN Control = Full
Sysinfo program /home/Benchmark/speccpu2017-fixflags-confirm/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on TX2550M4 Wed Dec 6 19:40:55 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
 1 "physical id"s (chips)
 20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture:        x86_64
CPU op-mode(s):     32-bit, 64-bit
Byte Order:         Little Endian
CPU(s):             20
On-line CPU(s) list: 0-19
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s):          1
NUMA node(s):       1

(Continued on next page)
## SPEC CPU2017 Integer Rate Result

**Fujitsu**
PRIMERGY TX2550 M4, Intel Xeon Silver 4114, 2.20GHz

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**CPU2017 License:** 19  
**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
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**Software Availability:** Sep-2017  
**Tested by:** Fujitsu

### Platform Notes (Continued)

```plaintext
Vendor ID:             GenuineIntel  
CPU family:            6  
Model:                 85  
Model name:            Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz  
Stepping:              4  
CPU MHz:               2829.619  
CPU max MHz:           3000.0000  
CPU min MHz:           800.0000  
BogoMIPS:              4389.70  
Virtualization:        VT-x  
L1d cache:             32K  
L1i cache:             32K  
L2 cache:              1024K  
L3 cache:              14080K  
NUMA node0 CPU(s):     0-19  
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good ntop mm_notify stpmi msr_dts aperfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave xsaveopt xsavec xaxiese xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc
```

From `numactl --hardware`

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19  
node 0 size: 94865 MB  
node 0 free: 94185 MB  
node distances:  
0: 10
```

From `/proc/meminfo`

```
MemTotal:        97141808 kB  
HugePages_Total: 0  
Hugepagesize:    2048 kB
```

From `/usr/bin/lsb_release -d`

```
SUSE Linux Enterprise Server 12 SP2
```

(Continued on next page)
Platform Notes (Continued)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux TX2550M4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64
   x86_64 x86_64 GNU/Linux
run-level 3 Dec 6 19:39

SPEC is set to: /home/Benchmark/speccpu2017-fixflags-confirm
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda4      xfs   889G   23G  867G   3% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   BIOS FUJITSU // American Megatrends Inc. V5.0.0.12 R1.13.0 for D3386-A1x
      11/02/2017
   Memory:
      6x NO DIMM NO DIMM
      6x Samsung M393A2K40BB2-CTD 16 GB 1 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

================================================================================
  CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
       525.x264_r(base, peak) 557.xz_r(base, peak)
================================================================================
  icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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Compiler Version Notes (Continued)
---

CC 500.perlbench_r(peak) 502.gcc_r(peak)
---
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
541.leela_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

FC 548.exchange2_r(base, peak)
---

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

Base Compiler Invocation
---

C benchmarks:
iccc

C++ benchmarks:
icpc

Fortran benchmarks:	ifort
**SPEC CPU2017 Integer Rate Result**

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PRIMERGY TX2550 M4, Intel Xeon Silver 4114, 2.20GHz

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**SPECraten2017_int_base = 44.9**  
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**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
- Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
- qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
- Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
- qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
- Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
- qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
- L/usr/local/je5.0.1-64/lib -ljemalloc

**Base Other Flags**

C benchmarks:
- m64 -std=c11

C++ benchmarks:
- m64

Fortran benchmarks:
- m64
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**Software Availability:** Sep-2017

### Peak Compiler Invocation

- **C benchmarks:**  
  - `icc`
- **C++ benchmarks:**  
  - `icpc`
- **Fortran benchmarks:**  
  - `ifort`

### Peak Portability Flags

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<tr>
<th>Benchmark</th>
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### Peak Optimization Flags

- **C benchmarks:**
  ```
  500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
  -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
  -fno-strict-overflow -L/usr/local/je5.0.1-64/lib  
  -ljemalloc
  ```
  ```
  502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32  
  -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
  -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3  
  -L/usr/local/je5.0.1-32/lib -ljemalloc
  ```
  ```
  505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib  
  -ljemalloc
  ```
  ```
  525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div  
  -qopt-mem-layout-trans=3 -fno-alias
  ```

(Continued on next page)
Peak Optimization Flags (Continued)

525.x264_r (continued):
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass1) -prof-use(pass2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass1) -prof-use(pass2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64

523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
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<tbody>
<tr>
<td><strong>Fujitsu</strong></td>
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<tr>
<td>PRIMERGY TX2550 M4, Intel Xeon Silver 4114, 2.20GHz</td>
<td>SPECrate2017_int_base = 44.9</td>
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<td>SPECrate2017_int_peak = 47.8</td>
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<td>Fujitsu</td>
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You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-12-06 19:40:54-0500.
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