**SPEC® CPU2017 Integer Rate Result**

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
Synergy 480 Gen10  
(2.60 GHz, Intel Xeon Silver 4112)

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<thead>
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**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE  
**Test Date:** Dec-2017  
**Hardware Availability:** Oct-2017  
**Software Availability:** Sep-2017

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<th>500.perlbench_r</th>
<th>16.8</th>
</tr>
</thead>
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<tr>
<td>502.gcc_r</td>
<td>20.3</td>
</tr>
<tr>
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<td>29.0</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>14.4</td>
</tr>
<tr>
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<td>24.3</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>42.9</td>
</tr>
<tr>
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<td>19.1</td>
</tr>
<tr>
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<td>17.2</td>
</tr>
<tr>
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<td>41.1</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>15.7</td>
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**Hardware**

- **CPU Name:** Intel Xeon Silver 4112  
- **Max MHz.:** 3000  
- **Nominal:** 2600  
- **Enabled:** 4 cores, 1 chip, 2 threads/core  
- **Orderable:** 1, 2 chip(s)  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 1 MB I+D on chip per core  
- **Cache L3:** 8.25 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 960 GB SATA SSD, RAID 0  
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 (x86_64) SP2  
- **Kernel:** 4.4.21-69-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++; Fortran: Version 18.0.0.128 of Intel Fortran  
- **Firmware:** HPE BIOS Version I42 released Oct-2017 (tested with I42 9/27/2017)  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1; jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets; jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5; jemalloc: sources available via jemalloc.net
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>copies</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>perbench_r</td>
<td>8</td>
<td>762</td>
<td>16.7</td>
<td>758</td>
<td>16.8</td>
<td>756</td>
<td>16.8</td>
</tr>
<tr>
<td>gcc_r</td>
<td>8</td>
<td>563</td>
<td>20.1</td>
<td>557</td>
<td>20.3</td>
<td>559</td>
<td>20.3</td>
</tr>
<tr>
<td>mcf_r</td>
<td>8</td>
<td>445</td>
<td>29.0</td>
<td>444</td>
<td>29.1</td>
<td>457</td>
<td>28.3</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>8</td>
<td>750</td>
<td>14.0</td>
<td>730</td>
<td>14.4</td>
<td>729</td>
<td>14.4</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>8</td>
<td>351</td>
<td>24.1</td>
<td>348</td>
<td>24.3</td>
<td>348</td>
<td>24.3</td>
</tr>
<tr>
<td>x264_r</td>
<td>8</td>
<td>334</td>
<td>42.0</td>
<td>326</td>
<td>42.9</td>
<td>323</td>
<td>43.3</td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>8</td>
<td>479</td>
<td>19.1</td>
<td>479</td>
<td>19.1</td>
<td>479</td>
<td>19.2</td>
</tr>
<tr>
<td>leela_r</td>
<td>8</td>
<td>782</td>
<td>16.9</td>
<td>771</td>
<td>17.2</td>
<td>764</td>
<td>17.3</td>
</tr>
<tr>
<td>exchange2_r</td>
<td>8</td>
<td>511</td>
<td>41.1</td>
<td>509</td>
<td>41.2</td>
<td>514</td>
<td>40.7</td>
</tr>
<tr>
<td>xz_r</td>
<td>8</td>
<td>550</td>
<td>15.7</td>
<td>551</td>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3>/proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40>/proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0>/proc/sys/kernel/numa_balancing"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
**Platform Notes**

BIOS Configuration:
- Thermal Configuration set to Maximum Cooling
- Memory Patrol Scrubbing set to Disabled
- LLC Prefetch set to Enabled
- LLC Dead Line Allocation set to Disabled
- Workload Profile set to General Throughput Compute
- Minimum Processor Idle Power Core C-State set to C1E State
- Workload Profile set to Custom
- Sub-NUMA Cluster set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-0f29 Thu Dec 7 11:06:42 2017

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
  - 1 "physical id"s (chips)
  - 8 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 4
  - siblings : 8
  - physical 0: cores 1 2 4 5

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 8
- On-line CPU(s) list: 0-7
- Thread(s) per core: 2
- Core(s) per socket: 4
- Socket(s): 1
- NUMA node(s): 1
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
- Stepping: 4
- CPU MHz: 2593.914
- BogoMIPS: 5187.82
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K

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Platform Notes (Continued)

L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-7
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpre pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vmpreedit vsyscall vsysload f1alters idxaes xsaveopt pti msra cmovCLU
cpuaid xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

/proc/cpuinfo cache data
  cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 1 nodes (0)
    node 0 cpus: 0 1 2 3 4 5 6 7
    node 0 size: 193118 MB
    node 0 free: 192257 MB
    node distances:
      node 0
        0: 10

From /proc/meminfo
  MemTotal: 197753048 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
  SUSE Linux Enterprise Server 12 SP2

From /etc/*release*/etc/*version*/
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 2
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
    os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"

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**Platform Notes (Continued)**

```bash
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:  
Linux linux-0f29 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)  
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 7 11:04

SPEC is set to: /home/cpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda4 xfs 852G 27G 826G 4% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret  
this section. The 'dmidecode' program reads system data which is "intended to allow  
hardware to be accurately determined", but the intent may not be met, as there are  
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
BIOS HPE I42 09/27/2017  
Memory:  
12x UNKNOWN NOT AVAILABLE  
12x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)
```

**Compiler Version Notes**

```plaintext
==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base)  
557.xz_r(base)
==============================================================================
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
541.leela_r(base)
==============================================================================
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC  548.exchange2_r(base)

ifort (IFORT) 18.0.0 20170811
```

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Compiler Version Notes (Continued)
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------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1,-z,muldefs -xCORE-AVX -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1,-z,muldefs -xCORE-AVX -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-W1,-z,muldefs -xCORE-AVX -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
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**Base Other Flags**

C benchmarks:
- `-m64`  `-std=c11`

C++ benchmarks:
- `-m64`

Fortran benchmarks:
- `-m64`

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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