**SPEC® CPU2017 Integer Rate Result**

**Dell Inc.**

PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz)

<table>
<thead>
<tr>
<th>SpecRate2017_int_base</th>
<th>93.6</th>
</tr>
</thead>
<tbody>
<tr>
<td>SpecRate2017_int_peak</td>
<td>98.2</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Dec-2017  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017

### Hardware

- **CPU Name:** Intel Xeon Silver 4114  
- **Max MHz.:** 3000  
- **Nominal:** 2200  
- **Enabled:** 20 cores, 2 chips, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 13.75 MB I+D on chip per core  
- **Other:** None  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 960 GB SATA SSD  
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 12 SP3 4.4.73-5-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
  Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 1.0.0 released Aug-2017  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1;  
  jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;  
  jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;  
  jemalloc: sources available via jemalloc.net

---

**500.perlbench_r**  
**502.gcc_r**  
**505.mcf_r**  
**520.omnetpp_r**  
**523.xalancbmk_r**  
**525.x264_r**  
**531.deepsjeng_r**  
**541.leela_r**  
**548.exchange2_r**  
**557.xz_r**
Dell Inc. PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz) SPECrate2017_int_base = 93.6
SPECrate2017_int_peak = 98.2

Results Table

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<td>62.9</td>
<td>684</td>
<td>63.2</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

General Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
Memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>

Platform Notes
BIOS settings:
Sub NUMA Cluster enabled

(Continued on next page)
Dell Inc.

PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz)

SPEC CPU2017 Integer Rate Result

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Dell Inc.

PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz)

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SPECrate2017_int_peak = 98.2

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Platform Notes (Continued)

Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b779c091c0f
running on linux-3.16.15 Wed Dec 20 05:16:44 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
Stepping: 4
CPU MHz: 2194.881
BogoMIPS: 4389.76

(Continued on next page)
## Dell Inc. PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz)

### SPEC CPU2017 Integer Rate Result

**SPECrate2017_int_base = 93.6**  
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<th>Tested by</th>
</tr>
</thead>
</table>

### Platform Notes (Continued)

```plaintext
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 14080K
NUMA node0 CPU(s): 0, 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38
NUMA node1 CPU(s): 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 33, 35, 37, 39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdelsgb rtscrp
lm constant_tsc art arch_performance pebs bts rep_good nopl xtopology nonstop_tsc
aperfmlperf eagerpfi pni pclmulqdq dtst64 monitor ds cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtrr pdcm pcid dca sse4_1  sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnni flexpriority vpt vpid fsstaff tsc_adjust bml1 hle avx2 smep bmi2
ersms invpcid ltd qpm mpq mpq512f mpq512dq mpq512dq rdseed adx smap clflushopt clwb mpq512cd
mpq512bw mpq512bw1 mpq512bw1 xsaveopt xsaveopt xsaveopt xgetbv1 qpm llc qcm_occup_llc pku ospke
```

From `/proc/cpuinfo` cache data  
```
cache size: 14080 KB
```

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a physical chip.  
```
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38
node 0 size: 193062 MB
node 0 free: 192547 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39
node 1 size: 193516 MB
node 1 free: 193189 MB
node distances:
   node 0 1
   0: 10 21
   1: 21 10
```

From `/proc/meminfo`  
```
MemTotal: 395856156 kB
HugePages_Total: 0
HugePagesize: 2048 kB
```

From `/usr/bin/lsb_release -d`  
```
SUSE Linux Enterprise Server 12 SP3
```

From `/etc/*release* /etc/*version*`  
```
SuSE-release:  
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
```

(Continued on next page)
### SPEC CPU2017 Integer Rate Result

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**Software Availability:** Sep-2017

### Platform Notes (Continued)

# This file is deprecated and will be removed in a future service pack or release.  
# Please check /etc/os-release for details about this release.

```plaintext
os-release:
    NAME="SLES"
    VERSION="12-SP3"
    VERSION_ID="12.3"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

```plaintext
uname -a:
    Linux linux-3eu1 4.4.73-5-default #1 SMP Tue Jul 4 15:33:39 UTC 2017 (b7ce4e4) x86_64
    x86_64 x86_64 GNU/Linux
```

```plaintext
run-level 3 Dec 20 05:16 last=5
```

**SPEC is set to:** /home/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>890G</td>
<td>66G</td>
<td>825G</td>
<td>8%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from `dmidecode` follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**BIOS Dell Inc. 1.0.0 08/09/2017**

**Memory:**

- 8x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666, configured at 2400
- 4x 002C0632002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666, configured at 2400
- 12x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2400

(End of data from `sysinfo` program)

---

### Compiler Version Notes

```plaintext
==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
    525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  500.perlbench_r(peak) 502.gcc_r(peak)
```
SPEC CPU2017 Integer Rate Result

Dell Inc.
PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz)

SPECratio2017_int_base = 93.6
SPECratio2017_int_peak = 98.2

CPU2017 License: 55
Test Sponsor: Dell Inc.
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Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.leela_r(base)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
541.leela_r(peak)
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 548.exchange2_r(base, peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
iccc
C++ benchmarks:
icpc
Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r -DSPEC_LP64
505.mcf_r -DSPEC_LP64

(Continued on next page)
## SPEC CPU2017 Integer Rate Result

**Dell Inc.**

**PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz)**

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</table>

| SPECrater2017_int_base = 93.6 |
| SPECrater2017_int_peak = 98.2 |

### Base Portability Flags (Continued)

- `520.omnetpp_r`: `-DSPEC_LP64`
- `523.xalancbmk_r`: `-DSPEC_LP64 -DSPEC_LINUX`
- `525.x264_r`: `-DSPEC_LP64`
- `531.deepsjeng_r`: `-DSPEC_LP64`
- `541.leela_r`: `-DSPEC_LP64`
- `548.exchange2_r`: `-DSPEC_LP64`
- `557.xz_r`: `-DSPEC_LP64`

### Base Optimization Flags

**C benchmarks:**

```bash
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**C++ benchmarks:**

```bash
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

**Fortran benchmarks:**

```bash
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

### Base Other Flags

**C benchmarks:**

```bash
-m64 -std=c11
```

**C++ benchmarks:**

```bash
-m64
```

**Fortran benchmarks:**

```bash
-m64
```

### Peak Compiler Invocation

**C benchmarks:**

```bash
icc
```

(Continued on next page)
Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

(Continued on next page)
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PowerEdge T640 (Intel Xeon Silver 4114, 2.20 GHz)

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Software Availability: Sep-2017

Peak Optimization Flags (Continued)

557.xz_r: Same as 505.mcf_r

C++ benchmarks:
520.omnetpp_r: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-\L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-\L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-\L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):
-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
-m64

523.xalancbmk_r: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
**SPEC CPU2017 Integer Rate Result**

Dell Inc.  
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<tr>
<td>93.6</td>
<td>98.2</td>
</tr>
</tbody>
</table>

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  
Test Date: Dec-2017  
Hardware Availability: Sep-2017  
Software Availability: Sep-2017

You can also download the XML flags sources by saving the following links:


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