## SPEC CPU®2017 Floating Point Speed Result

### Cisco Systems
Cisco UCS C220 M5 (Intel Xeon Bronze 3106, 1.70 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_peak</th>
<th>SPECspeed®2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>46.5</td>
<td>46.0</td>
</tr>
</tbody>
</table>

### CPU2017 License: 9019

- Test Sponsor: Cisco Systems
- Tested by: Cisco Systems
- Test Date: Dec-2017
- Hardware Availability: Aug-2017
- Software Availability: Sep-2017

### Hardware
- **CPU Name:** Intel Xeon Bronze 3106
- **Max MHz:** 1700
- **Nominal:** 1700
- **Enabled:** 16 cores, 2 chips
- **Orderable:** 1,2 Chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 11 MB I+D on chip per chip
- **Memory:** 384 GB (24 x 16 GB 2Rx4 PC4-2666V-R, running at 2133)
- **Storage:** 1 x 600 GB SAS HDD, 10K RPM
- **Other:** None

### Software
- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.21-69-default
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Version 3.1.1d released Jun-2017
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None
- **Power Management:** --

### Benchmarks

| Benchmark  | Threads | 0  | 15.0 | 30.0 | 45.0 | 60.0 | 75.0 | 90.0 | 105 | 120 | 135 | 150 | 165 | 180 | 195 | 210 | 225 | 240 | 255 | 270 | 285 | 290 |
|------------|---------|----|------|------|------|------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 603.bwaves_s | 16      |    |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 607.cactuBSSN_s | 16     |    |      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 619.lbm_s      | 16      | 30.3|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 621.wrf_s      | 16      | 32.8|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 627.cam4_s     | 16      | 17.6|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 628.pop2_s     | 16      | 33.3|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 638.imagick_s  | 16      | 29.2|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 644.nab_s      | 16      | 52.6|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 649.fotonik3d_s| 16      | 33.9|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
| 654.roms_s     | 16      | 55.8|      |      |      |      |      |      |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |     |
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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
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<tbody>
<tr>
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<td>204</td>
<td>290</td>
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<td>289</td>
<td>204</td>
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<tr>
<td>607.cactuBSSN_s</td>
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<td>619.lbm_s</td>
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<td>173</td>
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<td>30.1</td>
<td>173</td>
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<td>621.wrf_s</td>
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<td>55.8</td>
<td>285</td>
<td>55.3</td>
<td>282</td>
<td>55.9</td>
<td>266</td>
<td>59.1</td>
</tr>
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</table>

### Operating System Notes

- Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

- Environment variables set by runcpu before the start of the run:
  - KMP_AFFINITY = "granularity=fine,compact"
  - LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
  - OMP_STACKSIZE = "192M"

- Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:
  - sync; echo 3> /proc/sys/vm/drop_caches

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
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SPECspeed®2017_fp_base = 46.0
SPECspeed®2017_fp_peak = 46.5

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General Notes (Continued)

generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS Settings:
- CPU performance set to Enterprise
- Power Performance Tuning set to OS Controls
- SNC set to Disabled
- Patrol Scrub set to Disabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
- running on linux-79ix Sun Dec 17 15:26:26 2017

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
- 2 "physical id"s (chips)
- 16 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 8
  - siblings: 8
  - physical 0: cores 0 1 2 3 4 5 6 7
  - physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 16
- On-line CPU(s) list: 0-15
- Thread(s) per core: 1
- Core(s) per socket: 8
- Socket(s): 2
- NUMA node(s): 2

(Continued on next page)
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Tested by: Cisco Systems

Platform Notes (Continued)

Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
Stepping:              4
CPU MHz:               888.195
CPU max MHz:           1700.0000
CPU min MHz:           800.0000
BogoMIPS:              3392.02
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              11264K
NUMA node0 CPU(s):     0-7
NUMA node1 CPU(s):     8-15
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                       rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                       nonstop_tsc aperfmprefp eagerfpui pni pclmulqdq dttes64 monitor ds_cpl vmx
                       smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                       x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                       lahf_lm abm 3nowprefetch arat epb pni pts dtherm hwp
                       hwp_act_window hwp_epp hwp_pkg_req intel_pt tpr_shadow vnmi.flexpriority
                       ept vpid fsgsbase tsc_adjust bm1 hle avx2 smep bmi2 erts invpcid rtm
cqm mpx avx512f
                       avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw
                       avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

From /proc/cpuinfo cache data
    cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7
    node 0 size: 192019 MB
    node 0 free: 187924 MB
    node 1 cpus: 8 9 10 11 12 13 14 15
    node 1 size: 193384 MB
    node 1 free: 189535 MB
    node distances:
    node 0 1
    0:  10  21
    1:  21  10

From /proc/meminfo
    MemTotal:     394653928 kB
    HugePages_Total:      0

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Cisco Systems
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**Platform Notes (Continued)**

```
Hugepagesize:  2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP2"
    VERSION_ID="12.2"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
    Linux linux-79ix 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
    x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Dec 16 19:00
```

**Compiler Version Notes**

```
C
  619.ibm_s(base, peak) 638.imagick_s(base, peak)
  644.nab_s(base, peak)
```

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Compiler Version Notes (Continued)

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
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**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

**Base Other Flags**

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

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### Base Other Flags (Continued)

Benchmarks using both Fortran and C:
- `-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11`

### Peak Compiler Invocation

<table>
<thead>
<tr>
<th>C benchmarks:</th>
<th>ifort icc</th>
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<td>Benchmarks using Fortran, C, and C++:</td>
<td>icpc icc ifort</td>
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### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

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<td><code>619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP</code></td>
</tr>
<tr>
<td><code>638.imagick_s: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP</code></td>
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**Peak Optimization Flags (Continued)**

Fortran benchmarks:
- `-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP`
- `-DSPEC_OPENMP -O2 -xCORE-AVX512 -qopt-prefetch -ipo -O3`
- `-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp`
- `-nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:

621.wrf_s
- `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512`
- `-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div`
- `-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp`
- `-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte`

627.cam4_s
- `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
- `-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp`
- `-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte`

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
- `-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX512 -qopt-prefetch`
- `-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3`
- `-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs`
- `-align array32byte`

**Peak Other Flags**

C benchmarks:
- `-m64 -std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11`

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
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</table>

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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