Lenovo Global Technology
ThinkSystem ST550
(1.80 GHz, Intel Xeon Silver 4108)

SPECspeed2017_fp_base = 56.2
SPECspeed2017_fp_peak = 57.4

Hardware

- **CPU Name:** Intel Xeon Silver 4108
- **Max MHz.:** 3000
- **Nominal:** 1800
- **Enabled:** 16 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 11 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)
- **Storage:** 1 x 800 GB SAS SSD
- **Other:** None

Software

- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)
- **Kernel:** 4.4.21-69-default
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
  Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Lenovo BIOS Version 00E107W 1.01 released Aug-2017
- **File System:** btrfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** None

**Test Date:** Nov-2017
**Hardware Availability:** Aug-2017
**Software Availability:** Sep-2017

---

**Threads**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed2017_fp_base</th>
<th>SPECspeed2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>16</td>
<td>56.2</td>
<td>57.4</td>
</tr>
</tbody>
</table>
## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>191</td>
<td>309</td>
<td>191</td>
<td>309</td>
<td>190</td>
<td>310</td>
<td>16</td>
<td>191</td>
<td>309</td>
<td>190</td>
<td>310</td>
<td>191</td>
<td>310</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>258</td>
<td>64.6</td>
<td>258</td>
<td>64.6</td>
<td>258</td>
<td>64.6</td>
<td>16</td>
<td>252</td>
<td>66.1</td>
<td>252</td>
<td>66.2</td>
<td>253</td>
<td>66.0</td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>16</td>
<td>168</td>
<td>31.2</td>
<td>167</td>
<td>31.3</td>
<td>167</td>
<td>31.3</td>
<td>16</td>
<td>171</td>
<td>30.7</td>
<td>167</td>
<td>31.4</td>
<td>170</td>
<td>30.8</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>16</td>
<td>291</td>
<td>45.4</td>
<td>292</td>
<td>45.3</td>
<td>291</td>
<td>45.4</td>
<td>16</td>
<td>266</td>
<td>49.6</td>
<td>267</td>
<td>49.6</td>
<td>266</td>
<td>49.7</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>16</td>
<td>297</td>
<td>29.9</td>
<td>296</td>
<td>29.9</td>
<td>296</td>
<td>29.9</td>
<td>16</td>
<td>296</td>
<td>29.9</td>
<td>296</td>
<td>29.9</td>
<td>296</td>
<td>29.9</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>16</td>
<td>289</td>
<td>41.0</td>
<td>290</td>
<td>41.0</td>
<td>289</td>
<td>41.1</td>
<td>16</td>
<td>276</td>
<td>43.0</td>
<td>273</td>
<td>43.4</td>
<td>276</td>
<td>43.0</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>16</td>
<td>387</td>
<td>37.2</td>
<td>387</td>
<td>37.3</td>
<td>387</td>
<td>37.3</td>
<td>16</td>
<td>387</td>
<td>37.3</td>
<td>387</td>
<td>37.2</td>
<td>389</td>
<td>37.1</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>16</td>
<td>261</td>
<td>67.0</td>
<td>261</td>
<td>67.0</td>
<td>262</td>
<td>66.8</td>
<td>16</td>
<td>261</td>
<td>67.1</td>
<td>261</td>
<td>67.0</td>
<td>261</td>
<td>67.1</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>16</td>
<td>152</td>
<td>60.0</td>
<td>153</td>
<td>59.6</td>
<td>152</td>
<td>60.0</td>
<td>16</td>
<td>155</td>
<td>58.7</td>
<td>155</td>
<td>58.7</td>
<td>155</td>
<td>58.7</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>16</td>
<td>261</td>
<td>60.3</td>
<td>260</td>
<td>60.7</td>
<td>259</td>
<td>60.7</td>
<td>16</td>
<td>239</td>
<td>66.0</td>
<td>238</td>
<td>66.2</td>
<td>239</td>
<td>65.8</td>
</tr>
</tbody>
</table>

**SPECspeed2017_fp_base = 56.2**  
**SPECspeed2017_fp_peak = 57.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance  
Hyper-Threading set to Disable  
Adjacent Cache Prefetch set to Disable  
DCU Streamer Prefetcher set to Disable  
DCA set to Enable  
Uncore Frequency Scaling set to Disable  
MONITORMWAIT set to Enable  
XFT Prefetcher set to Enable
Lenovo Global Technology
ThinkSystem ST550
(1.80 GHz, Intel Xeon Silver 4108)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECspeed2017_fp_base = 56.2
SPECspeed2017_fp_peak = 57.4

Platform Notes (Continued)

Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on ST550 Thu Nov 2 13:10:00 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
  2 "physical id"s (chips)
  16 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

  cpu cores : 8
  siblings : 8
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 16
  On-line CPU(s) list: 0-15
  Thread(s) per core: 1
  Core(s) per socket: 8
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  CPU family: 6
  Model: 85
  Model name: Intel(R) Xeon(R) Silver 4108 CPU @ 1.80GHz
  Stepping: 4
  CPU MHz: 1795.775
  BogoMIPS: 3591.55
  Virtualization: VT-x
  L1d cache: 32K
  L1i cache: 32K
  L2 cache: 1024K
  L3 cache: 11264K
  NUMA node0 CPU(s): 0-7
  NUMA node1 CPU(s): 8-15
  Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
  pat pse36clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdscp
  lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
  aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
(Continued on next page)
Platform Notes (Continued)

`fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust bm11 hle avx2 smep bmi2
ermv invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsaveopt xgetbv1 cqm_llc cqm_occup_llc`

`/proc/cpuinfo cache data`
`cache size : 11264 KB`

From `numactl --hardware` WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7
node 0 size: 193111 MB
node 0 free: 190730 MB
node 1 cpus: 8 9 10 11 12 13 14 15
node 1 size: 193504 MB
node 1 free: 192211 MB
node distances:
node   0   1
0:  10  21
1:  21  10

From `/proc/meminfo`
` MemTotal:       395894372 kB`
` HugePages_Total:       0`
` Hugepagesize:       2048 kB`

From `/etc/*release*` `/etc/*version*`
` SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP2"
    VERSION_ID="12.2"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp2"

  uname -a:
  Linux ST550 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64
  x86_64 x86_64 GNU/Linux`

(Continued on next page)
Lenovo Global Technology
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SPECSpeed2017_fp_peak = 57.4

CPU2017 License: 9017
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Test Date: Nov-2017
Tested by: Lenovo Global Technology
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

run-level 3 Nov 2 02:09

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystem     Type   Size  Used Avail Use% Mounted on
/dev/sdb2      btrfs  744G  109G  635G  15% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[O0E107W-1.01]- 08/11/2017
Memory:
12x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| CC   619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak) □ |
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
| CC   619.lbm_s (peak) □ |
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
| FC  607.cactuBSSN_s(base) |
==============================================================================
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================
| FC  607.cactuBSSN_s (peak) |
(Continued on next page)
Lenovo Global Technology
ThinkSystem ST550
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SPEC CPU2017 Floating Point Speed Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

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Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Nov-2017
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Software Availability: Sep-2017

Compiler Version Notes (Continued)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC   603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC   621.wrf_s(peak) 628.pop2_s(peak)
==============================================================================
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
  icc

(Continued on next page)
Lenovo Global Technology
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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

(Continued on next page)
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Test Date: Nov-2017
Tested by: Lenovo Global Technology
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
- nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
- m64 -std=c11
Fortran benchmarks:
- m64

Benchmarks using both Fortran and C:
- m64 -std=c11

Benchmarks using Fortran, C, and C++:
- m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc
Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags
Lenovo Global Technology
ThinkSystem ST550
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Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Nov-2017
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Software Availability: Sep-2017

Peak Optimization Flags

C benchmarks:
619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OpenMP

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OpenMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OpenMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OpenMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OpenMP -nostandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OpenMP -nostandard-realloc-lhs
-alignment array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

(Continued on next page)
## Lenovo Global Technology

**ThinkSystem ST550**  
(1.80 GHz, Intel Xeon Silver 4108)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
<th>56.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_fp_peak</td>
<td>57.4</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>Lenovo Global Technology</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

### CPU2017 License
9017

### Test Date
Nov-2017

### Hardware Availability
Aug-2017

### Software Availability
Sep-2017

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### Peak Other Flags (Continued)

**Fortran benchmarks:**

- `-m64`

**Benchmarks using both Fortran and C:**

- `-m64 -std=c11`

**Benchmarks using Fortran, C, and C++:**

- `-m64 -std=c11`

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The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-11-02 01:10:00-0400.  
Originally published on 2018-01-10.