



# SPEC® CPU2017 Integer Rate Result

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## Fujitsu

PRIMERGY RX2520 M4, Intel Xeon Gold 5120  
2.20GHz

SPECrate2017\_int\_base = 135

SPECrate2017\_int\_peak = Not Run

CPU2017 License: 19

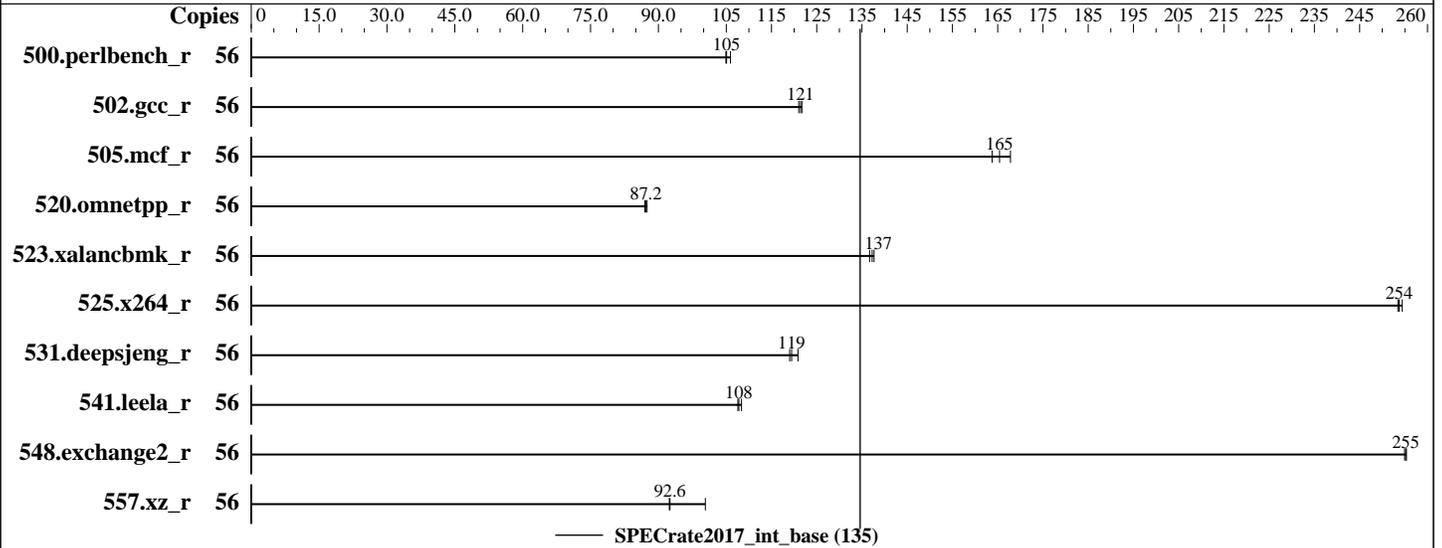
Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Nov-2017

Hardware Availability: Nov-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Gold 5120  
 Max MHz.: 3200  
 Nominal: 2200  
 Enabled: 28 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 19.25 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
 Storage: 384 GB tmpfs  
 Other: 1 x SATA HDD, 1000 GB, 7200 RPM, used for swap

### Software

OS: SUSE Linux Enterprise Server 12 SP2 4.4.21-69-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Fujitsu BIOS Version V5.0.0.12 R1.13.0 for D3386-A1x. Released Nov-2017  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: jemalloc: jemalloc memory allocator library V5.0.1;  
 jemalloc: configured and built at default for 32bit (i686) and 64bit (x86\_64) targets;  
 jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;  
 jemalloc: sources available from jemalloc.net or releases



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	56	850	105	842	106	<b>848</b>	<b>105</b>							
502.gcc_r	56	651	122	655	121	<b>653</b>	<b>121</b>							
505.mcf_r	56	539	168	<b>547</b>	<b>165</b>	553	164							
520.omnetpp_r	56	840	87.5	<b>842</b>	<b>87.2</b>	845	87.0							
523.xalancbmk_r	56	<b>431</b>	<b>137</b>	430	138	433	137							
525.x264_r	56	387	253	385	254	<b>386</b>	<b>254</b>							
531.deepsjeng_r	56	531	121	<b>537</b>	<b>119</b>	539	119							
541.leela_r	56	856	108	<b>860</b>	<b>108</b>	862	108							
548.exchange2_r	56	575	255	<b>575</b>	<b>255</b>	576	255							
557.xz_r	56	603	100	<b>653</b>	<b>92.6</b>	654	92.4							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Set Kernel Boot Parameter: nohz_full=1-55
Set CPU frequency governor to maximum performance with:
cpupower -c all frequency-set -g performance
Set tmpfs filesystem with:
mkdir /home/memory
mount -t tmpfs -o size=384g,rw tmpfs /home/memory
Process tuning settings:
echo 10000000 > /proc/sys/kernel/sched_min_granularity_ns
echo 15000000 > /proc/sys/kernel/sched_wakeup_granularity_ns
echo 0 > /proc/sys/kernel/numa_balancing
cpu idle state set with:
cpupower idle-set -d 1
cpupower idle-set -d 2
```

## General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/memory/speccpu/lib/ia32:/home/memory/speccpu/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/memory/speccpu/je5.0.1-32:/home/memory/speccpu/je5.0.1-64"
```

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## General Notes (Continued)

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3 > /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

## Platform Notes

BIOS configuration:  
HWPM Support = Disabled  
Intel Virtualization Technology = Disabled  
Link Frequency Select = 10.4 GT/s  
Sub NUMA Clustering = Enabled  
IMC Interleaving = 1-way  
LLC Dead Line Alloc = Disabled  
Stale AtoS = Enabled  
Sysinfo program /home/memory/speccpu/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on RX2520M4 Sun Nov 26 13:56:34 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz  
2 "physical id"s (chips)  
56 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following  
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 14  
siblings : 28  
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14  
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 56  
On-line CPU(s) list: 0-55  
Thread(s) per core: 2

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### Platform Notes (Continued)

```

Core(s) per socket:      14
Socket(s):               2
NUMA node(s):           4
Vendor ID:               GenuineIntel
CPU family:              6
Model:                   85
Model name:              Intel(R) Xeon(R) Gold 5120 CPU @ 2.20GHz
Stepping:                4
CPU MHz:                 2201.000
CPU max MHz:             2201.0000
CPU min MHz:             1000.0000
BogoMIPS:                4389.67
Virtualization:          VT-x
L1d cache:               32K
L1i cache:               32K
L2 cache:                1024K
L3 cache:                19712K
NUMA node0 CPU(s):      0-3,7-9,28-31,35-37
NUMA node1 CPU(s):      4-6,10-13,32-34,38-41
NUMA node2 CPU(s):      14-17,21-23,42-45,49-51
NUMA node3 CPU(s):      18-20,24-27,46-48,52-55
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc

```

```

/proc/cpuinfo cache data
cache size : 19712 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 9 28 29 30 31 35 36 37
node 0 size: 95023 MB
node 0 free: 85547 MB
node 1 cpus: 4 5 6 10 11 12 13 32 33 34 38 39 40 41
node 1 size: 96760 MB
node 1 free: 96354 MB
node 2 cpus: 14 15 16 17 21 22 23 42 43 44 45 49 50 51
node 2 size: 96760 MB
node 2 free: 96356 MB
node 3 cpus: 18 19 20 24 25 26 27 46 47 48 52 53 54 55

```

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### Platform Notes (Continued)

```
node 3 size: 96624 MB
node 3 free: 96288 MB
node distances:
node  0  1  2  3
  0:  10  11  21  21
  1:  11  10  21  21
  2:  21  21  10  11
  3:  21  21  11  10
```

```
From /proc/meminfo
MemTotal:          394412872 kB
HugePages_Total:      0
Hugepagesize:       2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux RX2520M4 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 23 04:43
```

```
SPEC is set to: /home/memory/speccpu
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs           tmpfs    384G   8.8G  376G   3% /home/memory
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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### Platform Notes (Continued)

BIOS FUJITSU // American Megatrends Inc. V5.0.0.12 R1.13.0 for D3386-A1x  
11/02/2017

Memory:

12x Micron 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

### Compiler Version Notes

=====  
CC 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base)  
557.xz\_r(base)

-----  
icc (ICC) 18.0.0 20170811

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-----

=====  
CXXC 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
541.leela\_r(base)

-----  
icpc (ICC) 18.0.0 20170811

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-----

=====  
FC 548.exchange2\_r(base)

-----  
ifort (IFORT) 18.0.0 20170811

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### Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort



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## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevD.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevD.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2017-11-26 13:56:33-0500.

Report generated on 2018-10-31 17:14:01 by CPU2017 PDF formatter v6067.

Originally published on 2018-01-13.