# SPEC® CPU2017 Integer Speed Result

**Lenovo Global Technology**

ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)

<table>
<thead>
<tr>
<th></th>
<th>SPECspeed2017_int_peak = 8.74</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_int_base = 8.49</td>
<td></td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Dec-2017

**Hardware**

- **CPU Name:** Intel Xeon Platinum 8156  
- **Max MHz.:** 3700  
- **Nominal:** 3600  
- **Enabled:** 16 cores, 4 chips  
- **Orderable:** 2,4 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 16.5 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)  
- **Storage:** 1 x 800 GB SAS SSD  
- **Other:** None

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
- **Kernel:** 4.4.21-69-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
- **Compiler for Linux:**  
- **Fortran:** Version 18.0.0.128 of Intel Fortran  
- **Compiler for Linux:**  
- **Parallel:** Yes  
- **Firmware:** Lenovo BIOS Version PSE103K 1.00 released Jun-2017  
- **File System:** btrfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:**  
  - jemalloc: jemalloc memory allocator library V5.0.1;  
  - jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;  
  - jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;  
  - jemalloc: sources available from jemalloc.net or releases

---

<table>
<thead>
<tr>
<th>Thread</th>
<th>Threads</th>
<th>SPECspeed2017_int_base (8.49)</th>
<th>SPECspeed2017_int_peak (8.74)</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>600.perlbench_s</strong></td>
<td>16</td>
<td>7.24</td>
<td>8.81</td>
</tr>
<tr>
<td><strong>602.gcc_s</strong></td>
<td>16</td>
<td>9.11</td>
<td>10.6</td>
</tr>
<tr>
<td><strong>605.mcf_s</strong></td>
<td>16</td>
<td>5.43</td>
<td>10.7</td>
</tr>
<tr>
<td><strong>620.omnetpp_s</strong></td>
<td>16</td>
<td>5.40</td>
<td>9.08</td>
</tr>
<tr>
<td><strong>623.xalancbmk_s</strong></td>
<td>16</td>
<td>9.91</td>
<td>11.6</td>
</tr>
<tr>
<td><strong>625.x264_s</strong></td>
<td>16</td>
<td>11.6</td>
<td>5.04</td>
</tr>
<tr>
<td><strong>631.deepsjeng_s</strong></td>
<td>16</td>
<td>8.62</td>
<td>4.33</td>
</tr>
<tr>
<td><strong>641.leela_s</strong></td>
<td>16</td>
<td>4.35</td>
<td>13.4</td>
</tr>
<tr>
<td><strong>648.exchange2_s</strong></td>
<td>16</td>
<td>13.4</td>
<td>20.4</td>
</tr>
<tr>
<td><strong>657.xz_s</strong></td>
<td>16</td>
<td>20.5</td>
<td>20.4</td>
</tr>
</tbody>
</table>

---

**CPU2017 License:** 9017
**Test Sponsor:** Lenovo Global Technology
**Hardware Availability:** Sep-2017
**Software Availability:** Sep-2017
**CPU Name:** Intel Xeon Platinum 8156
**Max MHz.:** 3700
**Nominal:** 3600
**Enabled:** 16 cores, 4 chips
**Orderable:** 2,4 chips
**Cache L1:** 32 KB I + 32 KB D on chip per core
**L2:** 1 MB I+D on chip per core
**L3:** 16.5 MB I+D on chip per chip
**Other:** None
**Memory:** 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
**Storage:** 1 x 800 GB SAS SSD
**Other:** None

**OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)
**Kernel:** 4.4.21-69-default
**Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++
**Compiler for Linux:**
**Fortran:** Version 18.0.0.128 of Intel Fortran
**Compiler for Linux:**
**Parallel:** Yes
**Firmware:** Lenovo BIOS Version PSE103K 1.00 released Jun-2017
**File System:** btrfs
**System State:** Run level 3 (multi-user)
**Base Pointers:** 64-bit
**Peak Pointers:** 32/64-bit
**Other:**
  - jemalloc: jemalloc memory allocator library V5.0.1;
  - jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
  - jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
  - jemalloc: sources available from jemalloc.net or releases
Lenovo Global Technology
ThinkSystem SR950
(3.60 GHz, Intel Xeon Platinum 8156)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Base</td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>600.perlenbench_s</td>
<td>16</td>
<td>292</td>
<td>6.08</td>
<td>292</td>
<td>6.07</td>
<td>291</td>
<td>6.09</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>16</td>
<td>452</td>
<td>8.80</td>
<td>452</td>
<td>8.81</td>
<td>450</td>
<td>8.85</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>16</td>
<td>444</td>
<td>10.6</td>
<td>441</td>
<td>10.7</td>
<td>443</td>
<td>10.6</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>300</td>
<td>5.43</td>
<td>300</td>
<td>5.43</td>
<td>311</td>
<td>5.24</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>16</td>
<td>156</td>
<td>9.08</td>
<td>156</td>
<td>9.10</td>
<td>156</td>
<td>9.08</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>16</td>
<td>153</td>
<td>11.5</td>
<td>152</td>
<td>11.6</td>
<td>152</td>
<td>11.6</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>16</td>
<td>284</td>
<td>5.04</td>
<td>284</td>
<td>5.04</td>
<td>284</td>
<td>5.04</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>16</td>
<td>394</td>
<td>4.33</td>
<td>394</td>
<td>4.33</td>
<td>394</td>
<td>4.33</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>221</td>
<td>13.3</td>
<td>220</td>
<td>13.4</td>
<td>220</td>
<td>13.4</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>16</td>
<td>303</td>
<td>20.4</td>
<td>307</td>
<td>20.1</td>
<td>303</td>
<td>20.4</td>
</tr>
</tbody>
</table>

SPECspeed2017_int_base = 8.49
SPECspeed2017_int_peak = 8.74

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**General Notes**

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)

is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(3.60 GHz, Intel Xeon Platinum 8156)

| SPECspeed2017_int_base | 8.49 |
| SPECspeed2017_int_peak | 8.74 |

| CPU2017 License: | 9017 |
| Test Sponsor: | Lenovo Global Technology |
| Tested by: | Lenovo Global Technology |
| Test Date: | Dec-2017 |
| Hardware Availability: | Sep-2017 |
| Software Availability: | Sep-2017 |

**General Notes (Continued)**

generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, [http://www.spec.org/osg/policy.html](http://www.spec.org/osg/policy.html)

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

**Platform Notes**

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable
MONITOR\WAIT set to Enable
DCU Streamer Prefetcher set to Disable
XPT Prefetcher set to Enable
Stale AtoS set to Enable
DCA set to Enable
Trusted Execution Technology set to Enable
LLC Deadline Alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b091c0f
running on linux-uyxw Sun Dec 17 20:02:22 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
  4 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 4
  siblings  : 4
  physical 0: cores 1 2 5 11
  physical 1: cores 1 5 9 13
  physical 2: cores 1 5 9 13
  physical 3: cores 1 2 5 11
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
```

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(3.60 GHz, Intel Xeon Platinum 8156)

**SPEC CPU2017 Integer Speed Result**

**Copyright 2017-2018 Standard Performance Evaluation Corporation**

**Lenovo Global Technology**

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Test Date:** Dec-2017  
**Hardware Availability:** Sep-2017  
**Tested by:** Lenovo Global Technology  
**Software Availability:** Sep-2017

**SPECspeed2017_int_base** = 8.49  
**SPECspeed2017_int_peak** = 8.74

---

**Platform Notes (Continued)**

```plaintext
CPU(s): 16  
On-line CPU(s) list: 0-15  
Thread(s) per core: 1  
Core(s) per socket: 4  
Socket(s): 4  
NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 85  
Model name: Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz  
Stepping: 4  
CPU Mhz: 3591.575  
BogoMIPS: 7183.15  
Virtualization: VT-x  
L1d cache: 32K  
L1i cache: 32K  
L2 cache: 1024K  
L3 cache: 16896K  
NUMA node0 CPU(s): 0-3  
NUMA node1 CPU(s): 4-7  
NUMA node2 CPU(s): 8-11  
NUMA node3 CPU(s): 12-15  
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl apic sm mce pse36 kt debug mtrr nonstop_tsc aperfmperf eagerfpu nptialum qdts64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epbpls dtherm intel_pt tpr_shadow vmx flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2  
emms invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512v1 xsaveopt xsaveopt xgetbv1 cqm_llc cqm_occup_llc
```

/proc/cpuinfo cache data  
```plaintext
cache size : 16896 KB
```

From numactl --hardware  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
```plaintext
available: 4 nodes (0-3)  
node 0 cpus: 0 1 2 3  
node 0 size: 386500 MB  
node 0 free: 384483 MB  
node 1 cpus: 4 5 6 7  
node 1 size: 387042 MB  
node 1 free: 385973 MB  
node 2 cpus: 8 9 10 11  
node 2 size: 387042 MB  
node 2 free: 385881 MB
```

---

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017_int_base = 8.49
SPECspeed2017_int_peak = 8.74

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

node 3 cpus: 12 13 14 15
node 3 size: 387038 MB
node 3 free: 385517 MB
node distances:
node 0 1 2 3
0: 10 21 21 21
1: 21 10 21 21
2: 21 21 10 21
3: 21 21 21 10

From /proc/meminfo
MemTotal: 1584767360 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /
SuSE-release:
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-uyxw 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 16 12:31

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[PSE103K-1.00]- 06/19/2017
Memory:

(Continued on next page)
### SPEC CPU2017 Integer Speed Result

**Lenovo Global Technology**  
ThinkSystem SR950  
(3.60 GHz, Intel Xeon Platinum 8156)  

<table>
<thead>
<tr>
<th>CPU2017 License: 9017</th>
<th>SPECspeed2017_int_base = 8.49</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Lenovo Global Technology</td>
<td>SPECspeed2017_int_peak = 8.74</td>
</tr>
<tr>
<td>Tested by: Lenovo Global Technology</td>
<td>Test Date: Dec-2017</td>
</tr>
<tr>
<td>Hardware Availability: Sep-2017</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

#### Platform Notes (Continued)

- 48x NO DIMM NO DIMM  
- 48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666  

(End of data from sysinfo program)

#### Compiler Version Notes

```
==============================================================================
CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base,  
    peak) 657.xz_s(base)  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
==============================================================================
CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
==============================================================================
CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)  
    641.leela_s(base)  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
==============================================================================
CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)  
    641.leela_s(peak)  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
==============================================================================
FC  648.exchange2_s(base, peak)  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
==============================================================================
```
## Lenovo Global Technology

**ThinkSystem SR950**  
(3.60 GHz, Intel Xeon Platinum 8156)

### SPEC CPU2017 Integer Speed Result

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECspeed2017_int_base</th>
<th>SPECspeed2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>8.49</td>
<td>8.74</td>
</tr>
</tbody>
</table>

### Base Compiler Invocation

- **C benchmarks:**  
  - `icc`
- **C++ benchmarks:**  
  - `icpc`
- **Fortran benchmarks:**  
  - `ifort`

### Base Portability Flags

- `600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `602.gcc_s: -DSPEC_LP64`
- `605.mcf_s: -DSPEC_LP64`
- `620.omnetpp_s: -DSPEC_LP64`
- `623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX`
- `625.x264_s: -DSPEC_LP64`
- `631.deepsjeng_s: -DSPEC_LP64`
- `641.leela_s: -DSPEC_LP64`
- `648.exchange2_s: -DSPEC_LP64`
- `657.xz_s: -DSPEC_LP64`

### Base Optimization Flags

- **C benchmarks:**  
  - `-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
  - `-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP`
  - `-L/usr/local/je5.0.1-64/lib -ljemalloc`
- **C++ benchmarks:**  
  - `-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
  - `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`
- **Fortran benchmarks:**  
  - `-W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
  - `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
  - `-L/usr/local/je5.0.1-64/lib -ljemalloc`
SPEC CPU2017 Integer Speed Result

Lenovo Global Technology
ThinkSystem SR950
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017_int_base = 8.49
SPECspeed2017_int_peak = 8.74

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Dec-2017
Tested by: Lenovo Global Technology
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Base Other Flags

C benchmarks:
-m64 -std=c11
C++ benchmarks:
-m64
Fortran benchmarks:
-m64

Peak Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc
Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR950
(3.60 GHz, Intel Xeon Platinum 8156)

SPECspeed2017_int_base = 8.49
SPECspeed2017_int_peak = 8.74

Lenovo Global Technology
Lenovo Global Technology
Lenovo Global Technology

Copyright 2017-2018 Standard Performance Evaluation Corporation

Peak Optimization Flags (Continued)

600.perlbench_s (continued):
  -DSPEC_OPENMP -fno-strict-overflow
  -L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
  -xCORE-AVX512 -qopt-mem-layout-trans=3 -ipo -O3
  -no-prec-div -DSPEC_SUPPRESS_OPENMP -gopenmp
  -DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
  -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
  -L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
  -L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
  -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -DSPEC_SUPPRESS_OPENMP -gopenmp -DSPEC_OPENMP
  -L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: Same as 620.omnetpp_s

641.leela_s: Same as 620.omnetpp_s

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
  -L/usr/local/je5.0.1-64/lib -ljemalloc
Lenovo Global Technology
ThinkSystem SR950
(3.60 GHz, Intel Xeon Platinum 8156)

SPEC CPU2017 Integer Speed Result

SPECspeed2017_int_base = 8.49
SPECspeed2017_int_peak = 8.74

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks (except as noted below):
-m64

623.xalancbmk_s: -m32

Fortran benchmarks:
-m64

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-A.xml

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-12-17 07:02:21-0500.
Report generated on 2018-10-31 16:45:54 by CPU2017 PDF formatter v6067.
Originally published on 2018-03-06.