### Lenovo Global Technology

**Lenovo System x3650 M5**  
(2.40 GHz, Intel Xeon E5-2699A v4)

---

<table>
<thead>
<tr>
<th>Test Sponsor:</th>
<th>Lenovo Global Technology</th>
</tr>
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<tbody>
<tr>
<td>Tested by:</td>
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<tr>
<td>CPU2017 License:</td>
<td>9017</td>
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<td>Test Date:</td>
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<td>Hardware Availability:</td>
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<td>Software Availability:</td>
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**SPECspeed2017_fp_base** = 92.7  
**SPECspeed2017_fp_peak** = 94.5

---

**Threads**  
<table>
<thead>
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<td>644.nab_s</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>44</td>
</tr>
<tr>
<td>654.roms_s</td>
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</table>

---

**Hardware**

- **CPU Name:** Intel Xeon E5-2699A v4  
- **Max MHz.:** 3600  
- **Nominal:** 2400  
- **Enabled:** 44 cores, 2 chips  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 256 KB I+D on chip per core  
- **Cache L3:** 55 MB I+D on chip per chip  
- **Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
- **Storage:** 1 x 480 GB SAS SSD  
- **Other:** None

---

**Software**

- **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
- **Kernel:** 4.4.21-69-default  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
- **Compiler for Linux:**  
- **Fortran:** Version 18.0.0.128 of Intel Fortran  
- **Compiler for Linux:**  
- **Parallel:** Yes  
- **Firmware:** Lenovo BIOS Version TCE126O 2.20 released Sep-2016  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
## Lenovo Global Technology

**Lenovo System x3650 M5**  
(2.40 GHz, Intel Xeon E5-2699A v4)

**SPEC CPU2017 Floating Point Speed Result**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>654.roms_s</td>
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<td>163</td>
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<td>97.6</td>
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</table>

**SPECspeed2017_fp_base** = 92.7  
**SPECspeed2017_fp_peak** = 94.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

*Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
- LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
- OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4 

Transparent Huge Pages enabled by default 

Prior to runcpu invocation 

Filesystem page cache synced and cleared with: 

```
sync; echo 3> /proc/sys/vm/drop_caches```

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented. 

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented. 

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented. 

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page. 

The system as described on this result page was formerly
General Notes (Continued)

generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Snoop Mode set to Home Snoop
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f
running on linux-mj3w Mon Jan 8 14:29:12 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2699A v4 @ 2.40GHz
  2 "physical id"s (chips)
  44 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 22
siblings : 22
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 44
On-line CPU(s) list: 0-43
Thread(s) per core: 1
Core(s) per socket: 22
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 79

(Continued on next page)
## Platform Notes (Continued)

Model name: \(\text{Intel (R) Xeon(R) CPU E5-2699A v4 @ 2.40GHz}\)
Stepping: 1
CPU MHz: 3203.617
CPU max MHz: 3600.0000
CPU min MHz: 1200.0000
BogoMIPS: 4799.75
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 56320K
NUMA node0 CPU(s): 0-21
NUMA node1 CPU(s): 22-43

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pni pkp bts dtherm intel_pt tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3dmov cr4clf cqm rdtscp rtta aperfmperf

From /proc/cpuinfo cache data

```
cache size : 56320 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21
node 0 size: 128240 MB
node 0 free: 127673 MB
node 1 cpus: 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43
node 1 size: 129019 MB
node 1 free: 128660 MB
node distances:
node 0 1
0: 10 21
1: 21 10
```

From /proc/meminfo

```
MemTotal: 263434656 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
```

(Continued on next page)
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**Platform Notes (Continued)**

```
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
  NAME="SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
  Linux linux-mj3w 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 8 14:26

SPEC is set to: /home/cpu2017.1.0.2.ic18.0

Filesystem  Type  Size  Used  Avail  Use% Mounted on
/dev/sda3    xfs   397G   12G   386G   3%  /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[TCE126O-2.20]- 09/07/2016
Memory:
  16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400
  8x NO DIMM Unknown

(End of data from sysinfo program)

**Compiler Version Notes**

---------------------------------------------------------------------
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
---------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---------------------------------------------------------------------

---------------------------------------------------------------------
CC  619.lbm_s(peak)
---------------------------------------------------------------------

(Continued on next page)
Lenovo Global Technology
Lenovo System x3650 M5
(2.40 GHz, Intel Xeon E5-2699A v4)

SPEC CPU2017 Floating Point Speed Result

Lenovo Global Technology
Lenovo System x3650 M5
(2.40 GHz, Intel Xeon E5-2699A v4)

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Test Date:** Jan-2018

**Tested by:** Lenovo Global Technology

**Hardware Availability:** Apr-2016

**Software Availability:** Sep-2017

**SPECspeed2017_fp_base =** 92.7

**SPECspeed2017_fp_peak =** 94.5

---

**Compiler Version Notes (Continued)**

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 607.cactusBSSN_s(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 607.cactusBSSN_s(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CC 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
Lenovo Global Technology
Lenovo System x3650 M5
(2.40 GHz, Intel Xeon E5-2699A v4)

<table>
<thead>
<tr>
<th>SPECspeed2017_fp_base</th>
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<td>SPECspeed2017_fp_peak</td>
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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Compiler Version Notes (Continued)**

---
CC 621.wrf_s (peak) 628.pop2_s (peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

**Base Compiler Invocation**

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

**Base Portability Flags**

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
**SPEC CPU2017 Floating Point Speed Result**

**Lenovo Global Technology**

Lenovo System x3650 M5  
(2.40 GHz, Intel Xeon E5-2699A v4)

| SPECspeed2017_fp_base = 92.7 |
| SPECspeed2017_fp_peak = 94.5 |

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jan-2018  
**Hardware Availability:** Apr-2016  
**Software Availability:** Sep-2017

---

**Base Optimization Flags**

C benchmarks:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-DSPEC_OPENMP`

Fortran benchmarks:
- `-DSPEC_OPENMP`  
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

Benchmarks using both Fortran and C:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

Benchmarks using Fortran, C, and C++:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-qopenmp`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`

---

**Base Other Flags**

C benchmarks:
- `-m64`  
- `-std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64`  
- `-std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64`  
- `-std=c11`

---

**Peak Compiler Invocation**

C benchmarks:
- `icc`

Fortran benchmarks:
- `ifort`

(Continued on next page)
Lenovo Global Technology
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SPECspeed2017_fp_base = 92.7
SPECspeed2017_fp_peak = 94.5

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

ifort icc

Benchmarks using Fortran, C, and C++:

icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP

638.imagick_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

Fortran benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

621.wrf_s: -prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

627.cam4_s: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Lenovo Global Technology

Lenovo System x3650 M5
(2.40 GHz, Intel Xeon E5-2699A v4)

SPECspeed2017_fp_base = 92.7
SPECspeed2017_fp_peak = 94.5

Peak Optimization Flags (Continued)

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nostandard-realloc-lhs
-align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-BDW-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-BDW-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.