# SPEC® CPU2017 Integer Speed Result

## Huawei

Huawei 1288H V5 (Intel Xeon Platinum 8170)

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>9.03</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_int_peak</td>
<td>9.32</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3175</th>
</tr>
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<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Huawei</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jan-2018</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2017</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
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### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>7.38</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>9.54</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>9.78</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>7.29</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>7.55</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>10.2</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>5.16</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>4.31</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>13.5</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>22.7</td>
</tr>
</tbody>
</table>

### SPECspeed2017_int_base (9.03)

### SPECspeed2017_int_peak (9.32)

### Hardware

- **CPU Name:** Intel Xeon Platinum 8170
- **Max MHz.:** 3700
- **Nominal:** 2100
- **Enabled:** 52 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **Cache L2:** 1 MB I+D on chip per core
- **Cache L3:** 35.75 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)
- **Storage:** 1 x 1200 GB SAS, 10000 RPM
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-514.el7.x86_64
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** Yes
- **Firmware:** Version 0.31 Released Sep-2017
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1
Huawei

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<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>605.mcf_s</td>
<td>104</td>
<td>430</td>
<td>11.0</td>
<td>430</td>
<td>11.0</td>
<td>431</td>
<td>11.0</td>
<td>104</td>
<td>428</td>
<td>11.0</td>
<td>430</td>
<td>11.0</td>
<td>429</td>
<td>11.0</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>104</td>
<td>222</td>
<td>7.33</td>
<td>224</td>
<td>7.29</td>
<td>224</td>
<td>7.27</td>
<td>104</td>
<td>220</td>
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<td>216</td>
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<td>7.57</td>
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<td>623.xalanchmk_s</td>
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<td>148</td>
<td>9.59</td>
<td>147</td>
<td>9.61</td>
<td>147</td>
<td>9.63</td>
<td>104</td>
<td>139</td>
<td>10.2</td>
<td>139</td>
<td>10.2</td>
<td>138</td>
<td>10.3</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>104</td>
<td>152</td>
<td>11.6</td>
<td>152</td>
<td>11.6</td>
<td>152</td>
<td>11.6</td>
<td>104</td>
<td>152</td>
<td>11.6</td>
<td>153</td>
<td>11.6</td>
<td>152</td>
<td>11.6</td>
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<tr>
<td>631.deepsjeng_s</td>
<td>104</td>
<td>278</td>
<td>5.16</td>
<td>278</td>
<td>5.16</td>
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<td>5.16</td>
<td>104</td>
<td>278</td>
<td>5.16</td>
<td>278</td>
<td>5.16</td>
<td>278</td>
<td>5.16</td>
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<tr>
<td>641.leela_s</td>
<td>104</td>
<td>396</td>
<td>4.31</td>
<td>396</td>
<td>4.31</td>
<td>396</td>
<td>4.31</td>
<td>104</td>
<td>396</td>
<td>4.31</td>
<td>396</td>
<td>4.31</td>
<td>396</td>
<td>4.31</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>104</td>
<td>218</td>
<td>13.5</td>
<td>219</td>
<td>13.4</td>
<td>218</td>
<td>13.5</td>
<td>104</td>
<td>218</td>
<td>13.5</td>
<td>219</td>
<td>13.4</td>
<td>218</td>
<td>13.5</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>104</td>
<td>272</td>
<td>22.8</td>
<td>272</td>
<td>22.7</td>
<td>274</td>
<td>22.5</td>
<td>104</td>
<td>268</td>
<td>23.1</td>
<td>267</td>
<td>23.2</td>
<td>269</td>
<td>23.0</td>
</tr>
</tbody>
</table>

Specspeed2017_int_base = 9.03
Specspeed2017_int_peak = 9.32

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/spec2017/lib/ia32;/spec2017/lib/intel64;/spec2017/je5.0.1-32;/spec2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

jemalloc: configured and built at default for 32bit (x86) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)
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**Huawei**  
Huawei 1288H V5 (Intel Xeon Platinum 8170)  

<table>
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**CPU2017 License:** 3175  
**Test Date:** Jan-2018  
**Test Sponsor:** Huawei  
**Hardware Availability:** Jul-2017  
**Tested by:** Huawei  
**Software Availability:** Sep-2017

### General Notes (Continued)

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, [http://www.spec.org/osg/policy.html](http://www.spec.org/osg/policy.html)

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

### Platform Notes

BIOS configuration:  
Power Efficiency Mode Set to Custom  
Hyper-Threading Set to Disable  
Sysinfo program /spec2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bccc091c0f  
running on localhost.localdomain Tue Jan 9 18:50:21 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

From /proc/cpuinfo  
- model name: Intel(R) Xeon(R) Platinum 8170 CPU @ 2.10GHz  
- 2 "physical id"s (chips)  
- 52 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
- cpu cores: 26  
- siblings: 26  
- physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29  
- physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29

From lscpu:  
- Architecture: x86_64  
- CPU op-mode(s): 32-bit, 64-bit

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Test Date: Jan-2018
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Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Byte Order:</th>
<th>Little Endian</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU(s):</td>
<td>52</td>
</tr>
<tr>
<td>On-line CPU(s) list:</td>
<td>0-51</td>
</tr>
<tr>
<td>Thread(s) per core:</td>
<td>1</td>
</tr>
<tr>
<td>Core(s) per socket:</td>
<td>26</td>
</tr>
<tr>
<td>Socket(s):</td>
<td>2</td>
</tr>
<tr>
<td>NUMA node(s):</td>
<td>2</td>
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<tr>
<td>Vendor ID:</td>
<td>GenuineIntel</td>
</tr>
<tr>
<td>CPU family:</td>
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<tr>
<td>Model:</td>
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<tr>
<td>Model name:</td>
<td>Intel(R) Xeon(R) Platinum 8170 CPU @ 2.10GHz</td>
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<td>L1d cache:</td>
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<td>L1i cache:</td>
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<tr>
<td>L2 cache:</td>
<td>1024K</td>
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<tr>
<td>L3 cache:</td>
<td>36608K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s):</td>
<td>0-25</td>
</tr>
<tr>
<td>NUMA node1 CPU(s):</td>
<td>26-51</td>
</tr>
</tbody>
</table>

/proc/cpuinfo cache data

| cache size: | 36608 KB |

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

| available: | 2 nodes (0-1) |
| node 0 cpus: | 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 |
| node 0 size: | 194709 MB |
| node 0 free: | 189846 MB |
| node 1 cpus: | 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 |
| node 1 size: | 196608 MB |
| node 1 free: | 191624 MB |
| node distances: |
| node 0 | 1 |
| 0: | 10 21 |
| 1: | 21 10 |

From /proc/meminfo

| MemTotal: | 394145208 kB |
| HugePages_Total: | 0 |
| Hugepagesize: | 2048 kB |

From /etc/*release* /etc/*version*

os-release:

(Continued on next page)
Huawei

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SPECspeed2017_int_base = 9.03
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CPU2017 License: 3175
Test Sponsor: Huawei
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Platform Notes (Continued)

NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 9 18:50

SPEC is set to: /spec2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 781G 229G 553G 30% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.31 09/29/2017
Memory:
24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base)
==============================================================================
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
CC 600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak)
==============================================================================
icc (ICC) 18.0.0 20170811

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Compiler Version Notes (Continued)

Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak) 641.leela_s(peak)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base, peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64

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Base Portability Flags (Continued)

623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leea_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
- Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
- Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
- Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
- m64 -std=c11

C++ benchmarks:
- m64

Fortran benchmarks:
- m64

Peak Compiler Invocation

C benchmarks:
icc

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Peak Compiler Invocation (continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-mem-layout-trans=3 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

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### Peak Optimization Flags (Continued)

- 657.xz_s: Same as 602.gcc_s

#### C++ benchmarks:

620.omnetpp_s: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`  
`-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=3`  
`-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP`  
`-L/usr/local/je5.0.1-64/lib -ljemalloc`

623.xalancbmk_s: `-L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32`  
`-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`  
`-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=3`  
`-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP`  
`-L/usr/local/je5.0.1-32/lib -ljemalloc`

631.deepsjeng_s: `basepeak = yes`

641.leela_s: `basepeak = yes`

#### Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

### Peak Other Flags

#### C benchmarks:

- `-m64 -std=c11`

#### C++ benchmarks (except as noted below):

- `-m64`

623.xalancbmk_s: `-m32`

#### Fortran benchmarks:

- `-m64`

The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)
<table>
<thead>
<tr>
<th>Huawei 1288H V5 (Intel Xeon Platinum 8170)</th>
<th>Huawei</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_int_base = 9.03</td>
<td></td>
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<tr>
<td>SPECspeed2017_int_peak = 9.32</td>
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</tbody>
</table>

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  

**Hardware Availability:** Jul-2017  
**Software Availability:** Sep-2017  
**Test Date:** Jan-2018

You can also download the XML flags sources by saving the following links:

- http://www.spec.org/cpu2017/flags/Intel-iocl8.0-official-linux64.xml
- http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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