# SPEC® CPU2017 Floating Point Rate Result

## Lenovo Global Technology

ThinkSystem SR630  
(1.70 GHz, Intel Xeon Bronze 3104)  

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>45.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>45.9</td>
</tr>
</tbody>
</table>

### CPU2017 License
9017

### Test Sponsor
Lenovo Global Technology

### Tested by
Lenovo Global Technology

### Hardware

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Bronze 3104</td>
</tr>
<tr>
<td>Max MHz.</td>
<td>1700</td>
</tr>
<tr>
<td>Nominal</td>
<td>1700</td>
</tr>
<tr>
<td>Enabled</td>
<td>12 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>8.25 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 800 GB SAS SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>SUSE Linux Enterprise Server 12 SP2 (x86_64)</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Lenovo BIOS Version IVE113K 1.10 released Sep-2017</td>
</tr>
<tr>
<td>File System</td>
<td>btrfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Test Date
Jan-2018

### Hardware Availability
Aug-2017

### Software Availability
Sep-2017

## SPECrate2017_fp_base

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>224</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>224</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>210</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>200</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>190</td>
</tr>
<tr>
<td>519.hmmer_r</td>
<td>180</td>
</tr>
<tr>
<td>520.snake_r</td>
<td>170</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>160</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>150</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>140</td>
</tr>
<tr>
<td>528.cactus_r</td>
<td>130</td>
</tr>
<tr>
<td>529.chemmd_r</td>
<td>120</td>
</tr>
<tr>
<td>530.fio_r</td>
<td>110</td>
</tr>
<tr>
<td>531.libquantum_r</td>
<td>100</td>
</tr>
<tr>
<td>532.mcf_r</td>
<td>90</td>
</tr>
<tr>
<td>533.rr_r</td>
<td>80</td>
</tr>
<tr>
<td>534.h264rnr_r</td>
<td>70</td>
</tr>
<tr>
<td>535.omnetpp_r</td>
<td>60</td>
</tr>
<tr>
<td>536.ortools_r</td>
<td>50</td>
</tr>
<tr>
<td>537.gobmk_r</td>
<td>40</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>30</td>
</tr>
<tr>
<td>540.lbm_r</td>
<td>20</td>
</tr>
<tr>
<td>541.lbm_r2</td>
<td>10</td>
</tr>
<tr>
<td>542.sopleap_r</td>
<td>0</td>
</tr>
</tbody>
</table>

## SPECrate2017_fp_peak

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>224</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>224</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>210</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>200</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>190</td>
</tr>
<tr>
<td>519.hmmer_r</td>
<td>180</td>
</tr>
<tr>
<td>520.snake_r</td>
<td>170</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>160</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>150</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>140</td>
</tr>
<tr>
<td>528.cactus_r</td>
<td>130</td>
</tr>
<tr>
<td>529.chemmd_r</td>
<td>120</td>
</tr>
<tr>
<td>530.fio_r</td>
<td>110</td>
</tr>
<tr>
<td>531.libquantum_r</td>
<td>100</td>
</tr>
<tr>
<td>532.mcf_r</td>
<td>90</td>
</tr>
<tr>
<td>533.rr_r</td>
<td>80</td>
</tr>
<tr>
<td>534.h264rnr_r</td>
<td>70</td>
</tr>
<tr>
<td>535.omnetpp_r</td>
<td>60</td>
</tr>
<tr>
<td>536.ortools_r</td>
<td>50</td>
</tr>
<tr>
<td>537.gobmk_r</td>
<td>40</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>30</td>
</tr>
<tr>
<td>540.lbm_r</td>
<td>20</td>
</tr>
<tr>
<td>541.lbm_r2</td>
<td>10</td>
</tr>
<tr>
<td>542.sopleap_r</td>
<td>0</td>
</tr>
</tbody>
</table>

---

Copyright 2017-2018 Standard Performance Evaluation Corporation

Standard Performance Evaluation Corporation (info@spec.org) - https://www.spec.org/
## Lenovo Global Technology

**ThinkSystem SR630**
(1.70 GHz, Intel Xeon Bronze 3104)

---

### CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>12</td>
<td>537</td>
<td>224</td>
<td>536</td>
<td>224</td>
<td>539</td>
<td>223</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>12</td>
<td>439</td>
<td>34.6</td>
<td>439</td>
<td>34.6</td>
<td>439</td>
<td>34.6</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>12</td>
<td>434</td>
<td>26.3</td>
<td>435</td>
<td>26.2</td>
<td>429</td>
<td>26.6</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>12</td>
<td>945</td>
<td>33.2</td>
<td>942</td>
<td>33.3</td>
<td>944</td>
<td>33.2</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>12</td>
<td>653</td>
<td>42.9</td>
<td>654</td>
<td>42.9</td>
<td>653</td>
<td>42.9</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>12</td>
<td>217</td>
<td>58.3</td>
<td>217</td>
<td>58.3</td>
<td>217</td>
<td>58.4</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>12</td>
<td>639</td>
<td>42.0</td>
<td>635</td>
<td>42.3</td>
<td>638</td>
<td>42.1</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>12</td>
<td>534</td>
<td>34.2</td>
<td>535</td>
<td>34.2</td>
<td>536</td>
<td>34.1</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>12</td>
<td>701</td>
<td>29.9</td>
<td>699</td>
<td>30.0</td>
<td>700</td>
<td>30.0</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>12</td>
<td>527</td>
<td>56.7</td>
<td>525</td>
<td>56.9</td>
<td>531</td>
<td>56.2</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>12</td>
<td>487</td>
<td>41.4</td>
<td>488</td>
<td>41.4</td>
<td>487</td>
<td>41.5</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>12</td>
<td>790</td>
<td>59.2</td>
<td>791</td>
<td>59.1</td>
<td>790</td>
<td>59.2</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>12</td>
<td>542</td>
<td>35.2</td>
<td>538</td>
<td>35.4</td>
<td>537</td>
<td>35.5</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>12</td>
<td>537</td>
<td>224</td>
<td>536</td>
<td>224</td>
<td>539</td>
<td>223</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>12</td>
<td>439</td>
<td>34.6</td>
<td>439</td>
<td>34.6</td>
<td>439</td>
<td>34.6</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>12</td>
<td>434</td>
<td>26.3</td>
<td>435</td>
<td>26.2</td>
<td>429</td>
<td>26.6</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>12</td>
<td>945</td>
<td>33.2</td>
<td>942</td>
<td>33.3</td>
<td>944</td>
<td>33.2</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>12</td>
<td>653</td>
<td>42.9</td>
<td>654</td>
<td>42.9</td>
<td>653</td>
<td>42.9</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>12</td>
<td>217</td>
<td>58.3</td>
<td>217</td>
<td>58.3</td>
<td>217</td>
<td>58.4</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>12</td>
<td>639</td>
<td>42.0</td>
<td>635</td>
<td>42.3</td>
<td>638</td>
<td>42.1</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>12</td>
<td>534</td>
<td>34.2</td>
<td>535</td>
<td>34.2</td>
<td>536</td>
<td>34.1</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>12</td>
<td>701</td>
<td>29.9</td>
<td>699</td>
<td>30.0</td>
<td>700</td>
<td>30.0</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>12</td>
<td>527</td>
<td>56.7</td>
<td>525</td>
<td>56.9</td>
<td>531</td>
<td>56.2</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>12</td>
<td>487</td>
<td>41.4</td>
<td>488</td>
<td>41.4</td>
<td>487</td>
<td>41.5</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>12</td>
<td>790</td>
<td>59.2</td>
<td>791</td>
<td>59.1</td>
<td>790</td>
<td>59.2</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>12</td>
<td>542</td>
<td>35.2</td>
<td>538</td>
<td>35.4</td>
<td>537</td>
<td>35.5</td>
</tr>
</tbody>
</table>

---

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### General Notes

Environment variables set by runcpu before the start of the run:

- LD_LIBRARY_PATH = "
  /home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
- LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

Memory using Redhat Enterprise Linux 7.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation:

- Filesystem page cache synced and cleared with:
  - sync; echo 3 > /proc/sys/vm/drop_caches

- runcpu command invoked through numactl i.e.:
  - numactl --interleave=all runcpu <etc>

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(1.70 GHz, Intel Xeon Bronze 3104)

SPEC CPU2017 Floating Point Rate Result

SPECraten=45.9
SPECraten=45.5

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

General Notes (Continued)
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to Enable
Hardware Prefetcher set to Disable
MONITORM/WAIT set to Enable
Execute Disable Bit set to Disable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC Deadline Alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on Cable-SPECcpu2006-SUSE12SP2 Tue Jan 9 11:40:13 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
2 "physical id"'s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(1.70 GHz, Intel Xeon Bronze 3104)

SPECrate2017_fp_base = 45.5
SPECrate2017_fp_peak = 45.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1696.019
BogoMIPS: 3392.03
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-5
NUMA node1 CPU(s): 6-11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch arat epb pni pts dtherm intel_pt
tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
erm sinvclcid rtm cqm mpx avx512f avx512d avx512cd avx512bw avx512v l xsavexp
xsaveopt xsavec xGETBVT1 cqm_llc cqm_occup_llc

/proc/cpuinfo cache data
  cache size: 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5
  node 0 size: 193110 MB
  node 0 free: 191189 MB
  node 1 cpus: 6 7 8 9 10 11

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(1.70 GHz, Intel Xeon Bronze 3104)

SPECrate2017_fp_base = 45.5
SPECrate2017_fp_peak = 45.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Platform Notes (Continued)

node 1 size: 193504 MB
node 1 free: 192407 MB
node distances:
node 0 1
 0: 10 21
 1: 21 10

From /proc/meminfo
MemTotal: 395893876 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.

os-release:
  NAME= "SLES"
  VERSION="12-SP2"
  VERSION_ID="12.2"
  PRETTY_NAME= "SUSE Linux Enterprise Server 12 SP2"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux Cable-SPECcpu2006-SUSE12SP2 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 9 02:27

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 btrfs 744G 191G 552G 26% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Lenovo -[IVE113K-1.10]- 09/06/2017
  Memory:
    24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2133

(End of data from sysinfo program)
Lenovo Global Technology
ThinkSystem SR630
(1.70 GHz, Intel Xeon Bronze 3104)

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECrate2017_fp_base = 45.5
SPECrate2017_fp_peak = 45.9

CPUTest License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

Compiler Version Notes
==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  519.lbm_r(peak) 544.nab_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(peak) 510.parest_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC  511.povray_r(peak) 526.blender_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630
(1.70 GHz, Intel Xeon Bronze 3104)

SPECrate2017_fp_base = 45.5
SPECrate2017_fp_peak = 45.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Compiler Version Notes (Continued)

FC 507.cactuBSSN_r(base)

------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

FC 507.cactuBSSN_r(peak)

------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC 554.roms_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC 521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC 521.wrf_r(peak) 527.cam4_r(peak)
------------------------------------------------------------------------------
(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SR630**
(1.70 GHz, Intel Xeon Bronze 3104)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>45.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>45.9</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by</td>
<td>Lenovo Global Technology</td>
</tr>
</tbody>
</table>

**Test Date:** Jan-2018  
**Hardware Availability:** Aug-2017  
**Software Availability:** Sep-2017

### Compiler Version Notes (Continued)

- **ifort (IFORT) 18.0.0 20170811**
  - Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
- **icc (ICC) 18.0.0 20170811**
  - Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

### Base Compiler Invocation

**C benchmarks:**
- `icc`

**C++ benchmarks:**
- `icpc`

**Fortran benchmarks:**
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using both C and C++:
- `icpc icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

### Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64
- 507.cactuBSSN_r: -DSPEC_LP64
- 508.namd_r: -DSPEC_LP64
- 510.parest_r: -DSPEC_LP64
- 511.povray_r: -DSPEC_LP64
- 519.lbm_r: -DSPEC_LP64
- 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64
### Base Optimization Flags

C benchmarks:
- `-xCORE-AVX2`  
- `-ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-mem-layout-trans=3`

C++ benchmarks:
- `-xCORE-AVX2`  
- `-ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-mem-layout-trans=3`

Fortran benchmarks:
- `-xCORE-AVX2`  
- `-ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:
- `-xCORE-AVX2`  
- `-ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-nostandard-realloc-lhs -align array32byte`

Benchmarks using both C and C++:
- `-xCORE-AVX2`  
- `-ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-mem-layout-trans=3`

Benchmarks using Fortran, C, and C++:
- `-xCORE-AVX2`  
- `-ipo -03 -no-prec-div -qopt-prefetch -ffinite-math-only`  
- `-qopt-mem-layout-trans=3`  
- `-nostandard-realloc-lhs -align array32byte`

### Base Other Flags

C benchmarks:
- `-m64 -std=c11`

C++ benchmarks:
- `-m64`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64 -std=c11`

Benchmarks using both C and C++:
- `-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11`
Lenovo Global Technology
ThinkSystem SR630
(1.70 GHz, Intel Xeon Bronze 3104)

SPECrate2017_fp_base = 45.5
SPECrate2017_fp_peak = 45.9

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2018
Hardware Availability: Aug-2017
Software Availability: Sep-2017

**Peak Compiler Invocation**

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

(Continued on next page)
Peak Optimization Flags (Continued)

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
- prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
- prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
- prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11
## Lenovo Global Technology

**ThinkSystem SR630**  
(1.70 GHz, Intel Xeon Bronze 3104)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>45.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>45.9</td>
</tr>
</tbody>
</table>

**CPU2017 License**: 9017  
**Test Sponsor**: Lenovo Global Technology  
**Tested by**: Lenovo Global Technology

<table>
<thead>
<tr>
<th>Test Date</th>
<th>Jan-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability</td>
<td>Aug-2017</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

**Peak Other Flags (Continued)**

Benchmarks using Fortran, C, and C++:

- `-m64`  
- `-std=c11`

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)  

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)  

---

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-01-08 22:40:12-0500.  
Originally published on 2018-03-06.