## SPEC® CPU2017 Floating Point Speed Result

### Dell Inc.

**PowerEdge FC640 (Intel Xeon Gold 6150, 2.70 GHz)**

<table>
<thead>
<tr>
<th>Test Sponsor: Dell Inc.</th>
<th>CPU2017 License: 55</th>
<th>Test Date: Nov-2017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Hardware Availability: Sep-2017</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

**SPECspeed2017_fp_base** = 101

**SPECspeed2017_fp_peak** = Not Run

### Hardware

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed2017_fp_base (101)</th>
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</thead>
<tbody>
<tr>
<td>603.bwaves_s 72</td>
<td>327</td>
</tr>
<tr>
<td>607.cactuBSSN_s 72</td>
<td>72</td>
</tr>
<tr>
<td>619.lbm_s 72</td>
<td>139</td>
</tr>
<tr>
<td>621.wrf_s 72</td>
<td>139</td>
</tr>
<tr>
<td>627.cam4_s 72</td>
<td>139</td>
</tr>
<tr>
<td>628.pop2_s 72</td>
<td>139</td>
</tr>
<tr>
<td>638.imagick_s 72</td>
<td>139</td>
</tr>
<tr>
<td>644.nab_s 72</td>
<td>139</td>
</tr>
<tr>
<td>649.fotonik3d_s 72</td>
<td>139</td>
</tr>
<tr>
<td>654.roms_s 72</td>
<td>139</td>
</tr>
</tbody>
</table>

#### SPECspeed2017_fp_base = 101

### Software

**OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.16-56-default

**Compiler:**
- C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
- Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux

**Parallel:** Yes

**Firmware:** Version 1.0.0 released Aug-2017

**System State:** Run level 3 (multi-user)

**File System:** btrfs

**Base Pointers:** 64-bit

**Peak Pointers:** 64-bit

**Other:** None
SPECCPU2017 Floating Point Speed Result

Dell Inc.
PowerEdge FC640 (Intel Xeon Gold 6150, 2.70 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
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Results Table

<table>
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<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
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<td>327</td>
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<tr>
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<td>167</td>
<td>94.0</td>
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</tr>
</tbody>
</table>

SPECspeed2017_fp_base = 101
SPECspeed2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
### SPEC CPU2017 Floating Point Speed Result

**Dell Inc.**

PowerEdge FC640 (Intel Xeon Gold 6150, 2.70 GHz)

| SPECspeed2017_fp_base = | 101 |
| SPECspeed2017_fp_peak = | Not Run |

| CPU2017 License: | 55 |
| Test Sponsor: | Dell Inc. |
| Tested by: | Dell Inc. |
| Test Date: | Nov-2017 |
| Hardware Availability: | Sep-2017 |
| Software Availability: | Sep-2017 |

**General Notes (Continued)**

generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

**Platform Notes**

BIOS settings:
Virtualization Technology disabled
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost enabled
C States disabled
Memory Patrol Scrub disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bdc091c0f
running on linux-u8yg Thu Nov 2 07:18:40 2017

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6150 CPU @ 2.70GHz
  2 "physical id"s (chips)
  72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 18
siblings : 36
physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27

From lscpu:
Architecture:        x86_64
CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              72
On-line CPU(s) list: 0-71
Thread(s) per core:  2

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

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Dell Inc.

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Software Availability: Sep-2017

Platform Notes (Continued)

Core(s) per socket: 18
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6150 CPU @ 2.70GHz
Stepping: 4
CPU MHz: 2700.122
BogoMIPS: 5400.24
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s):
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58
,60,62,64,66,68,70
NUMA node1 CPU(s):
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55,57,59
,61,63,65,67,69,71
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsdp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtss64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb pln pts dtherm intel_pt
tpr_shadow flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
ermis invpcid rtm cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occum llc

/proc/cpuinfo cache data
  cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50
  52 54 56 58 60 62 64 66 68 70
  node 0 size: 80221 MB
  node 0 free: 77799 MB
  node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51
  53 55 57 59 61 63 65 67 69 71
  node 1 size: 96736 MB
  node 1 free: 93136 MB
  node distances:
    node 0 1

(Continued on next page)
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Platform Notes (Continued)

0: 10 21
1: 21 10

From /proc/meminfo
MemTotal: 181204768 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 2
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP2"
    VERSION_ID="12.2"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-u8yg 4.4.16-56-default #1 SMP Mon Aug 8 14:24:26 UTC 2016 (5b281a8) x86_64
x86_64 x86_64 GNU/Linux

run-level 3 Nov 1 05:02

SPEC is set to: /root/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda1 btrfs 921G 37G 884G 5% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Dell Inc. 1.0.0 08/10/2017
Memory:
  11x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
  5x Not Specified Not Specified

(End of data from sysinfo program)
Dell Inc. PowerEdge FC640 (Intel Xeon Gold 6150, 2.70 GHz)

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Tested by: Dell Inc.  
Software Availability: Sep-2017

Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
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ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
CC  621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

(Continued on next page)
Dell Inc. PowerEdge FC640 (Intel Xeon Gold 6150, 2.70 GHz)

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**SPECspeed2017_fp_base = 101**
**SPECspeed2017_fp_peak = Not Run**

**Base Compiler Invocation (Continued)**

Benchmarks using Fortran, C, and C++:

- icpc
- ic
- ifort

**Base Portability Flags**

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:

- -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:

- -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
- -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:

- -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- -nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:

- -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
- -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
- -nostandard-realloc-lhs -align array32byte
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<tr>
<td>SPECspeed2017_fp_base</td>
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**Software Availability:** Sep-2017

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### Base Other Flags

C benchmarks:
- `-m64 -std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11`

---

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

- [Dell-Platform-Flags-PowerEdge14G-revD.xml](http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revD.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-11-02 08:18:39-0400.


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